

$PCInit \triangleq$ The initial predicate.
 $\wedge rmState = [r \in RM \mapsto \text{"working"}]$
 $\wedge aState = [r \in RM \mapsto$
 $\quad [ac \in Acceptor$
 $\quad \mapsto [mbal \mapsto 0, bal \mapsto -1, val \mapsto \text{"none"}]]]$
 $\wedge msgs = \{\}$
 $Send(m) \triangleq msgs' = msgs \cup \{m\}$
 $RMPprepare(r) \triangleq$
 $\wedge rmState[r] = \text{"working"}$
 $\wedge rmState' = [rmState \text{ EXCEPT } ![r] = \text{"prepared"}]$
 $\wedge Send([type \mapsto \text{"phase2a"}, ins \mapsto r, bal \mapsto 0, val \mapsto \text{"prepared"}])$
 $\wedge \text{UNCHANGED } aState$
 $RMChooseToAbort(r) \triangleq$
 $\wedge rmState[r] = \text{"working"}$
 $\wedge rmState' = [rmState \text{ EXCEPT } ![r] = \text{"aborted"}]$
 $\wedge Send([type \mapsto \text{"phase2a"}, ins \mapsto r, bal \mapsto 0, val \mapsto \text{"aborted"}])$
 $\wedge \text{UNCHANGED } aState$
 $RMRCvCommitMsg(r) \triangleq$
 $\wedge [type \mapsto \text{"Commit"}] \in msgs$
 $\wedge rmState' = [rmState \text{ EXCEPT } ![r] = \text{"committed"}]$
 $\wedge \text{UNCHANGED } \langle aState, msgs \rangle$
 $RMRCvAbortMsg(r) \triangleq$
 $\wedge [type \mapsto \text{"Abort"}] \in msgs$
 $\wedge rmState' = [rmState \text{ EXCEPT } ![r] = \text{"aborted"}]$
 $\wedge \text{UNCHANGED } \langle aState, msgs \rangle$
 $Phase1a(bal, r) \triangleq$
 $\wedge Send([type \mapsto \text{"phase1a"}, ins \mapsto r, bal \mapsto bal])$
 $\wedge \text{UNCHANGED } \langle rmState, aState \rangle$
 $Phase2a(bal, r) \triangleq$
 $\wedge \neg \exists m \in msgs : \wedge m.type = \text{"phase2a"}$
 $\quad \wedge m.bal = bal$
 $\quad \wedge m.ins = r$
 $\wedge \exists MS \in Majority :$
 $\quad \text{LET } mset \triangleq \{m \in msgs : \wedge m.type = \text{"phase1b"}$
 $\quad \quad \wedge m.ins = r$
 $\quad \quad \wedge m.mbal = bal$
 $\quad \quad \wedge m.acc \in MS\}$
 $\quad maxbal \triangleq \text{Maximum}(\{m.bal : m \in mset\})$
 $\quad val \triangleq \text{IF } maxbal = -1$
 $\quad \quad \text{THEN "aborted"}$

$$\begin{aligned}
& \vee \exists \textit{bal} \in \textit{Ballot} \setminus \{0\}, r \in \textit{RM} : \textit{Phase1a}(\textit{bal}, r) \vee \textit{Phase2a}(\textit{bal}, r) \\
& \vee \textit{PCDecide} \\
& \vee \exists \textit{acc} \in \textit{Acceptor} : \textit{Phase1b}(\textit{acc}) \vee \textit{Phase2b}(\textit{acc})
\end{aligned}$$

$$\textit{PCSpec} \triangleq \textit{PCInit} \wedge \Box[\textit{PCNext}]_{\langle \textit{rmState}, \textit{aState}, \textit{msgs} \rangle}$$

THEOREM $\textit{PCSpec} \Rightarrow \Box \textit{PCTypeOK}$

INSTANCE $\textit{TCommit}$

THEOREM $\textit{PCSpec} \Rightarrow \textit{TCSpec}$
