



ANEXO 1 – TABELA 1.

Ciclo	Reset	PC	Instrução memfile. Hexa	Instrução (Montagem / Máquina)	SrcA	SrcB	Branch	AluResult	Flags3:0	CondEx	WriteData	MemWrite	ReadData
1	0	0x0	E04F000F	sub r0, pc, pc	0x8	0x8	0	0x0	0000	1	0x8	0	xxxx
2	0	0x4	E2802005	add r2, r0, #5	0x0	0x5	0	0x5	0000	1	xxxx	0	xxxx
3	0	0x8	E280300C	add r3, r0, #0xc	0x0	0xc	0	0xc	0000	1	xxxx	0	xxxx
4	0	0xc	E2437009	sub r7, r3, #9	0xc	0x9	0	0x3	0000	1	xxxxx	0	xxxx
5	0	0x10	E1874002	orr r4, r7, r2	0x3	0x5	0	0x7	0000	1	0x5	0	xxxx
6	0	0x14	E0035004	and r5, r3, r4	0xc	0x7	0	0x4	0000	1	0x7	0	xxxx
7	0	0x18	E0855004	add r5, r5, r4	0x4	0x7	0	0xb	0000	1	0x7	0	xxxx
8	0	0x1c	E0558007	subs r8, r5, r7	0xb	0x3	0	0x8	0000	1	0x3	0	xxxx
9	0	0x20	0A00000C	beq #0x58	0x28	0x30	1	0x58	0010	0	xxxx	0	xxxx
10	0	0x24	E0538004	subs r8, r3, r4	0xc	0x7	0	0x5	0010	1	0x7	0	xxxx
11	0	0x28	AA000000	bge #0x30	0x30	0x0	1	0x30	0010	1	xxxx	0	xxxx
12	0	0x30	E0578002	subs r8, r7, r2	0x3	0x5	0	0xffffffe	0010	1	0x5	0	xxxx
13	0	0x34	B2857001	addlt r7, r5, #1	0xb	0x1	0	0xc	1000	1	xxxx	0	xxxx
14	0	0x38	E0477002	sub r7, r7, r2	0xc	0x5	0	0x7	1000	1	0x5	0	xxxx
15	0	0x3c	E5837054	str r7, [r3, #0x54]	0xc	0x54	0	0x60	1000	1	0x7	1	xxxx
16	0	0x40	E5902060	ldr r2, [r0, #0x60]	0x0	0x60	0	0x60	1000	1	0x0	0	xxxx
17	0	0x44	E08FF000	add pc, pc, r0	0x4c	0x0	0	0x4c	1000	1	0x0	0	xxxx
18	0	0x4c	EA000001	b #0x58	0x54	0x4	1	0x58	1000	1	xxxx	0	xxxx
19	0	0x58	E5802064	str r2, [r0, #0x64]	0x00	0x64	0	0x64	1000	1	0x7	1	xxxx