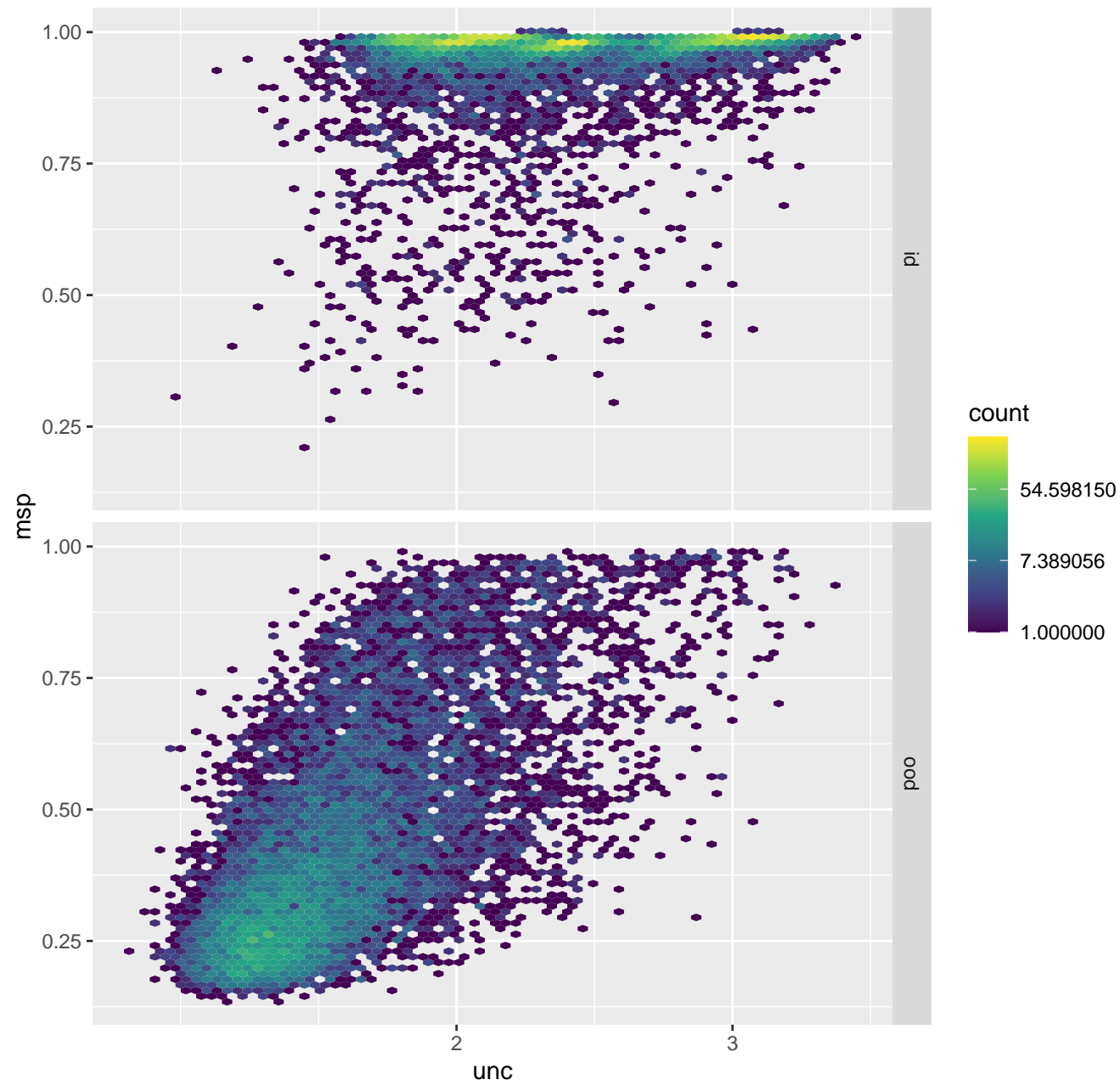
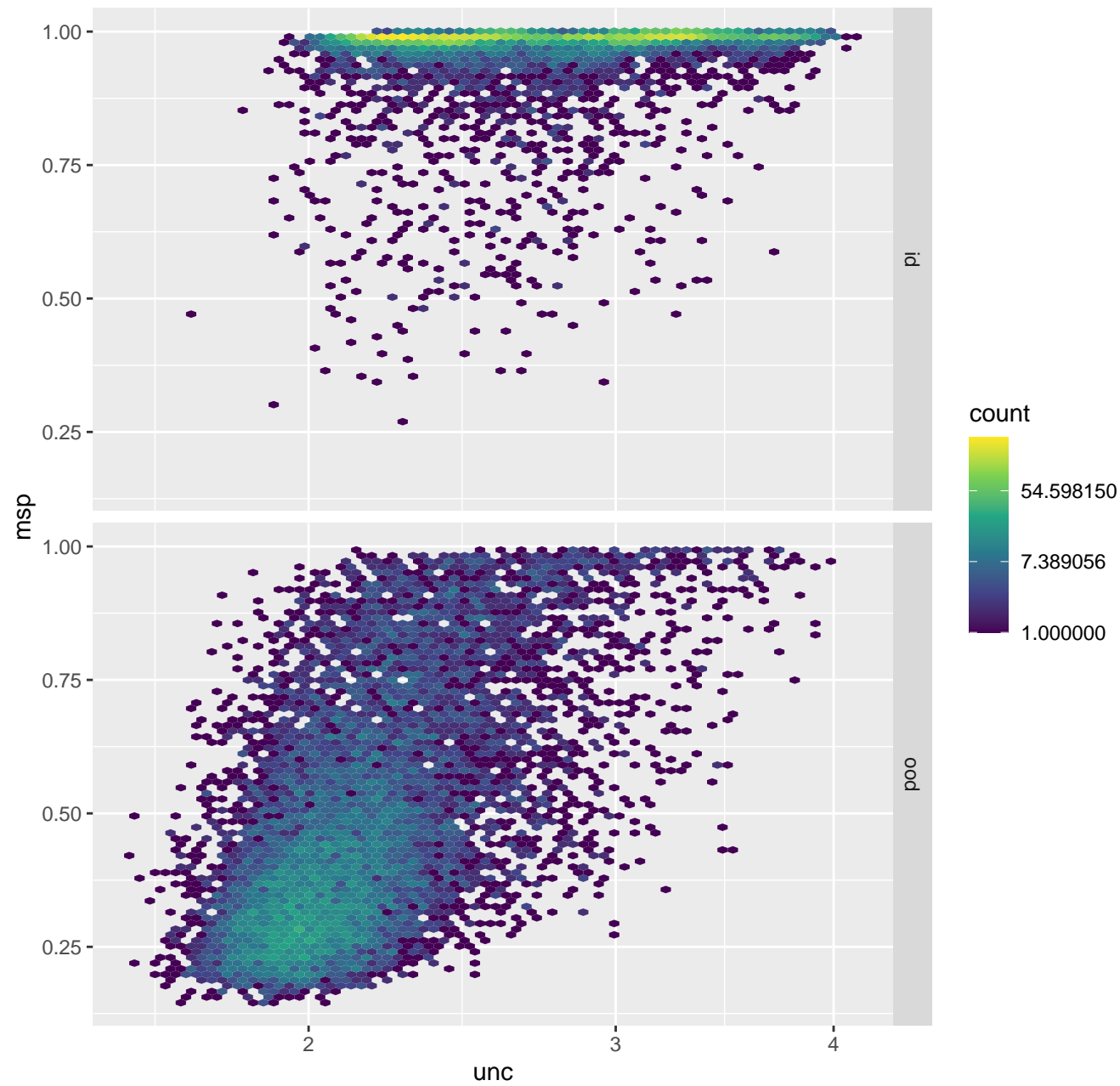


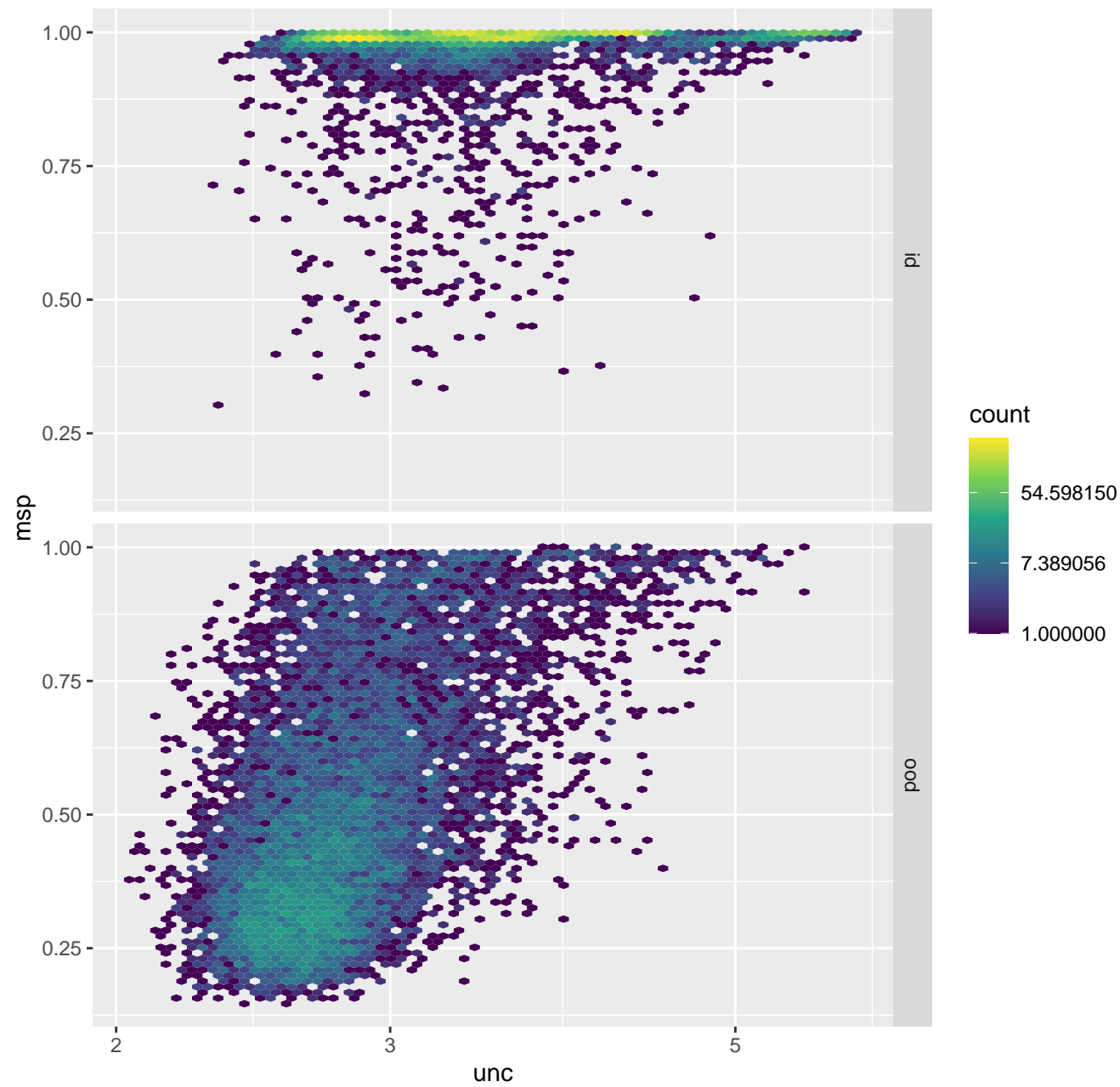
B16+M_B16



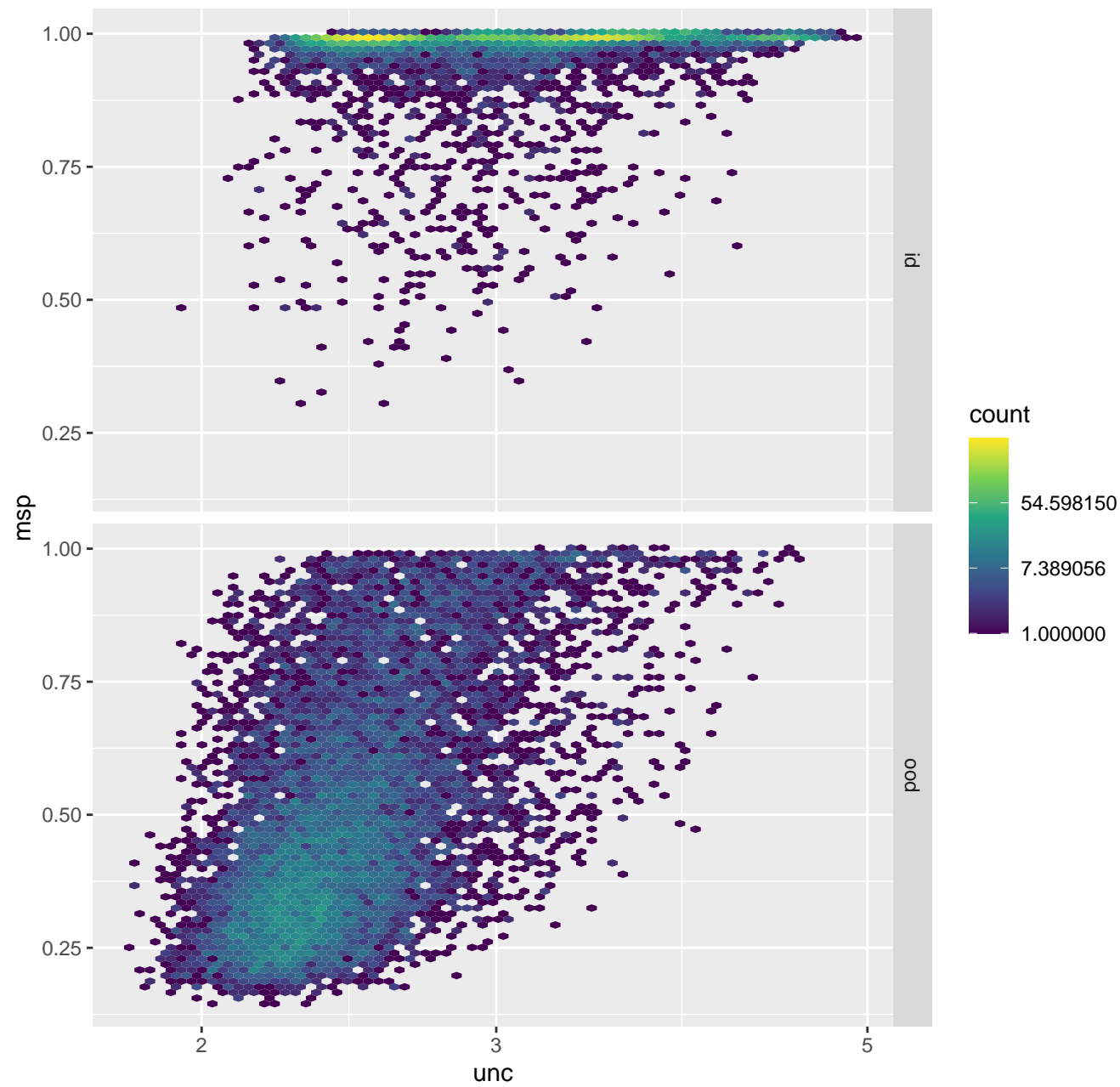
B16+M_B16+R101x3



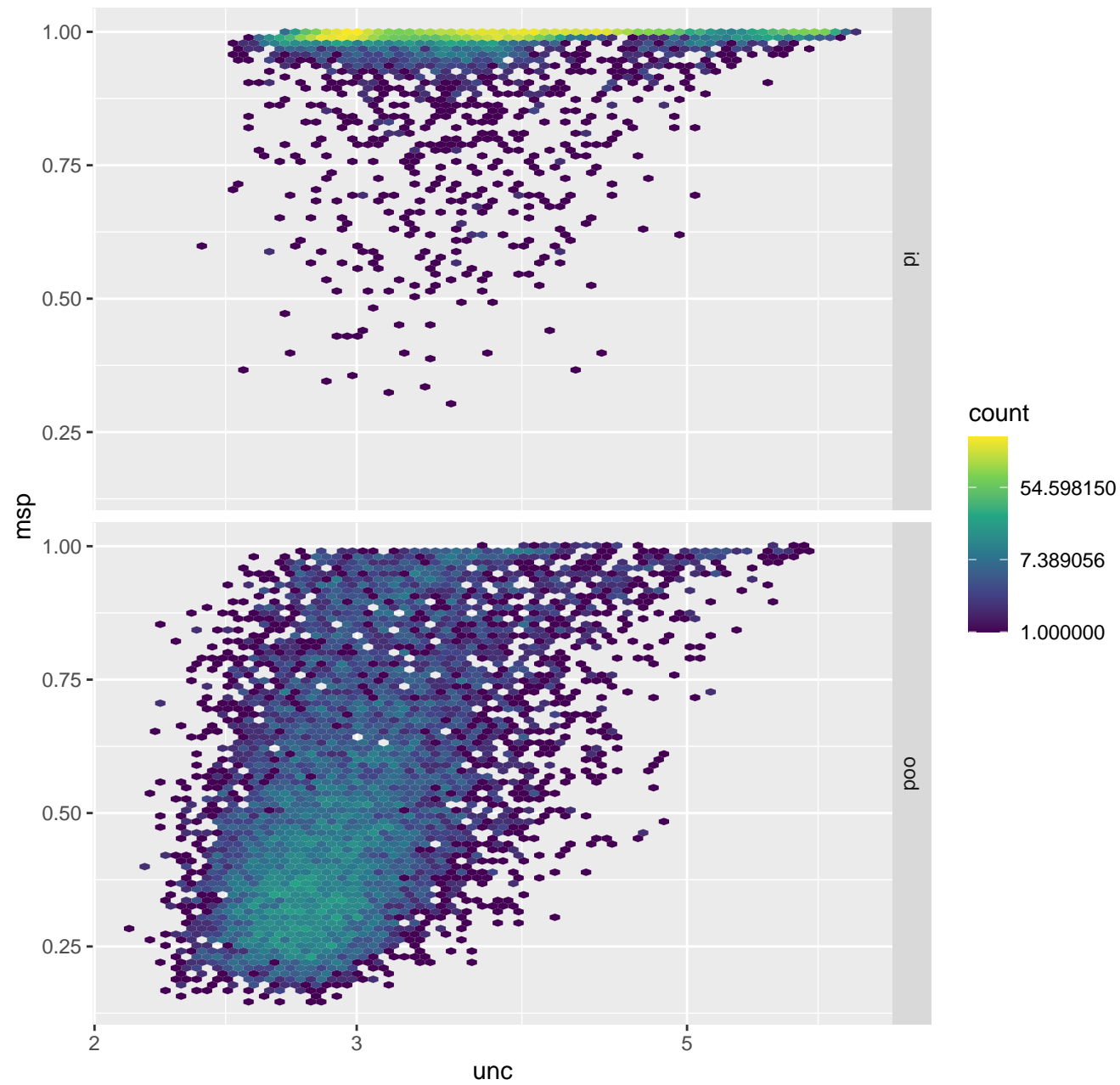
B16+M_B16+R101x3+R50_B16



B16+M_B16+R101x3+R50x1

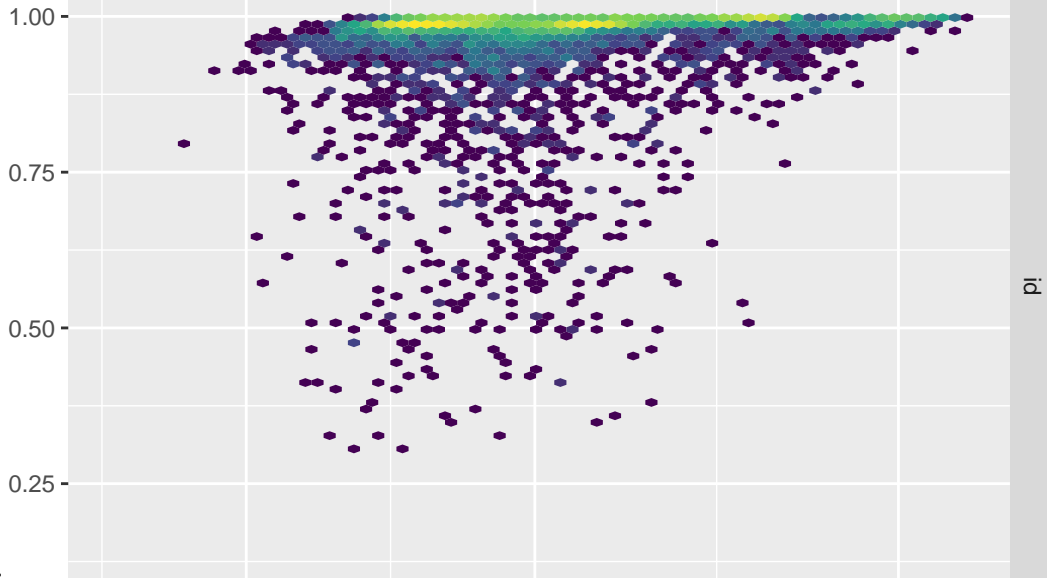


B16+M_B16+R101x3+R50x1+R50_B16

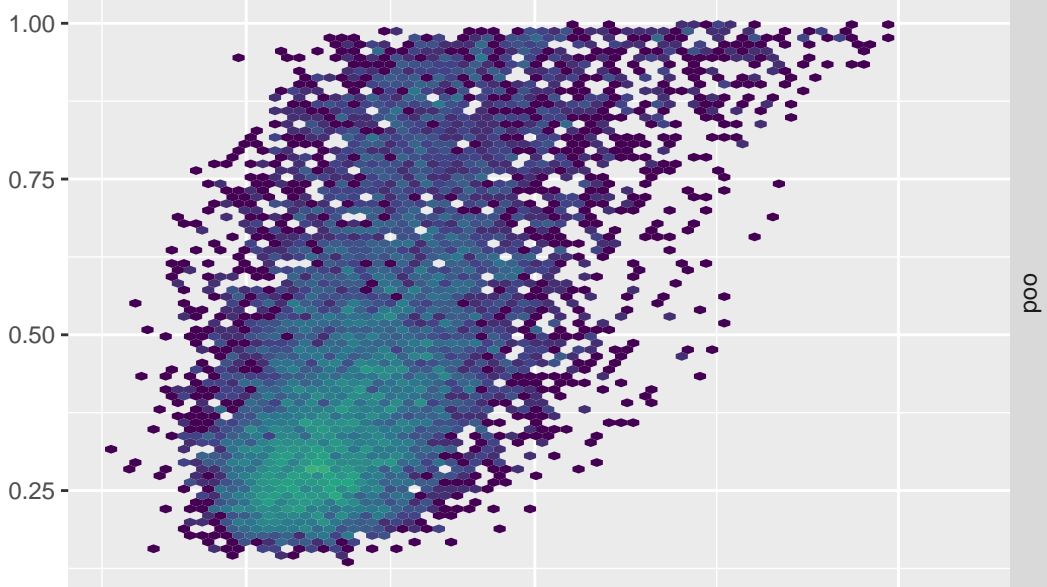


B16+M_B16+R50_B16

mzp

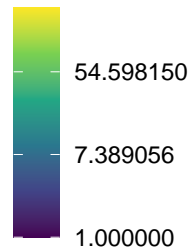


pi

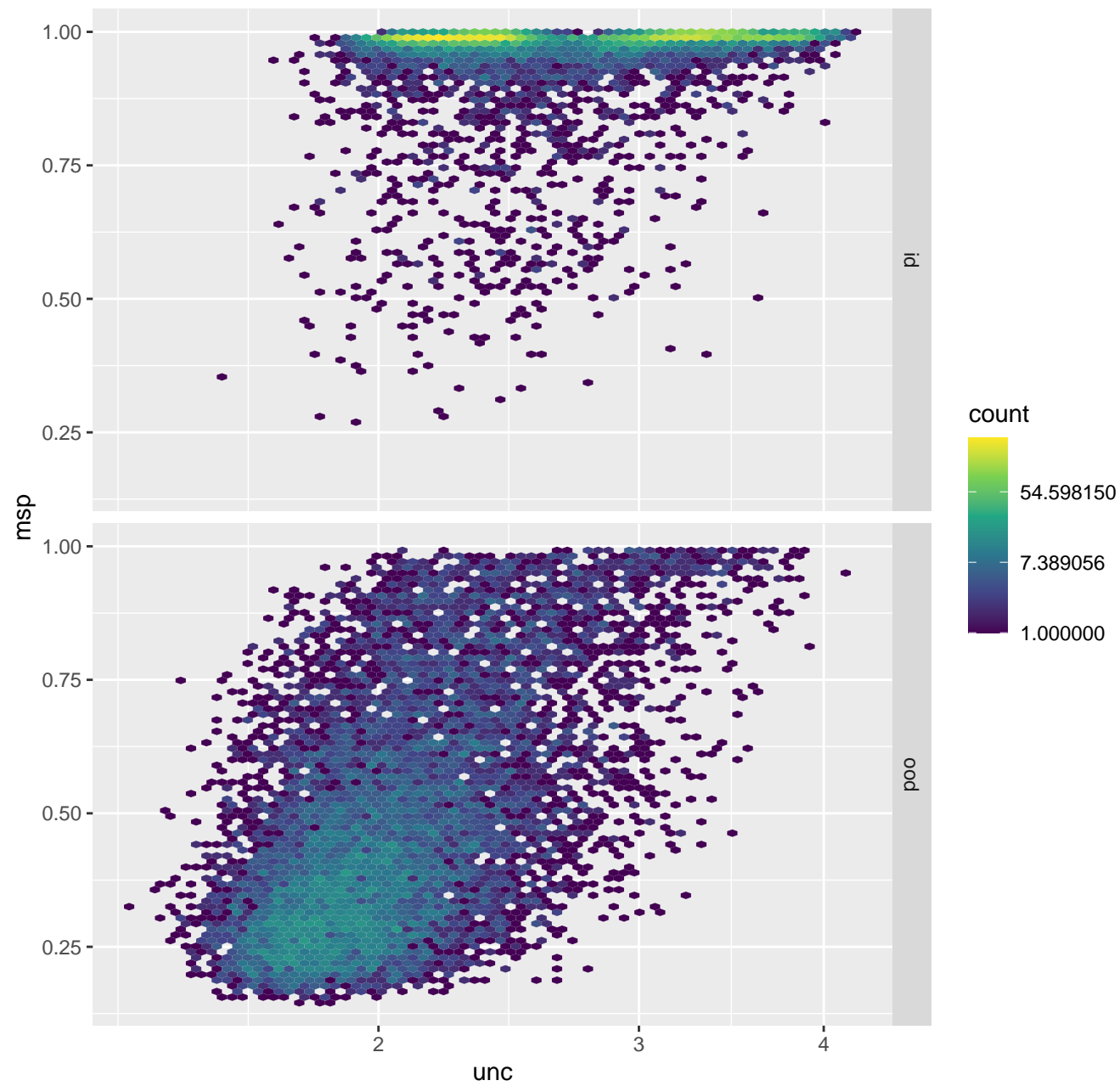


poo

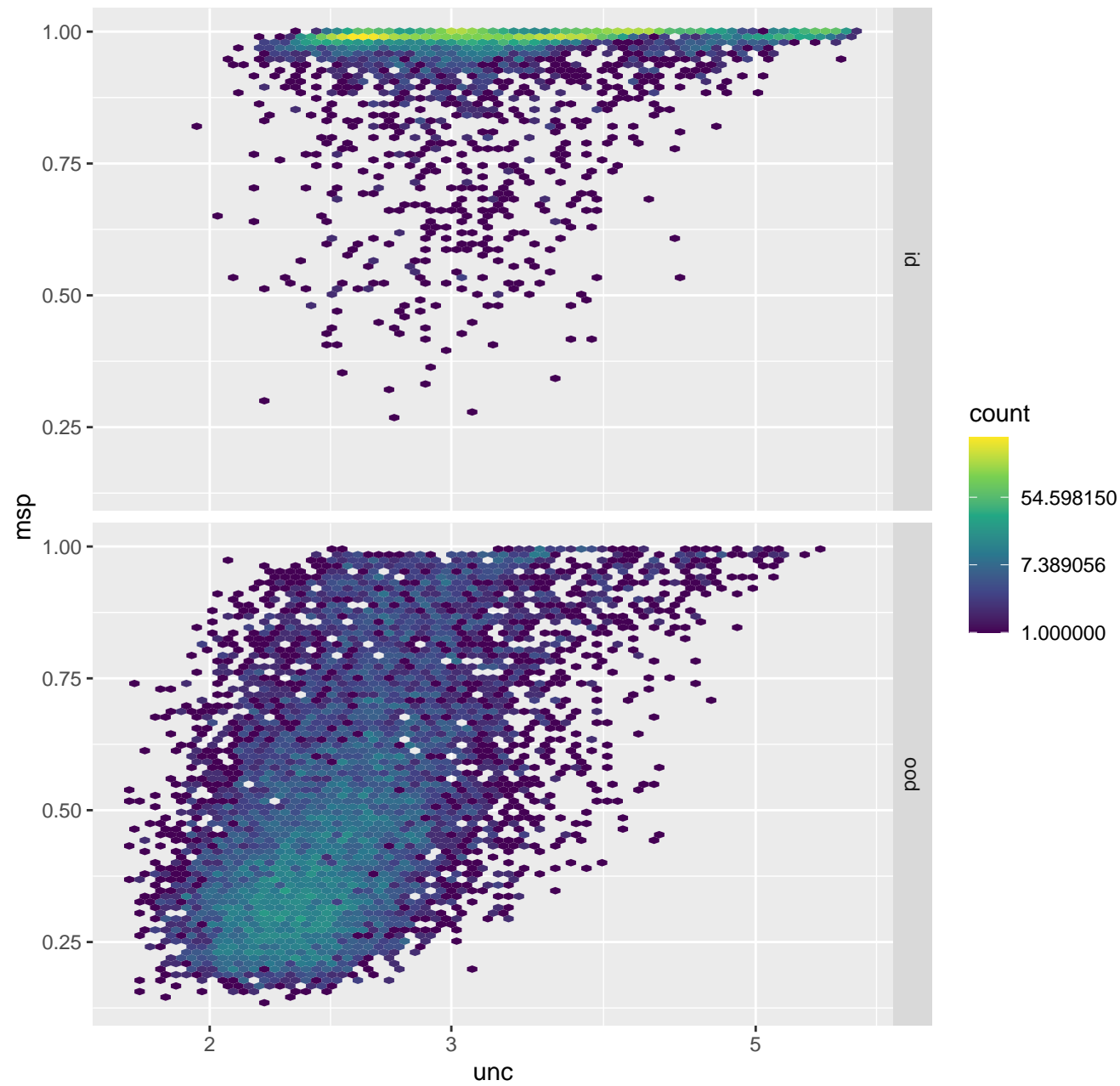
count



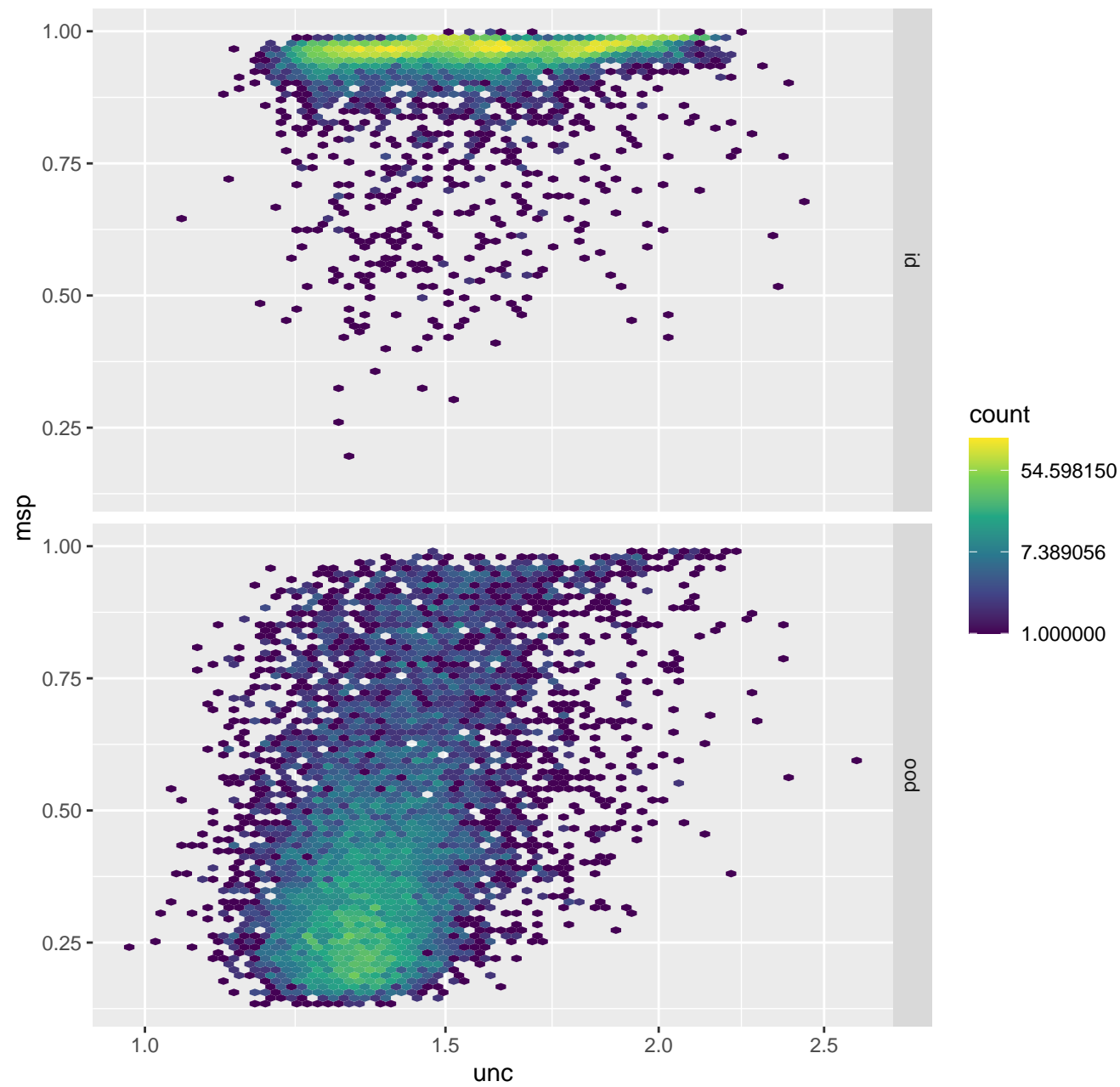
B16+M_B16+R50x1



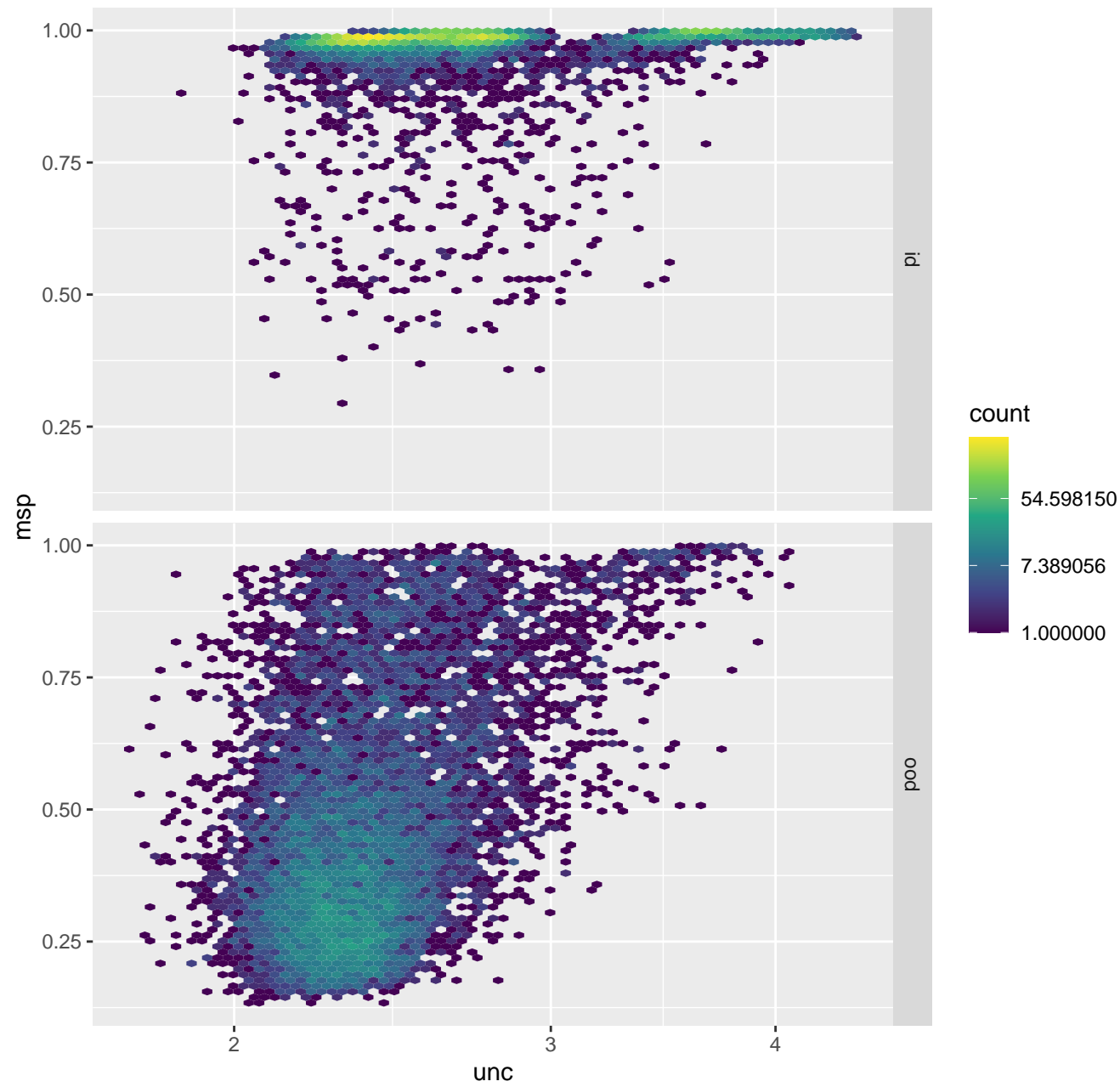
B16+M_B16+R50x1+R50_B16



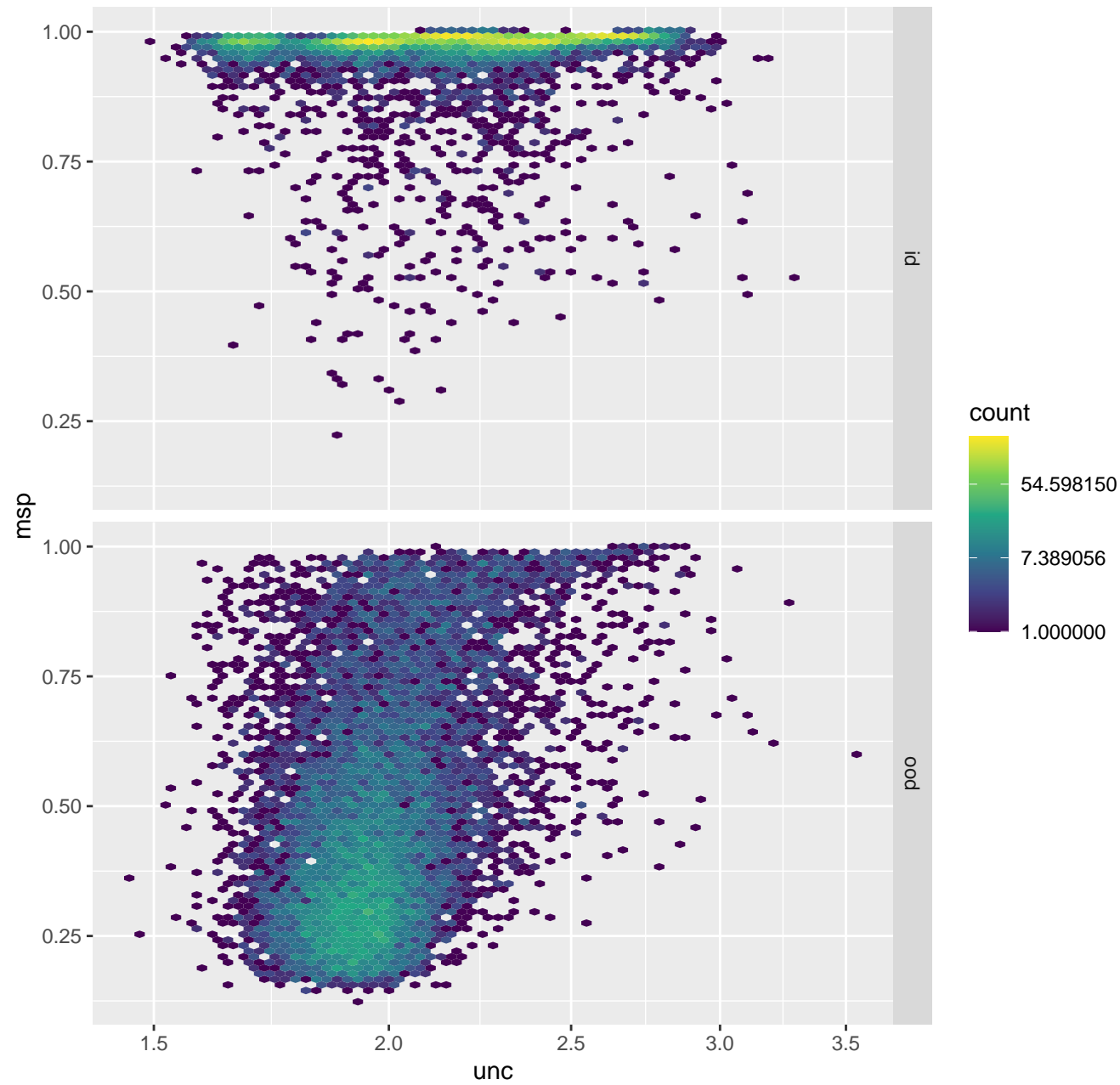
B16+R101x3



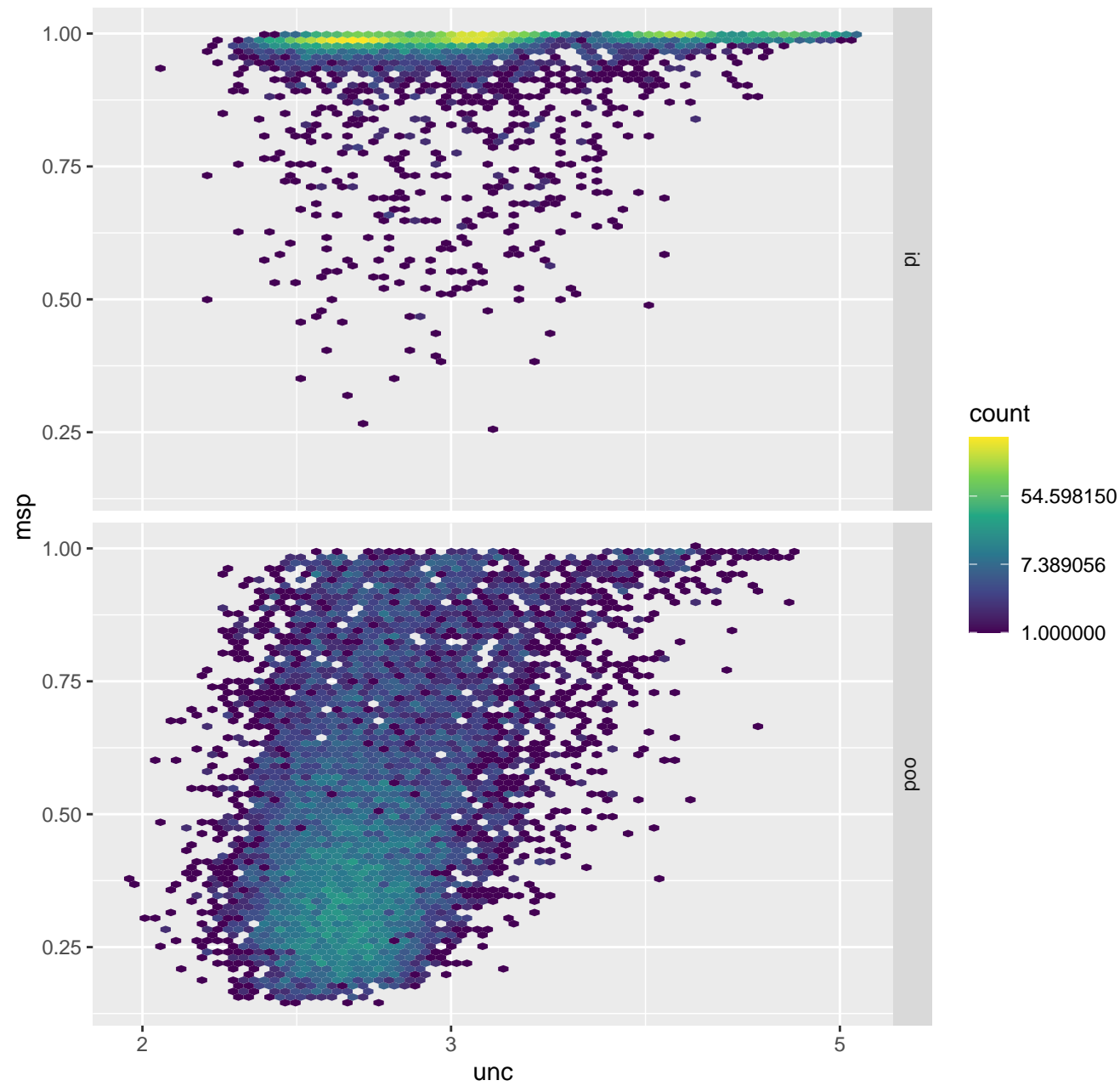
B16+R101x3+R50_B16



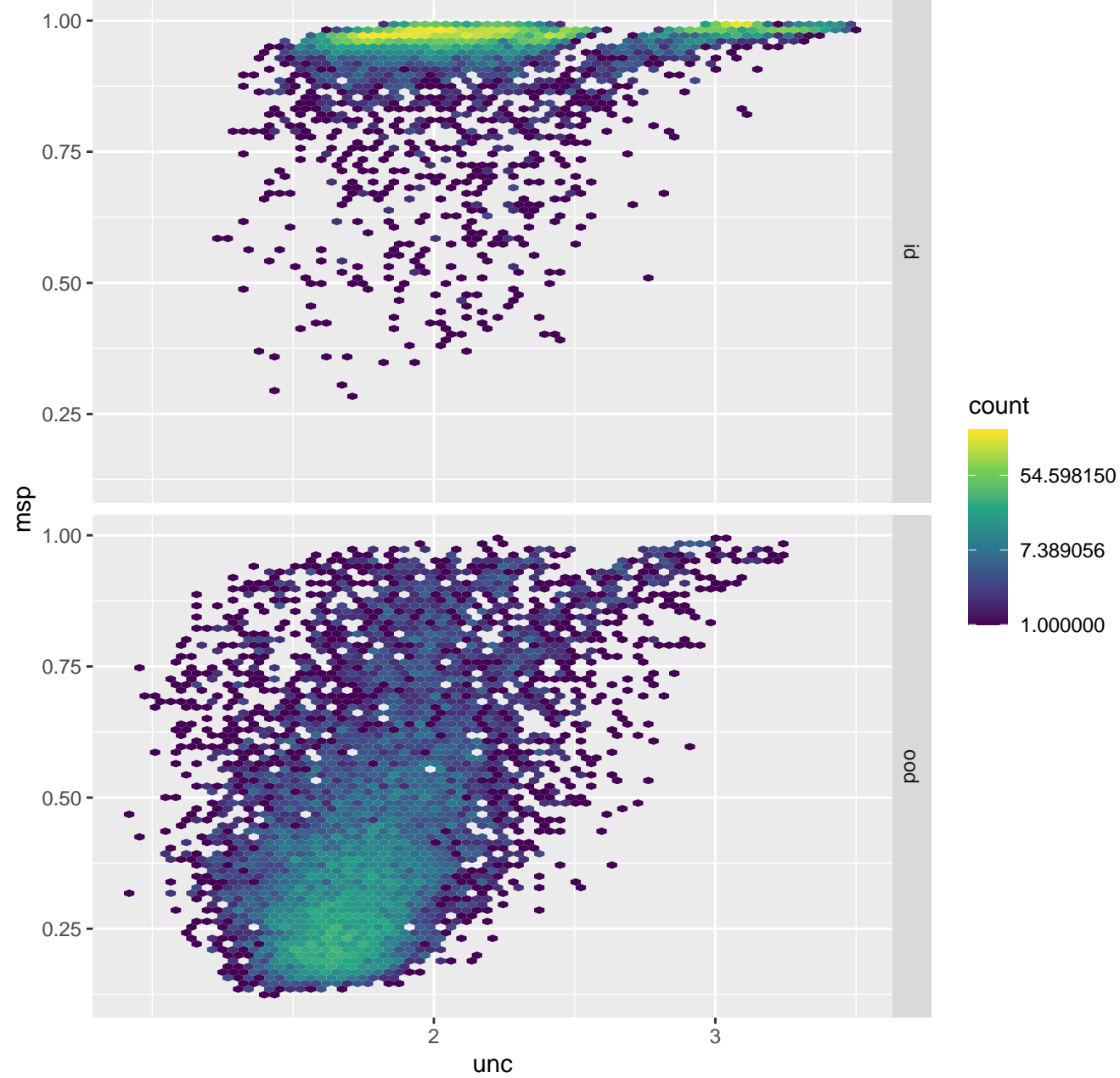
B16+R101x3+R50x1



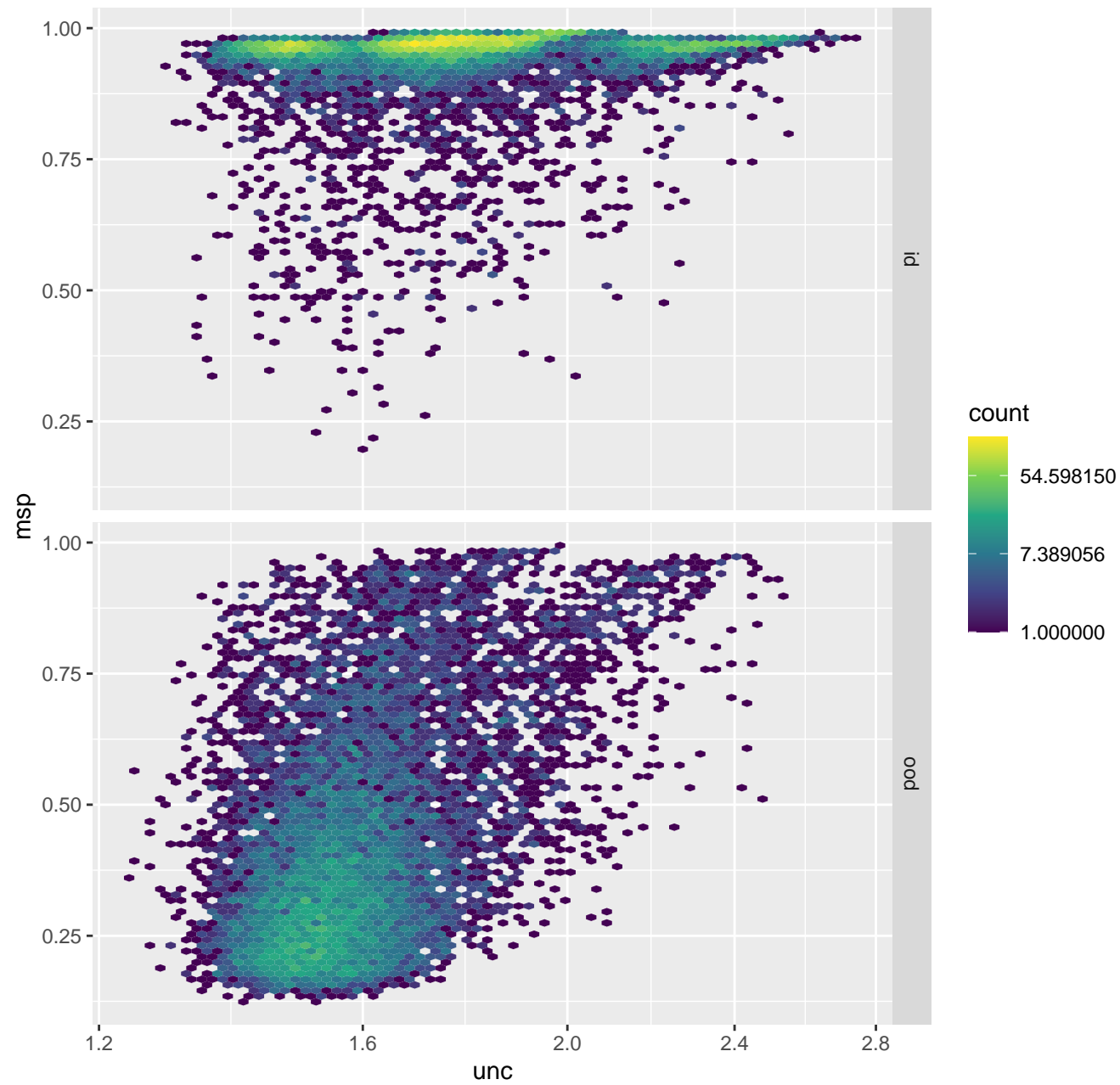
B16+R101x3+R50x1+R50_B16



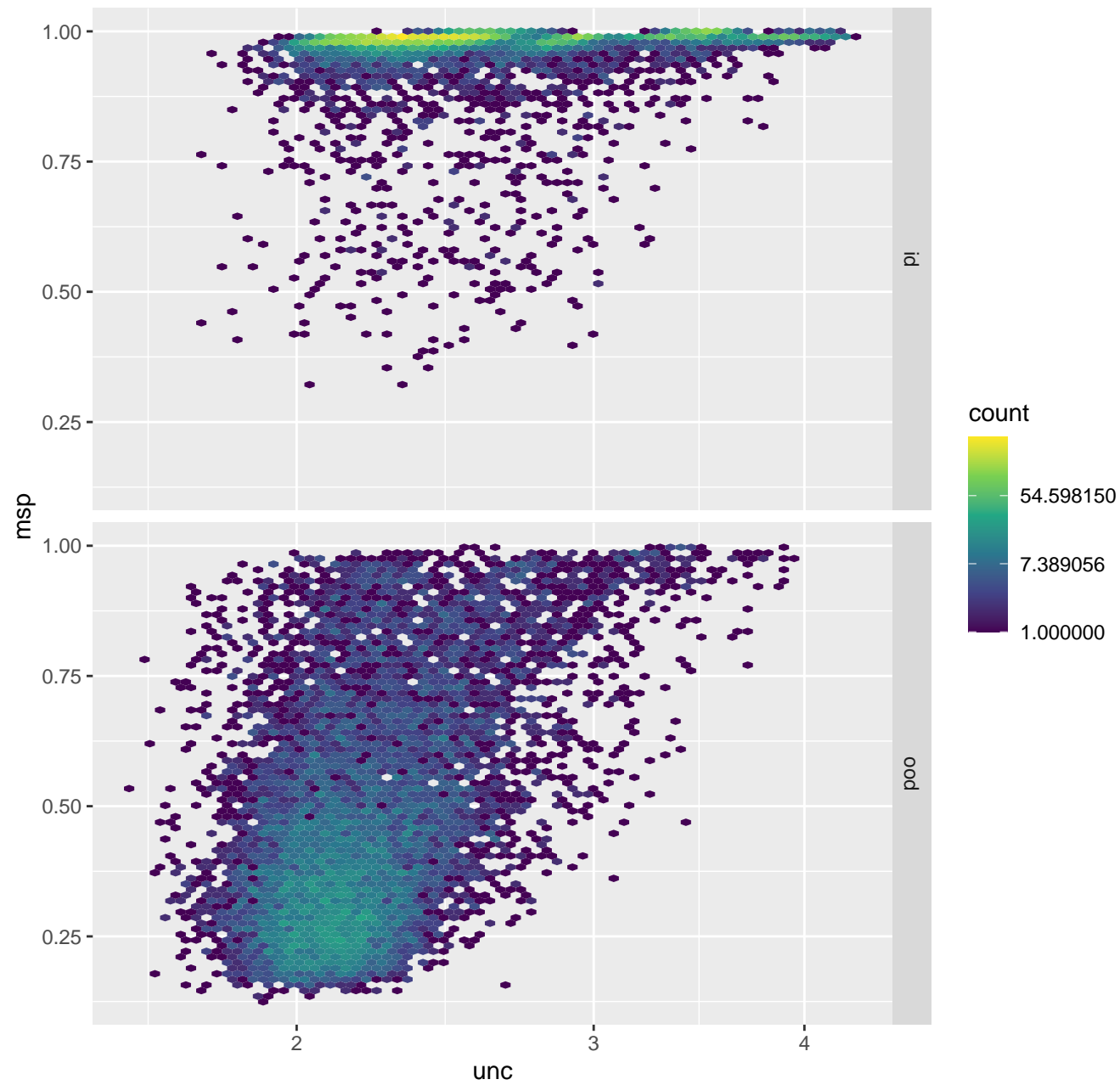
B16+R50_B16



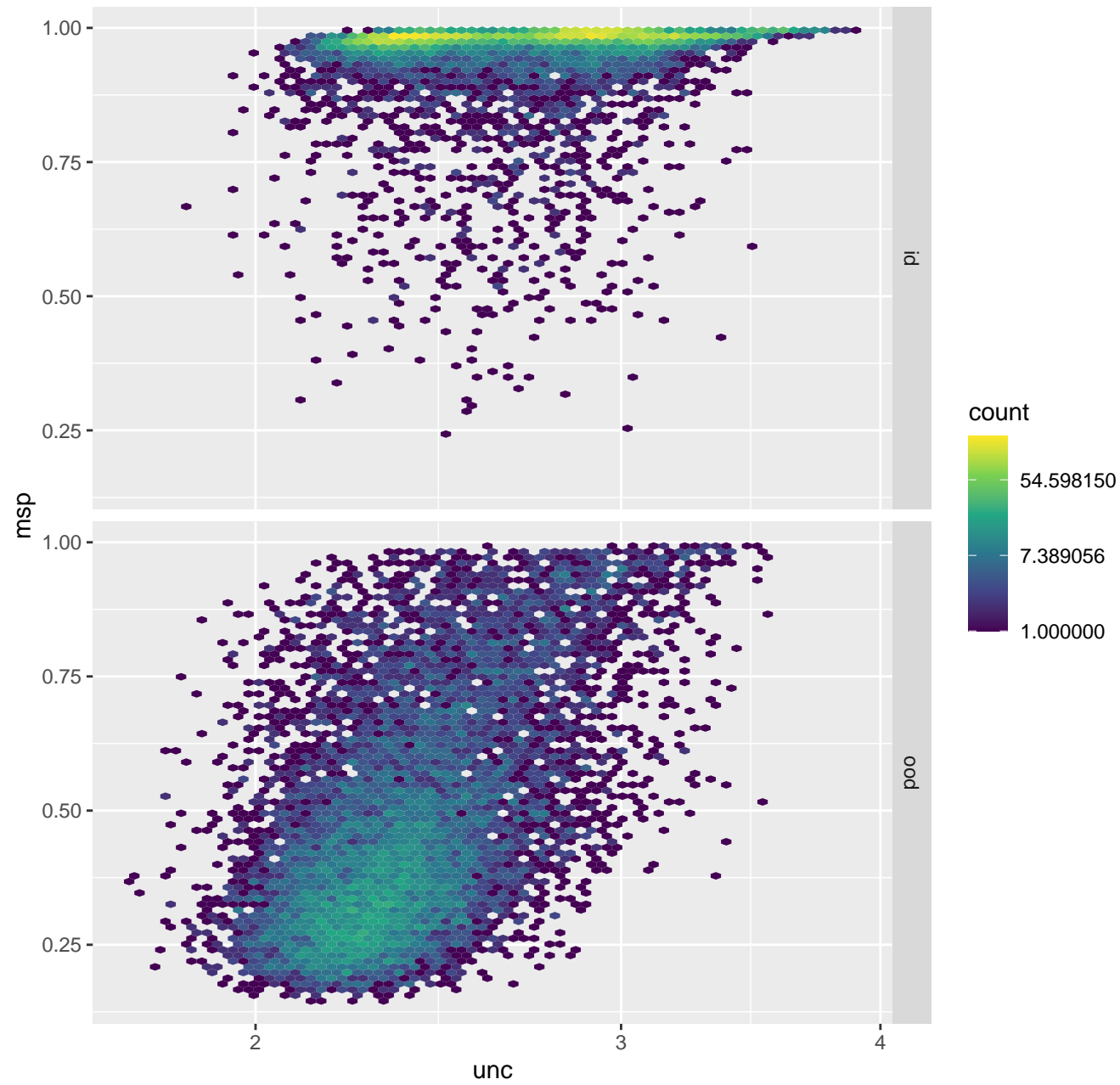
B16+R50x1



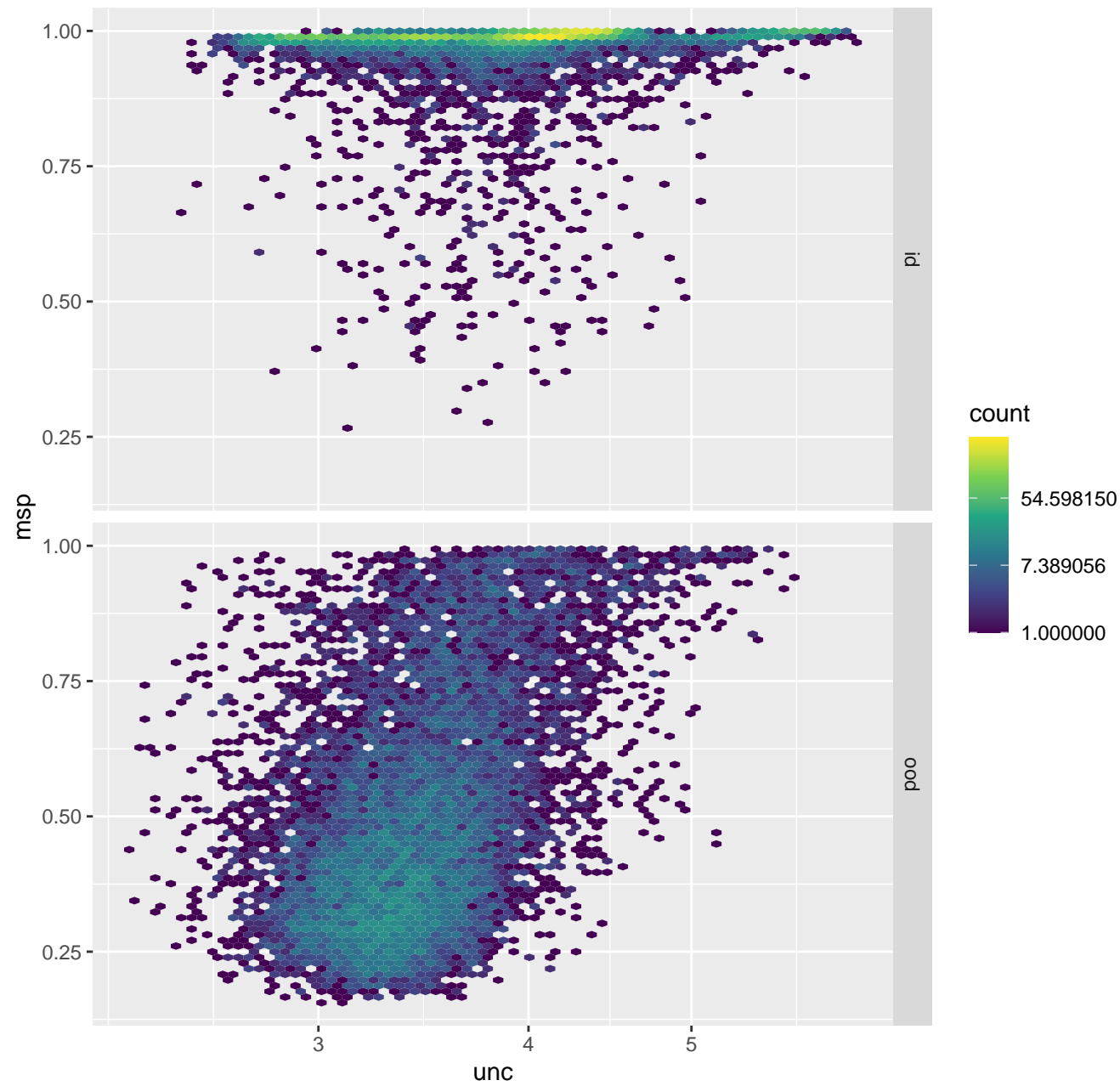
B16+R50x1+R50_B16



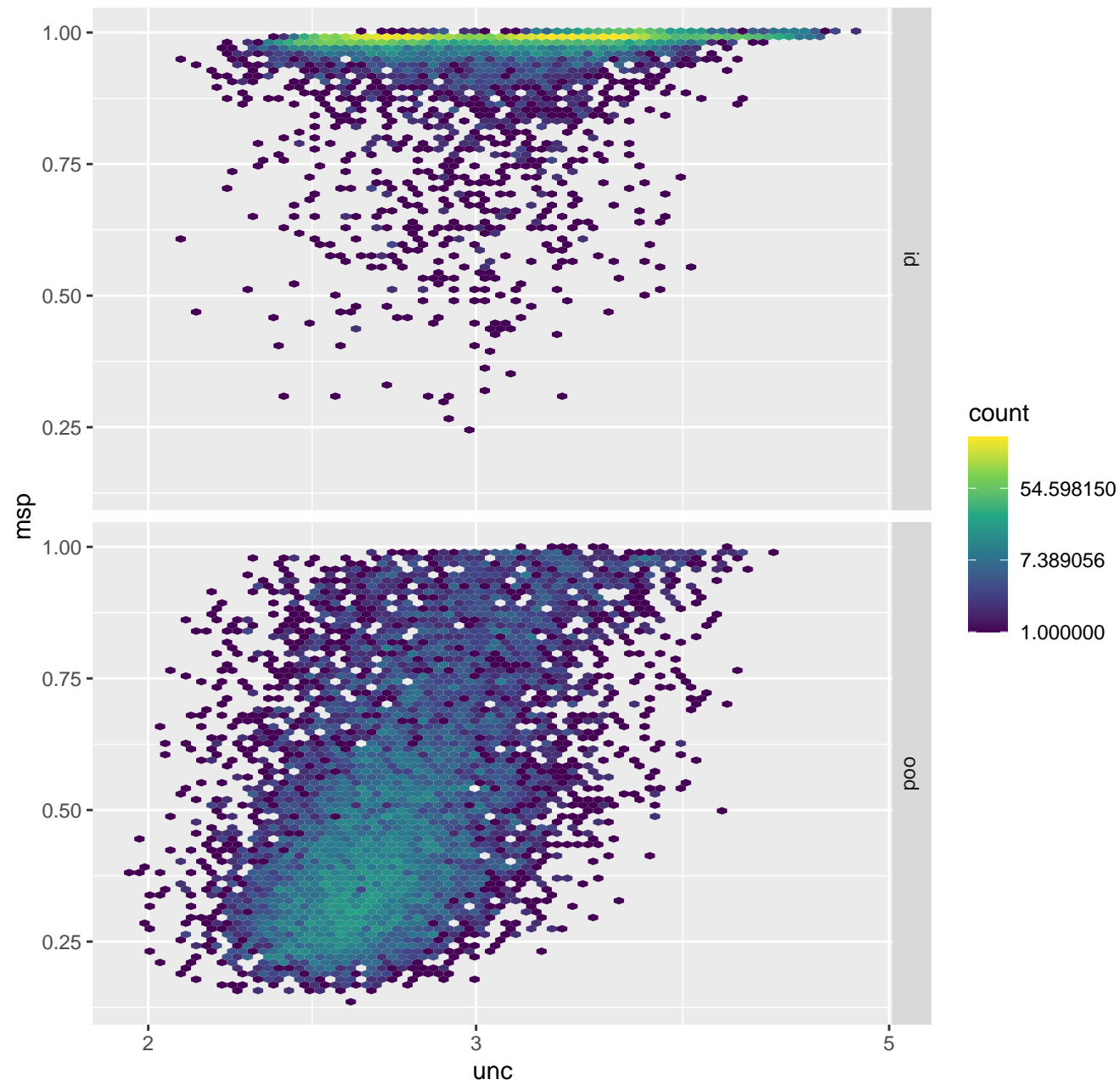
M_B16+R101x3



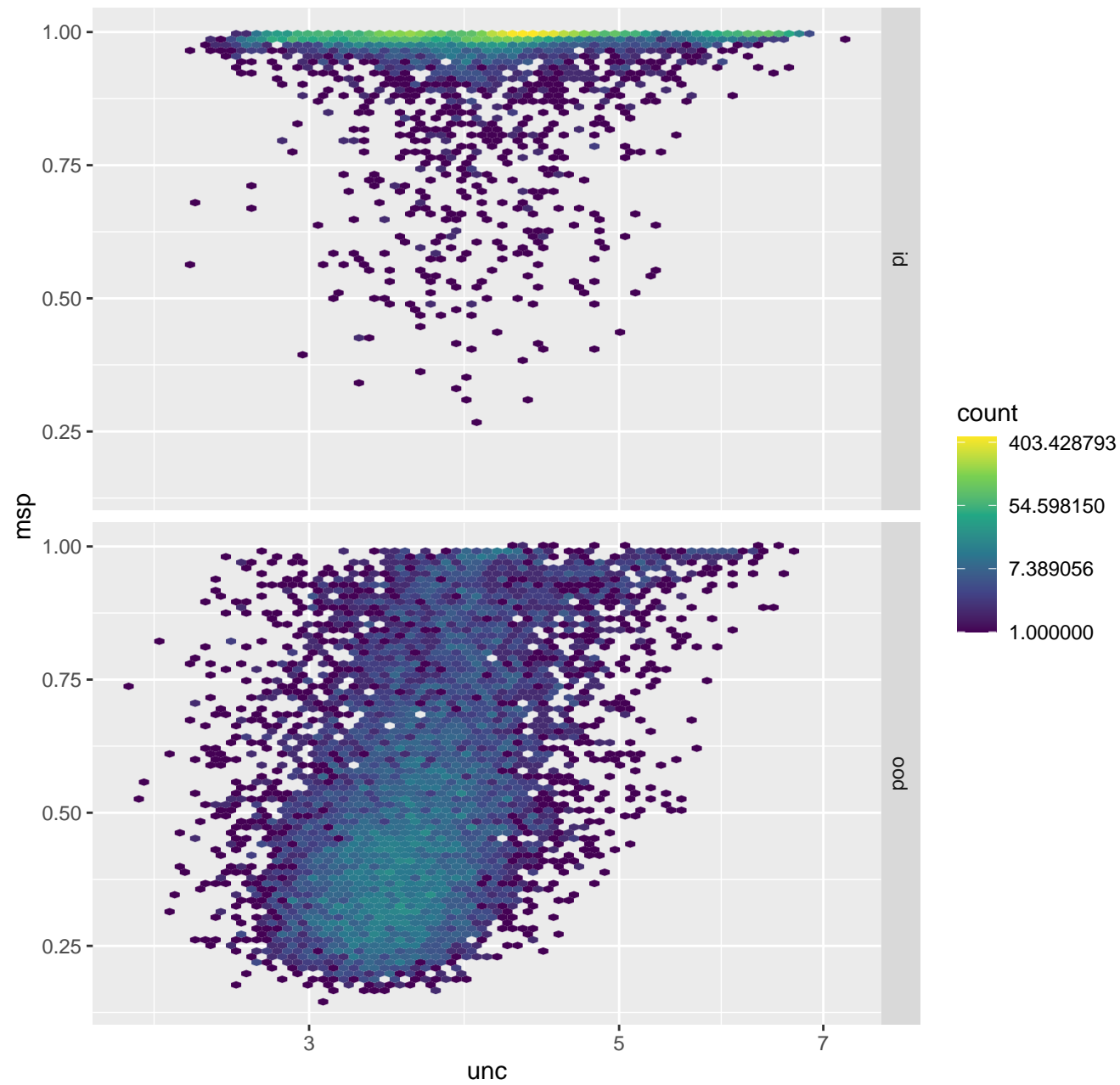
M_B16+R101x3+R50_B16



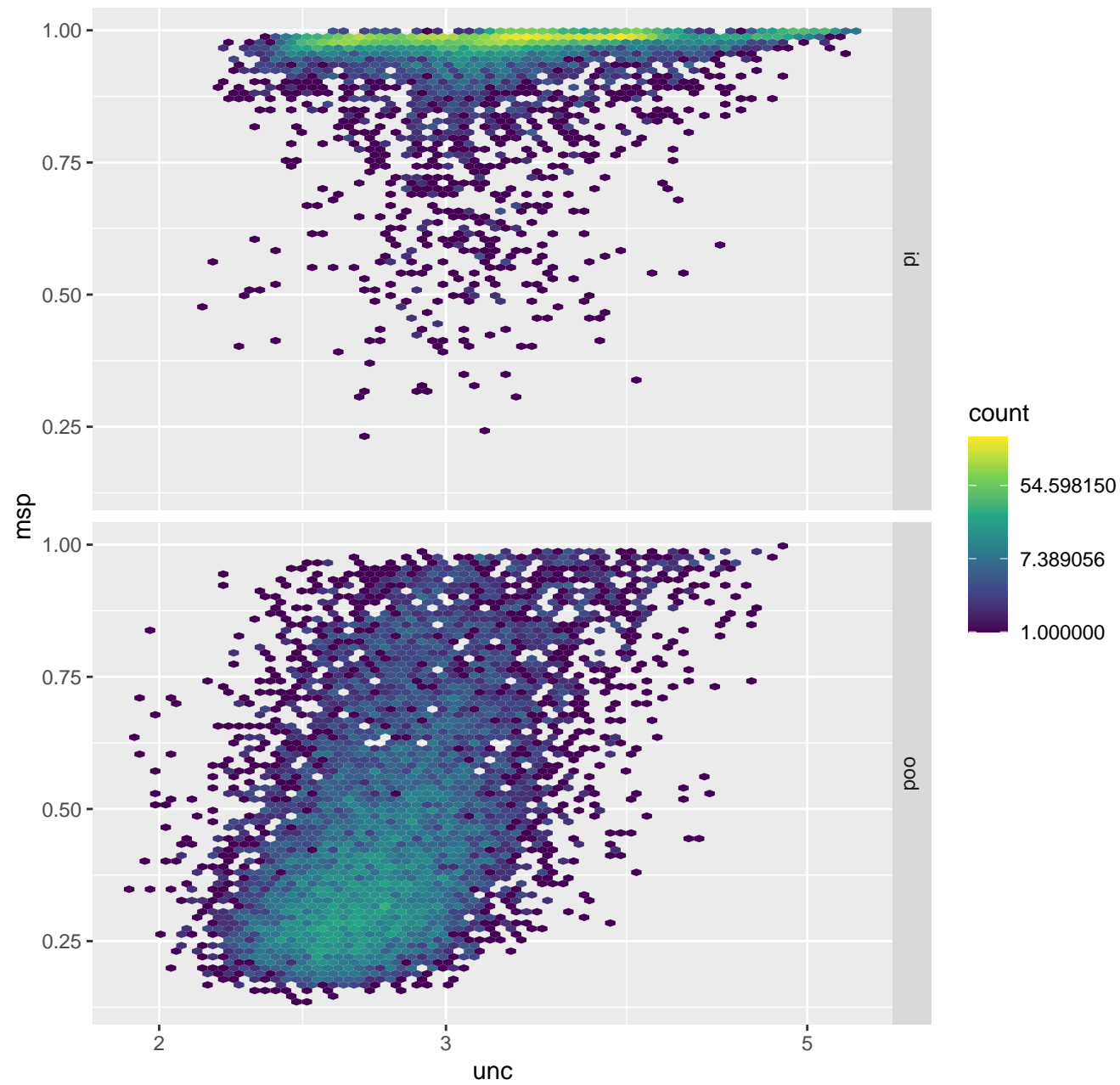
M_B16+R101x3+R50x1



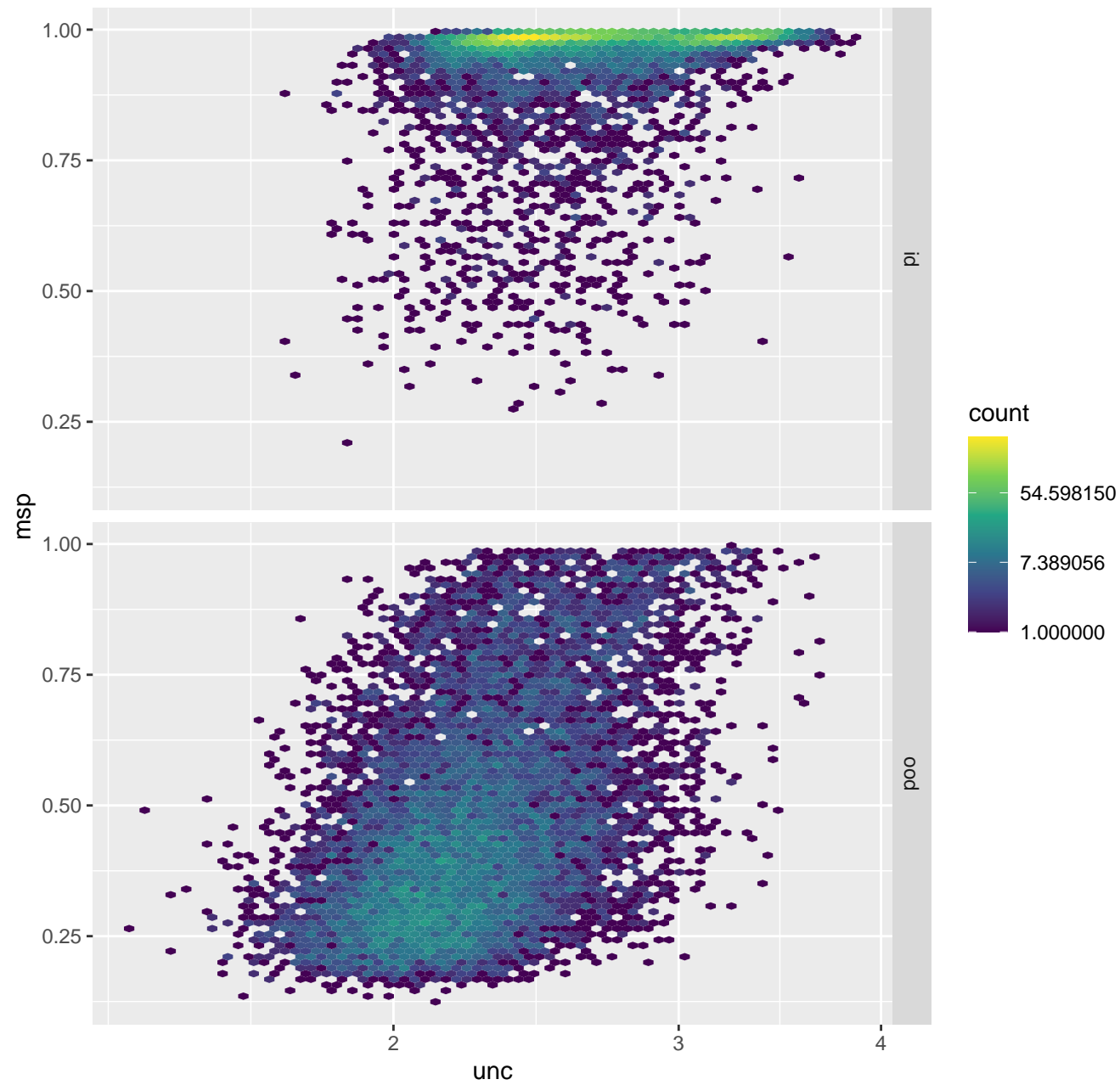
M_B16+R101x3+R50x1+R50_B16



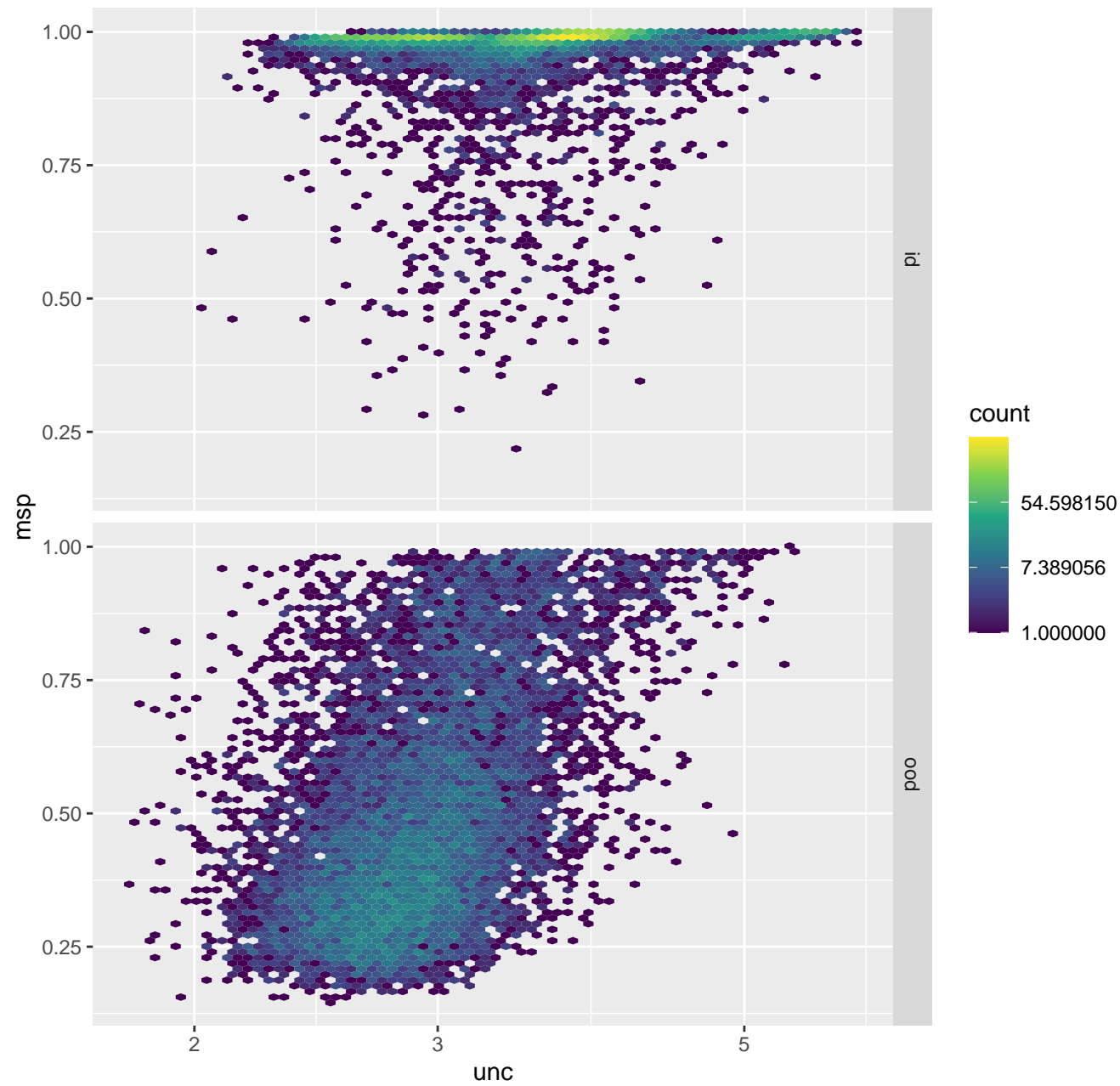
M_B16+R50_B16



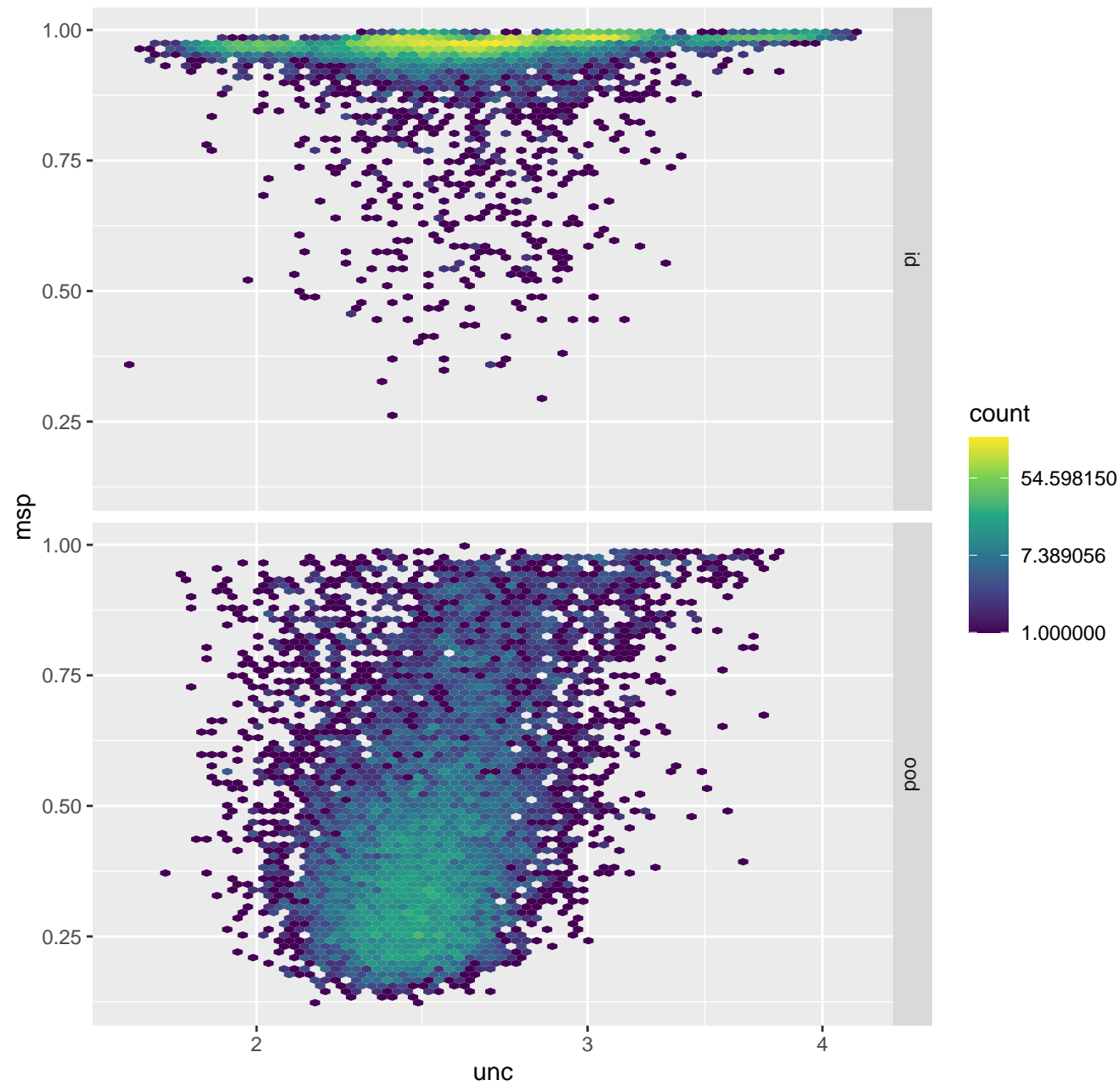
M_B16+R50x1



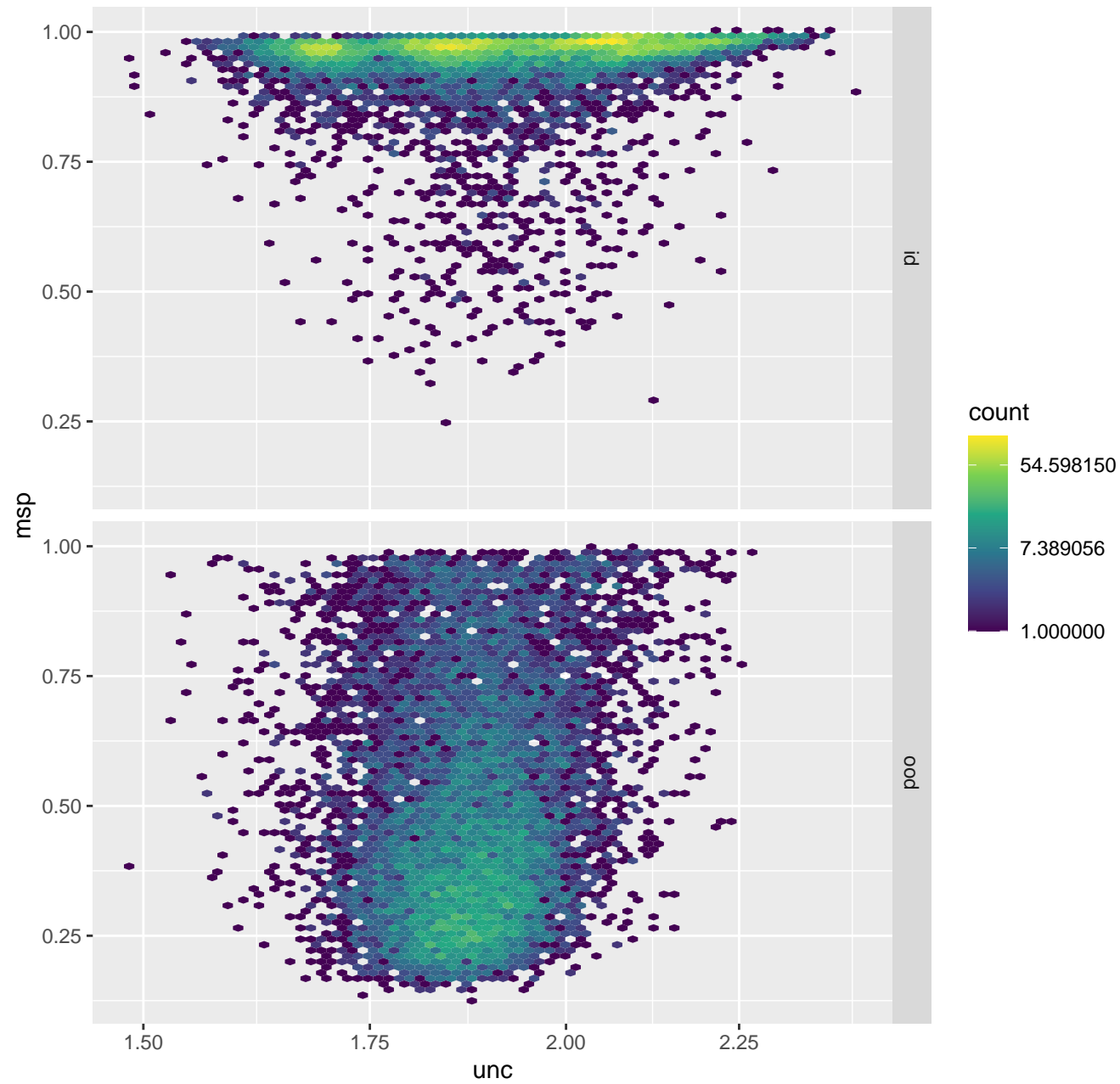
M_B16+R50x1+R50_B16



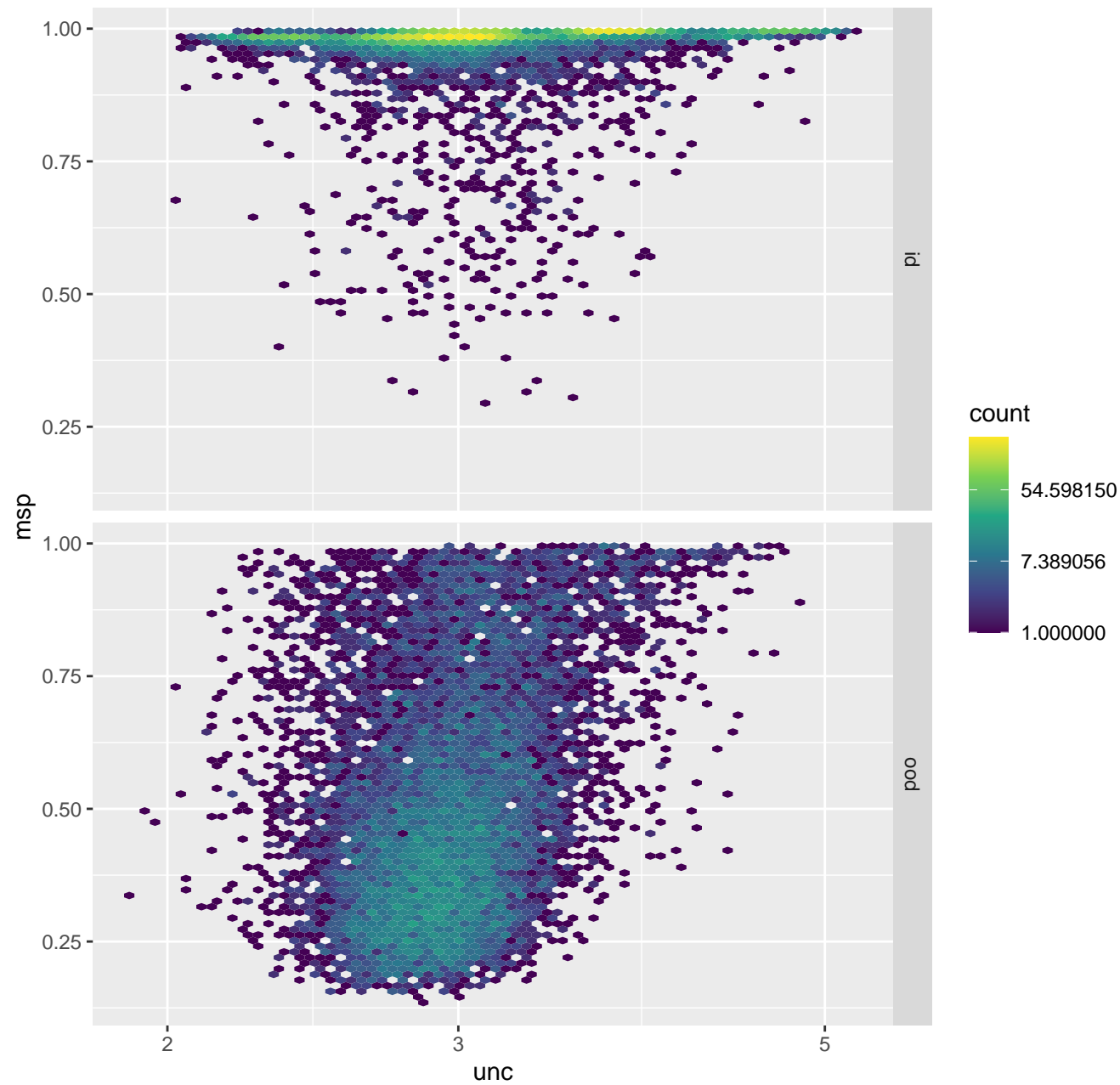
R101x3+R50_B16



R101x3+R50x1



R101x3+R50x1+R50_B16



R50x1+R50_B16

