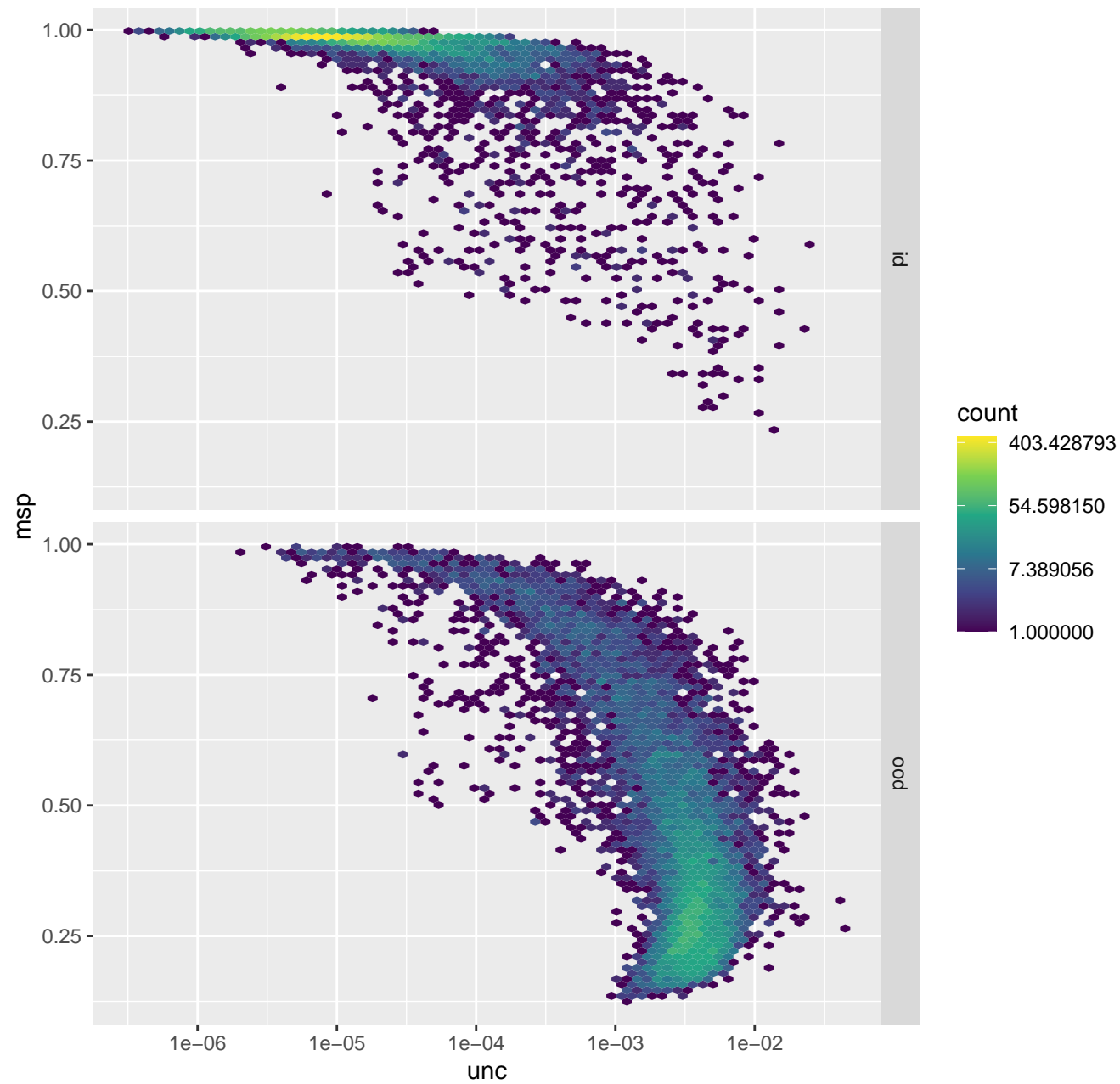
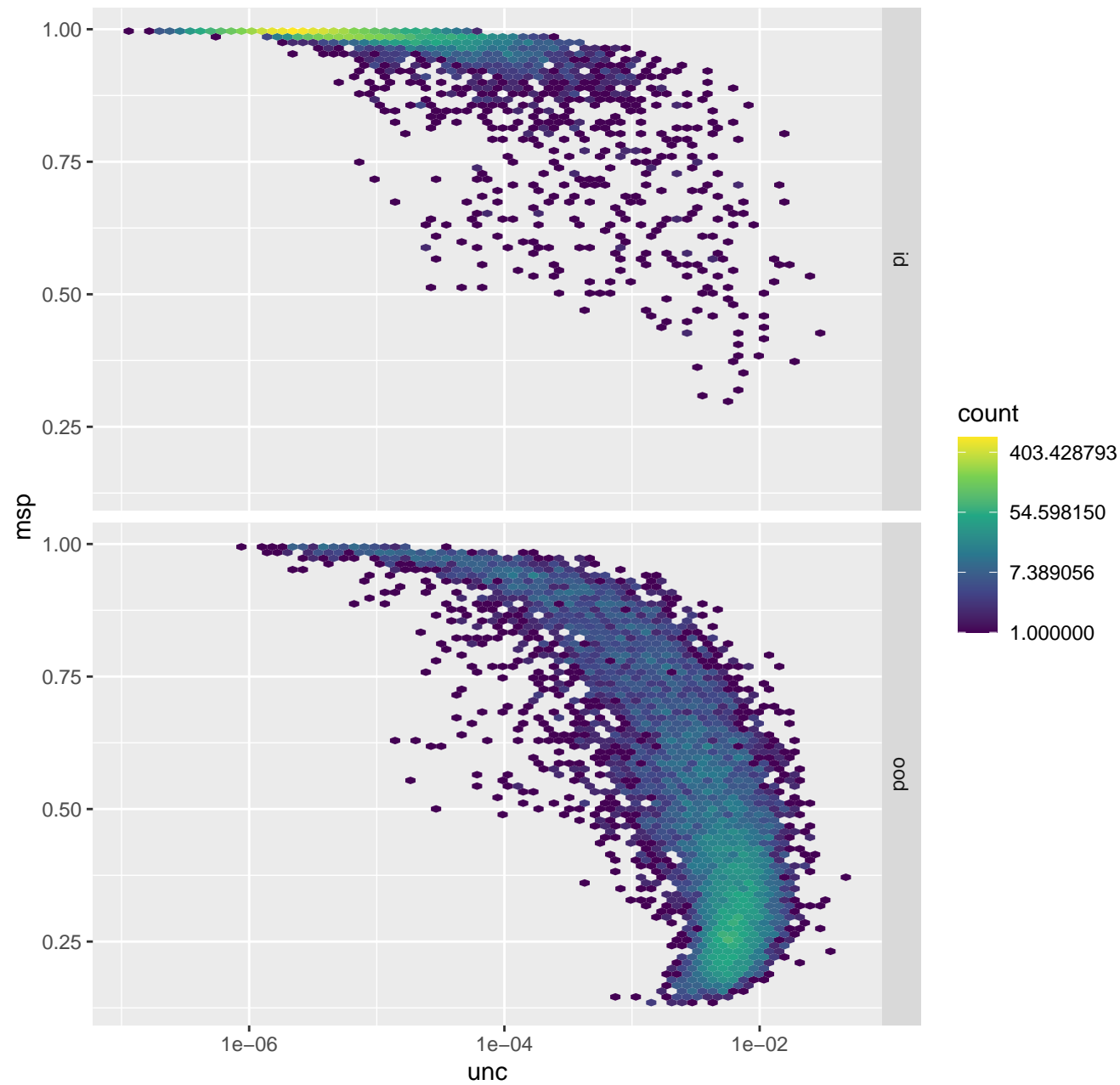


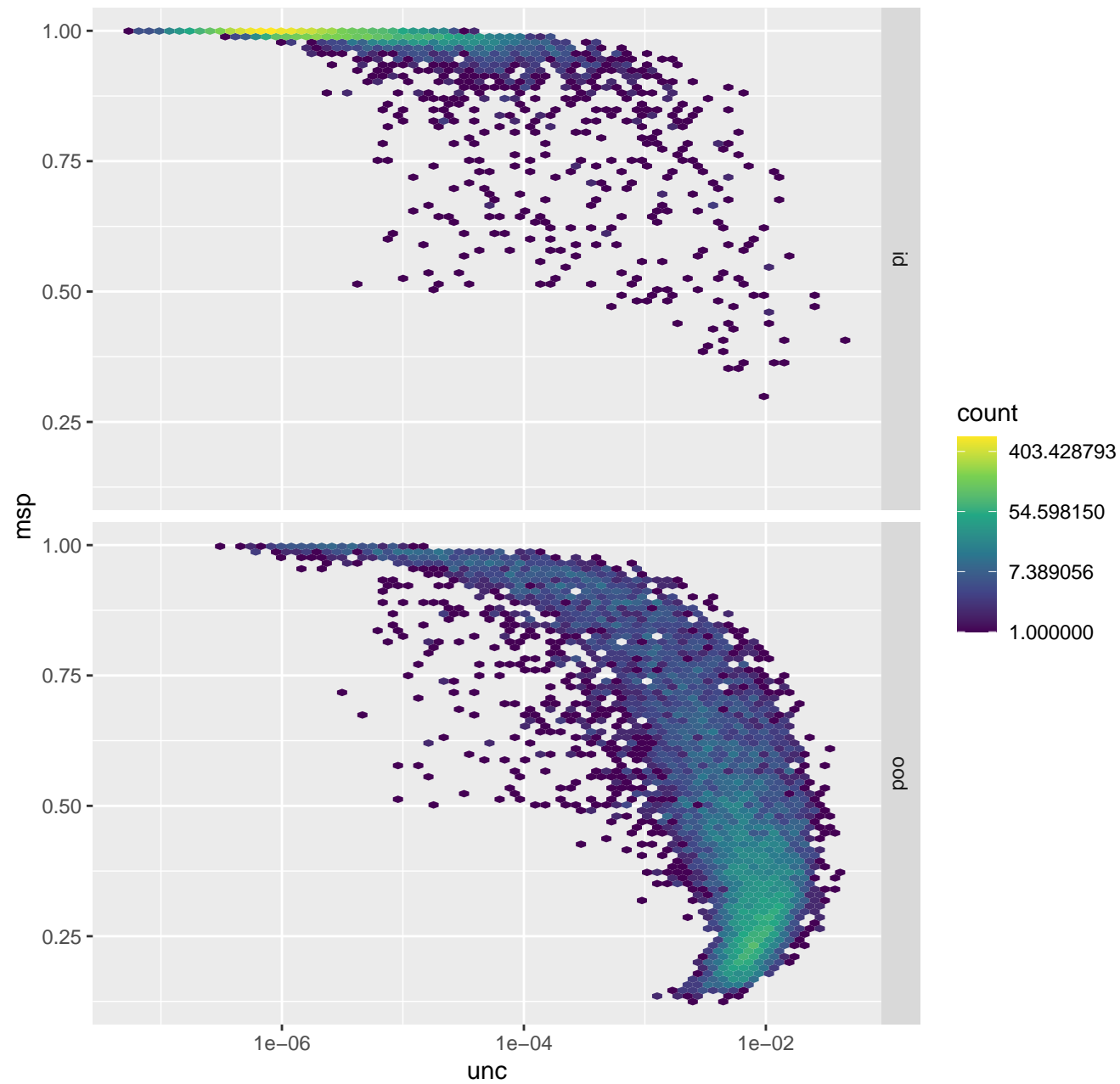
B16+M\_B16



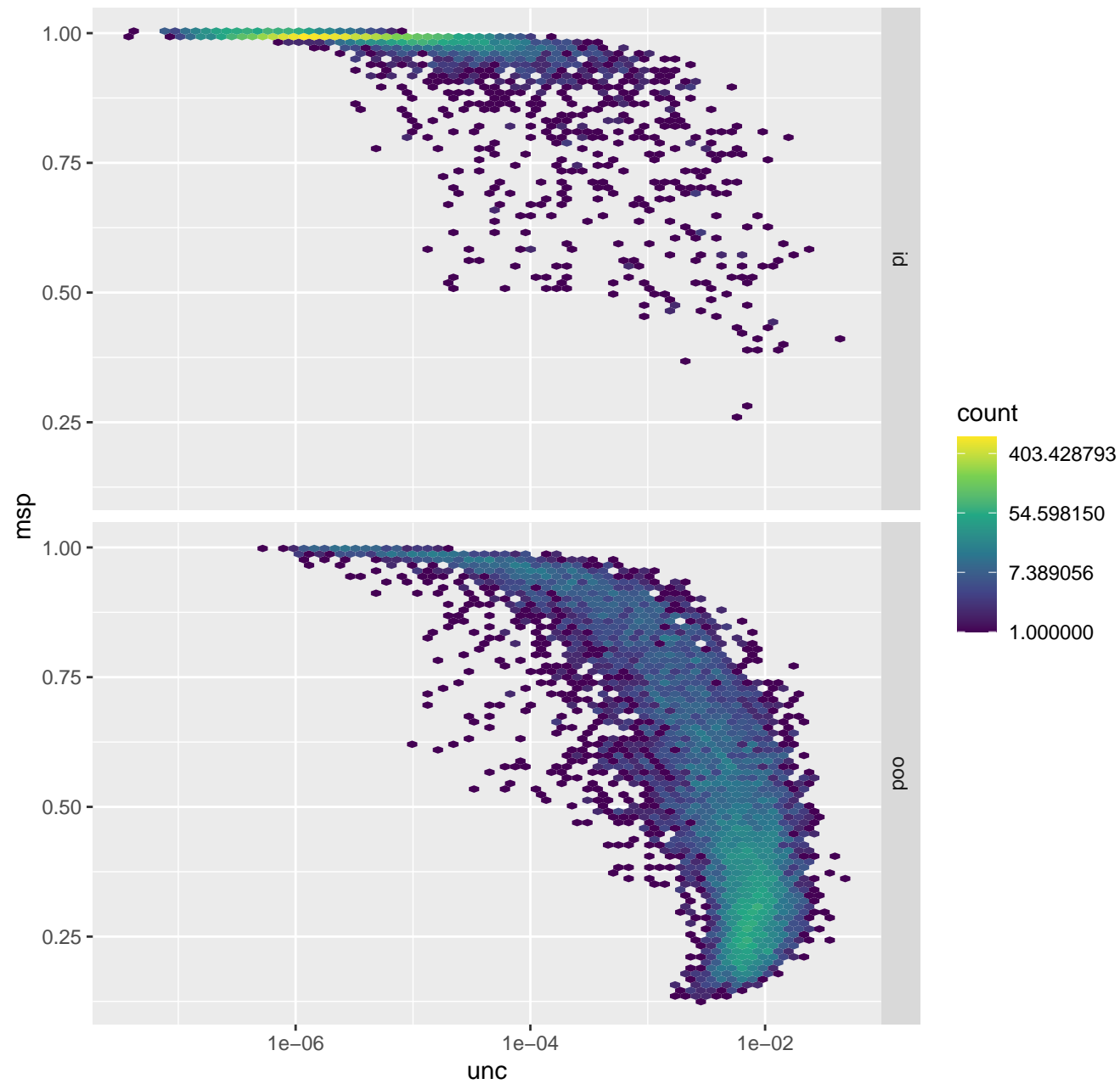
B16+M\_B16+R101x3



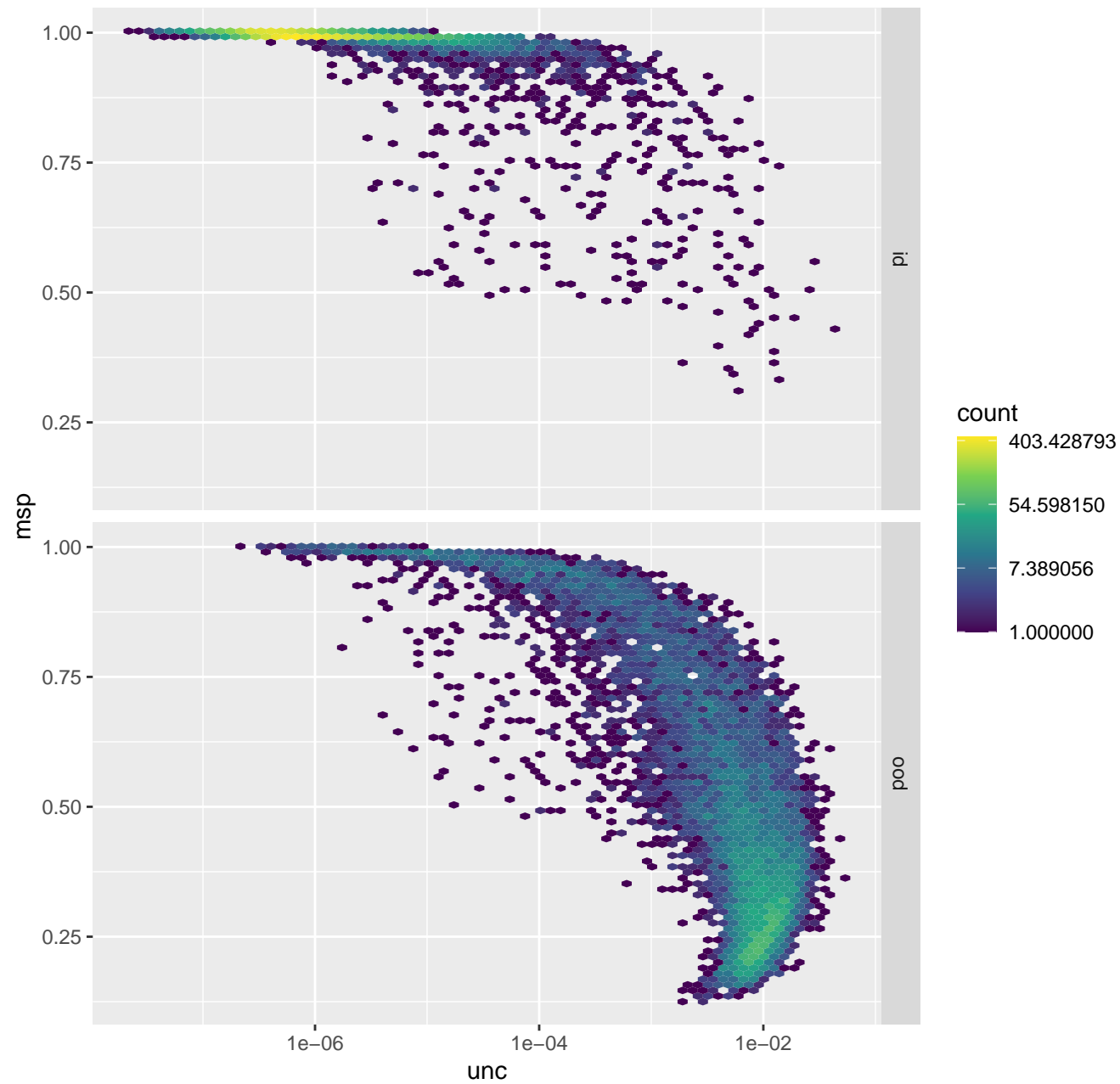
B16+M\_B16+R101x3+R50\_B16



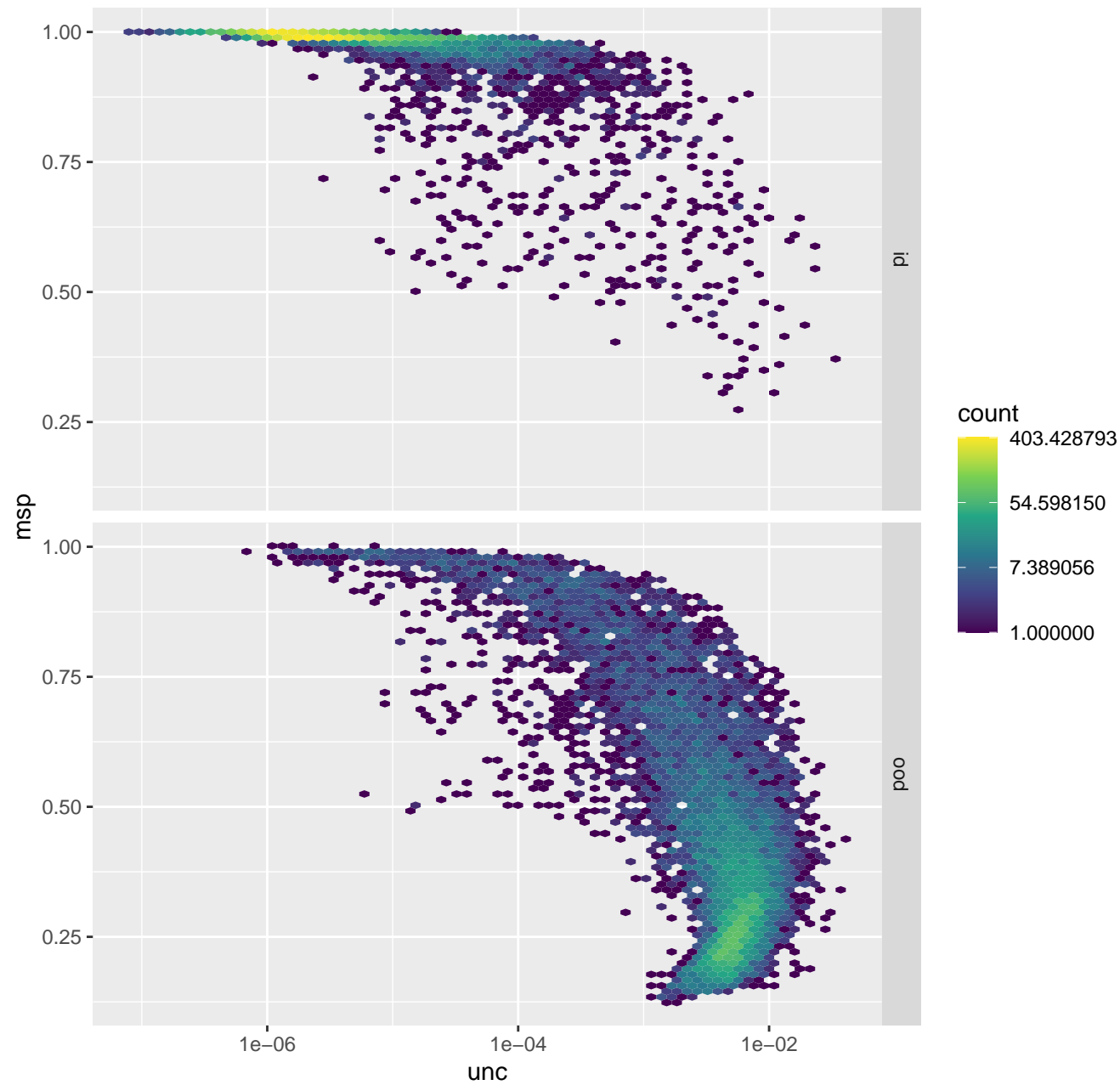
B16+M\_B16+R101x3+R50x1



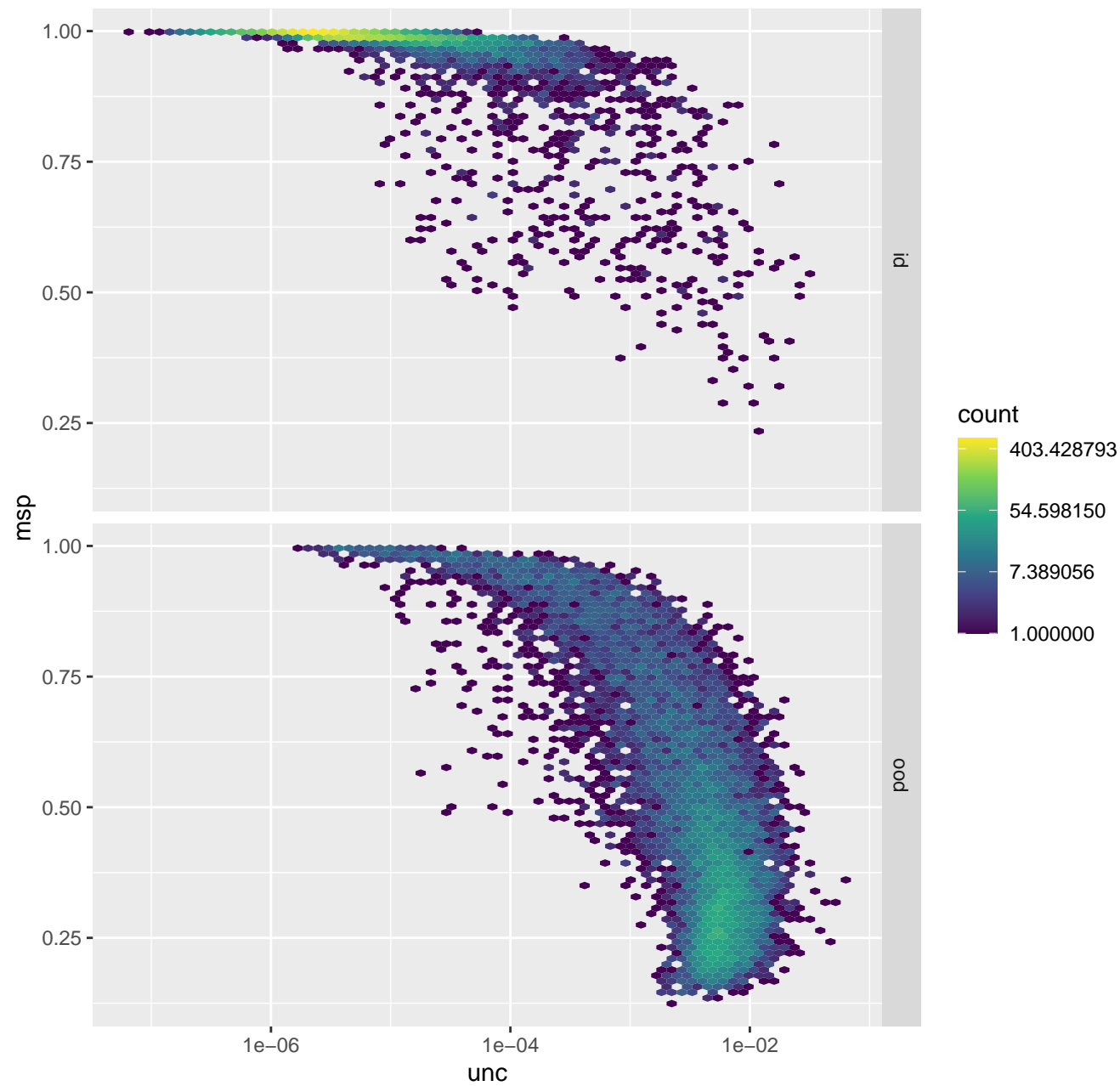
B16+M\_B16+R101x3+R50x1+R50\_B16



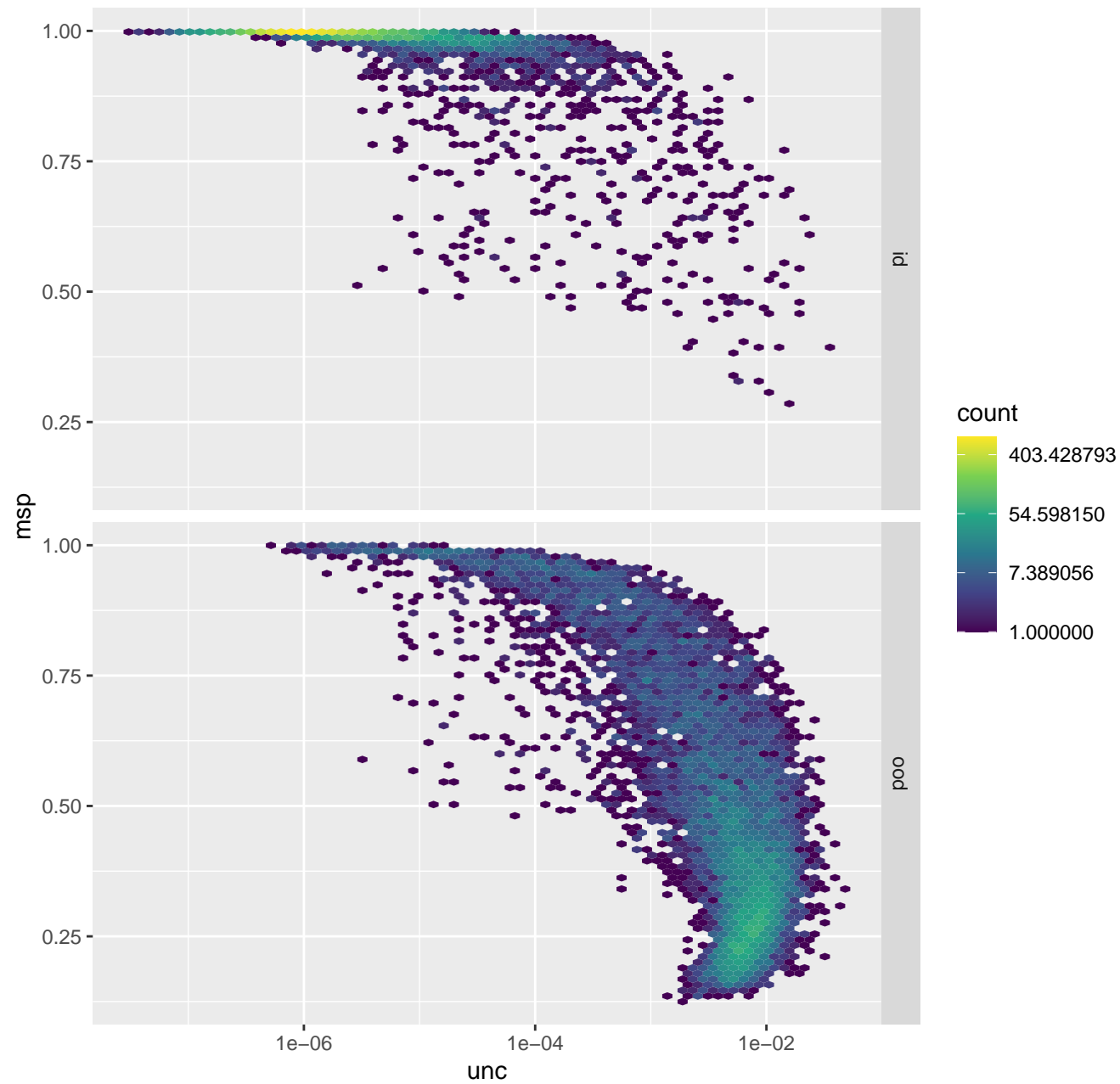
B16+M\_B16+R50\_B16



B16+M\_B16+R50x1

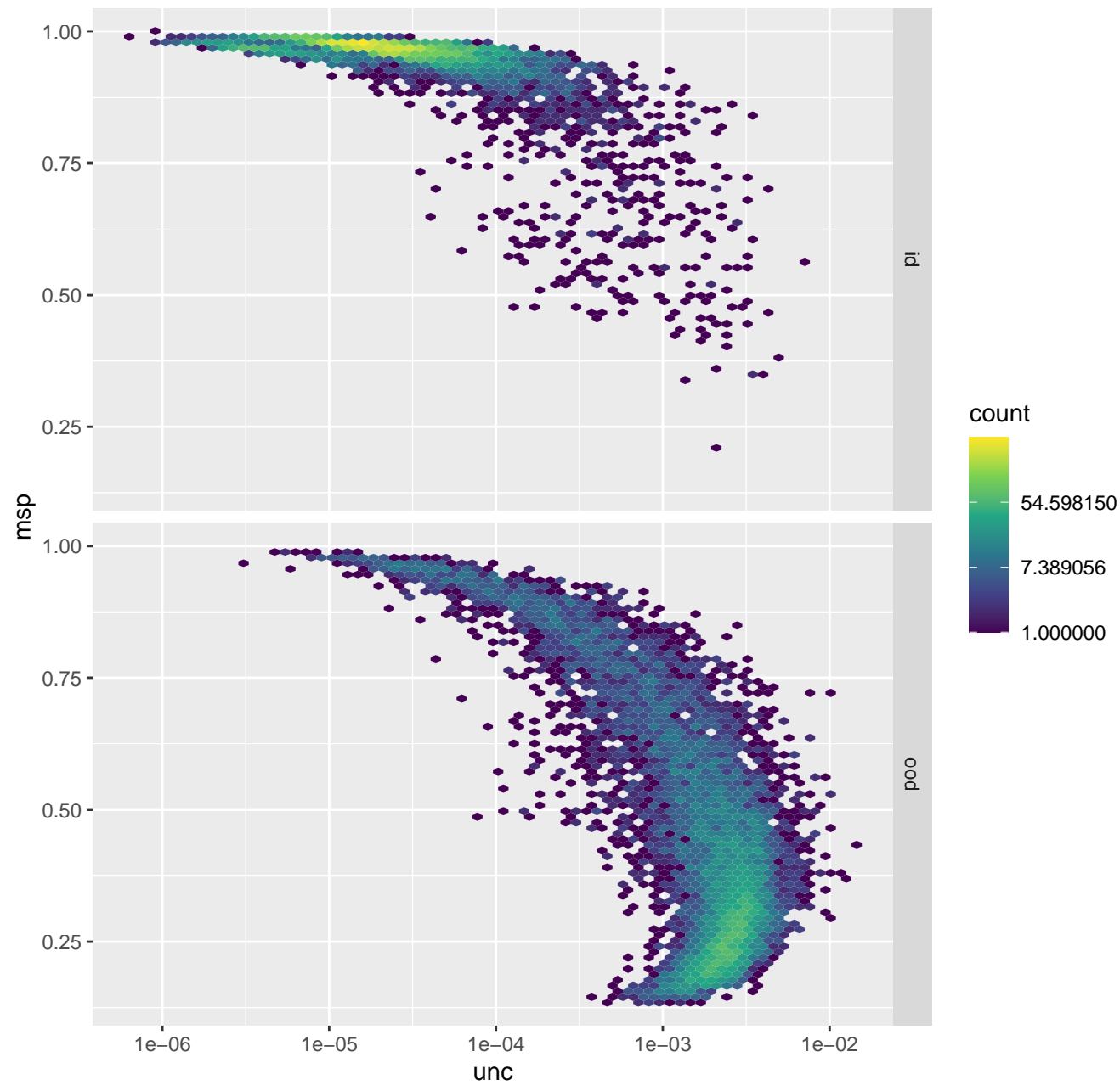


B16+M\_B16+R50x1+R50\_B16

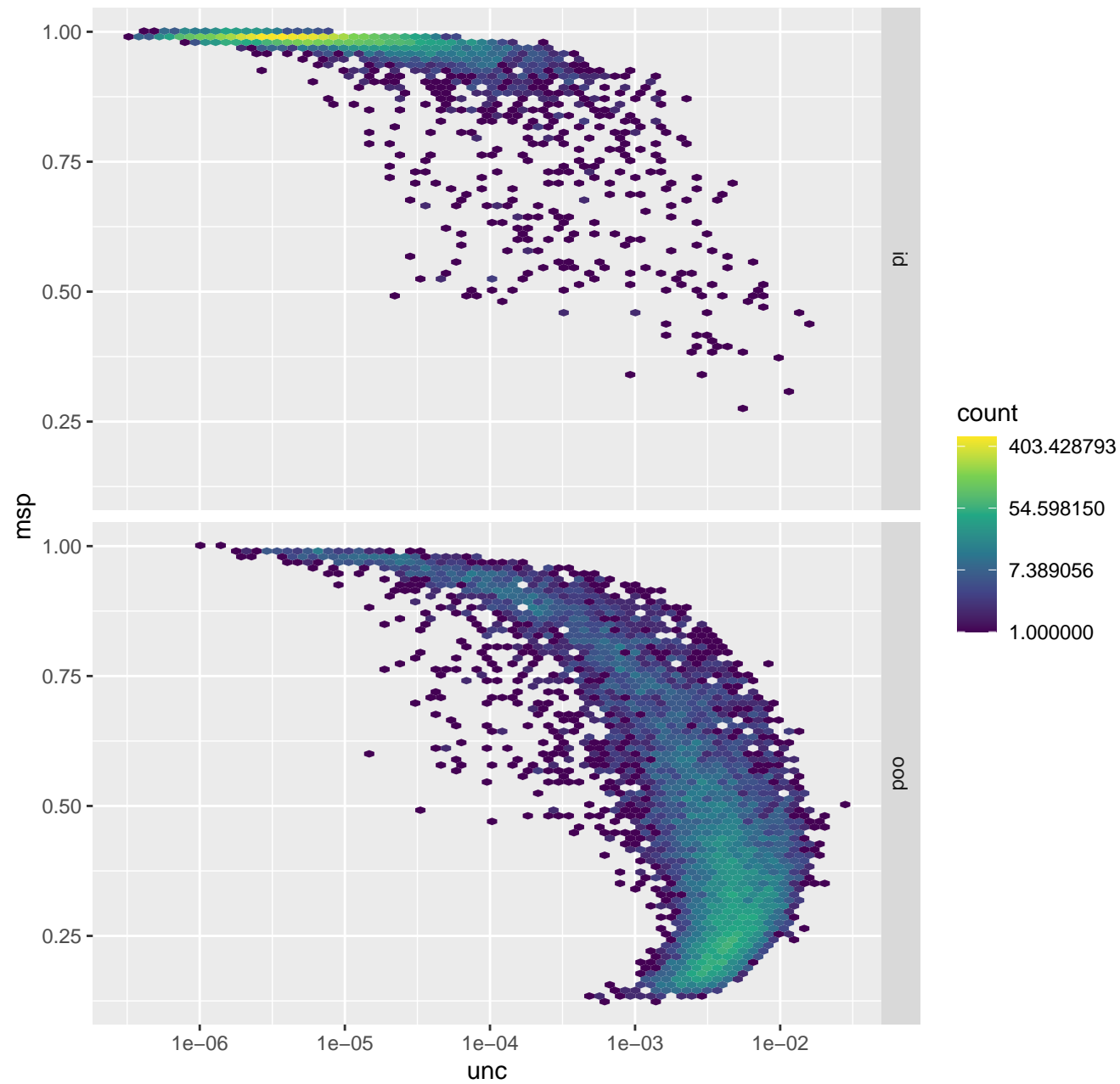




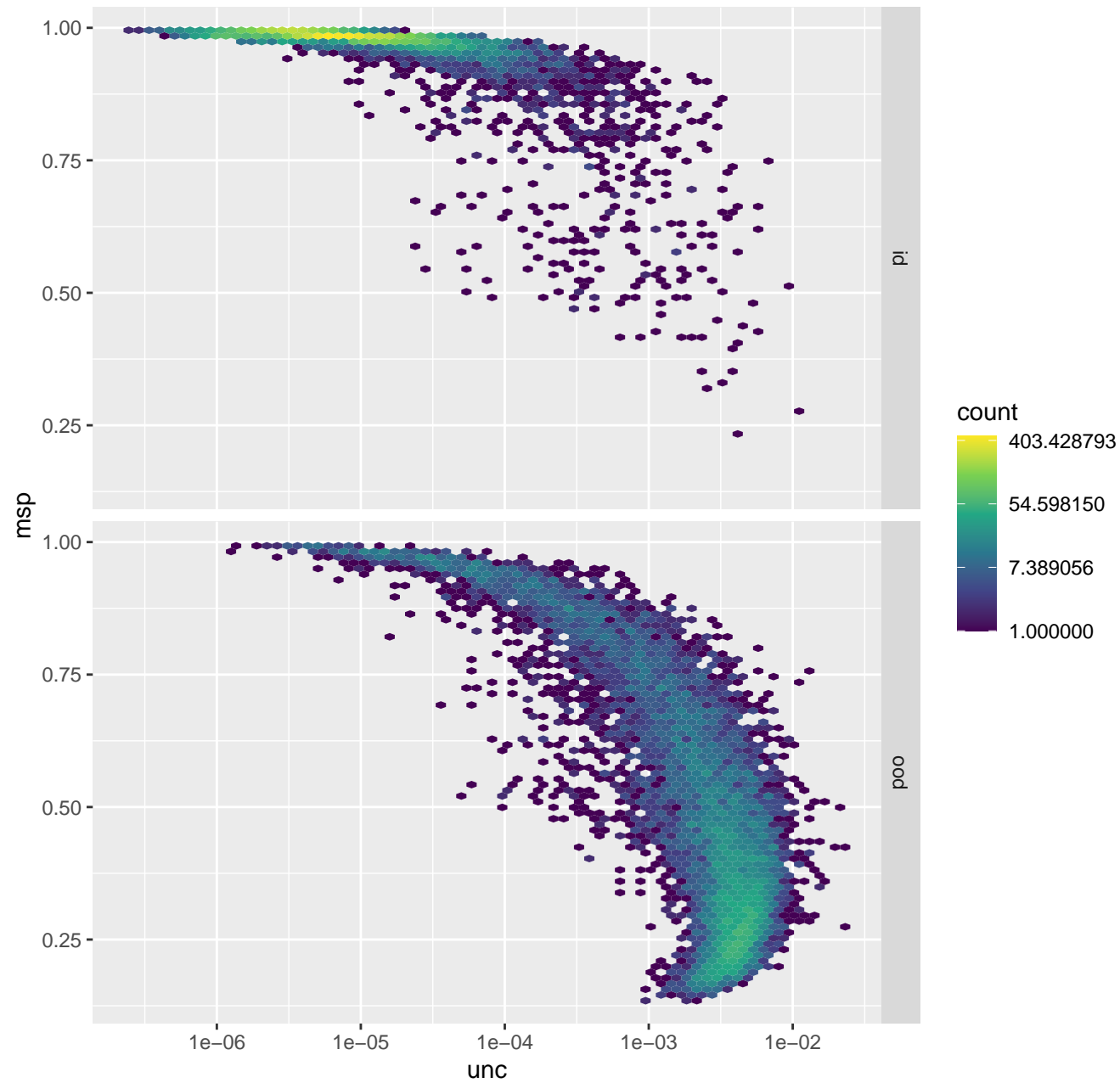
B16+R101x3



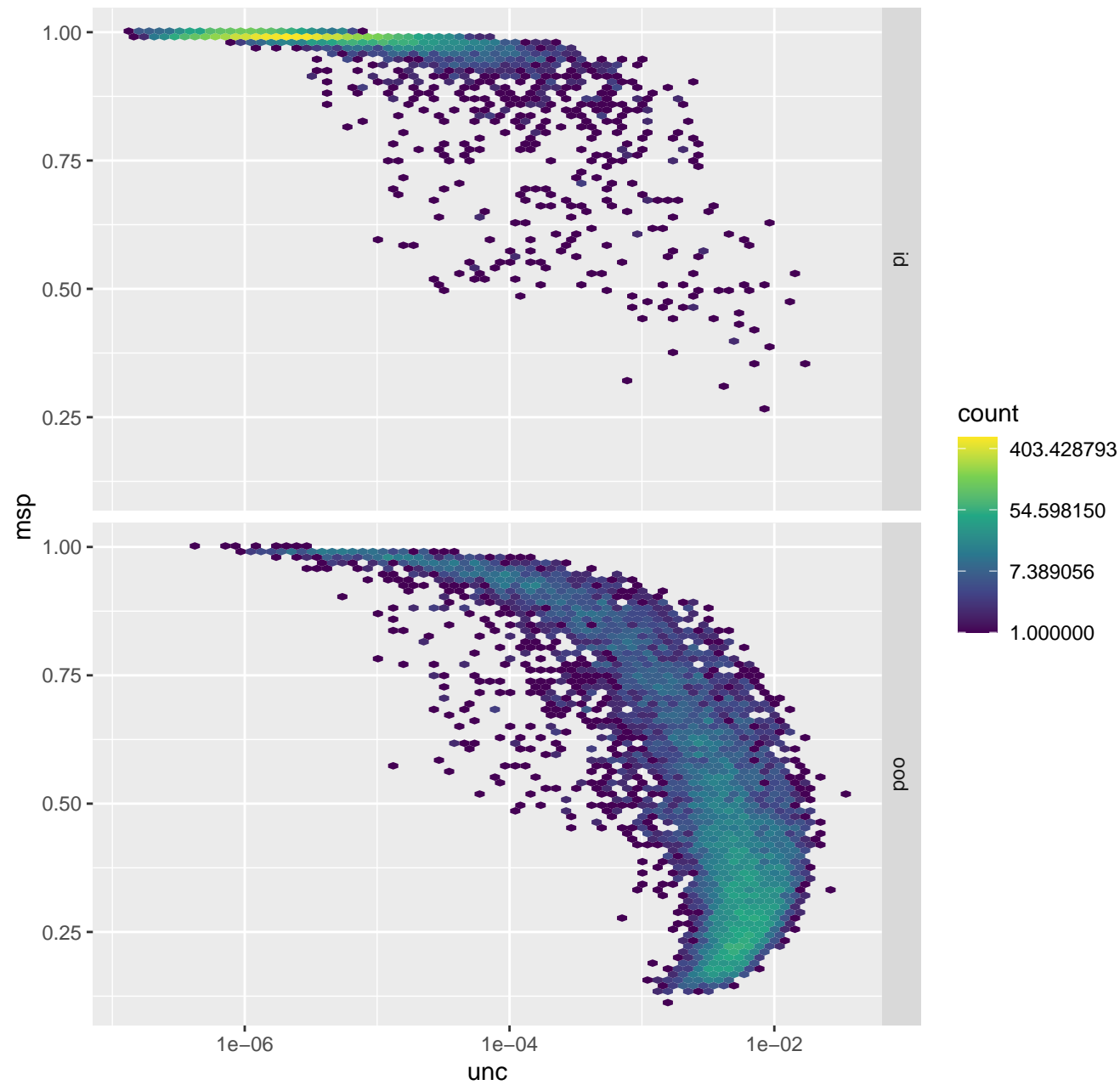
B16+R101x3+R50\_B16



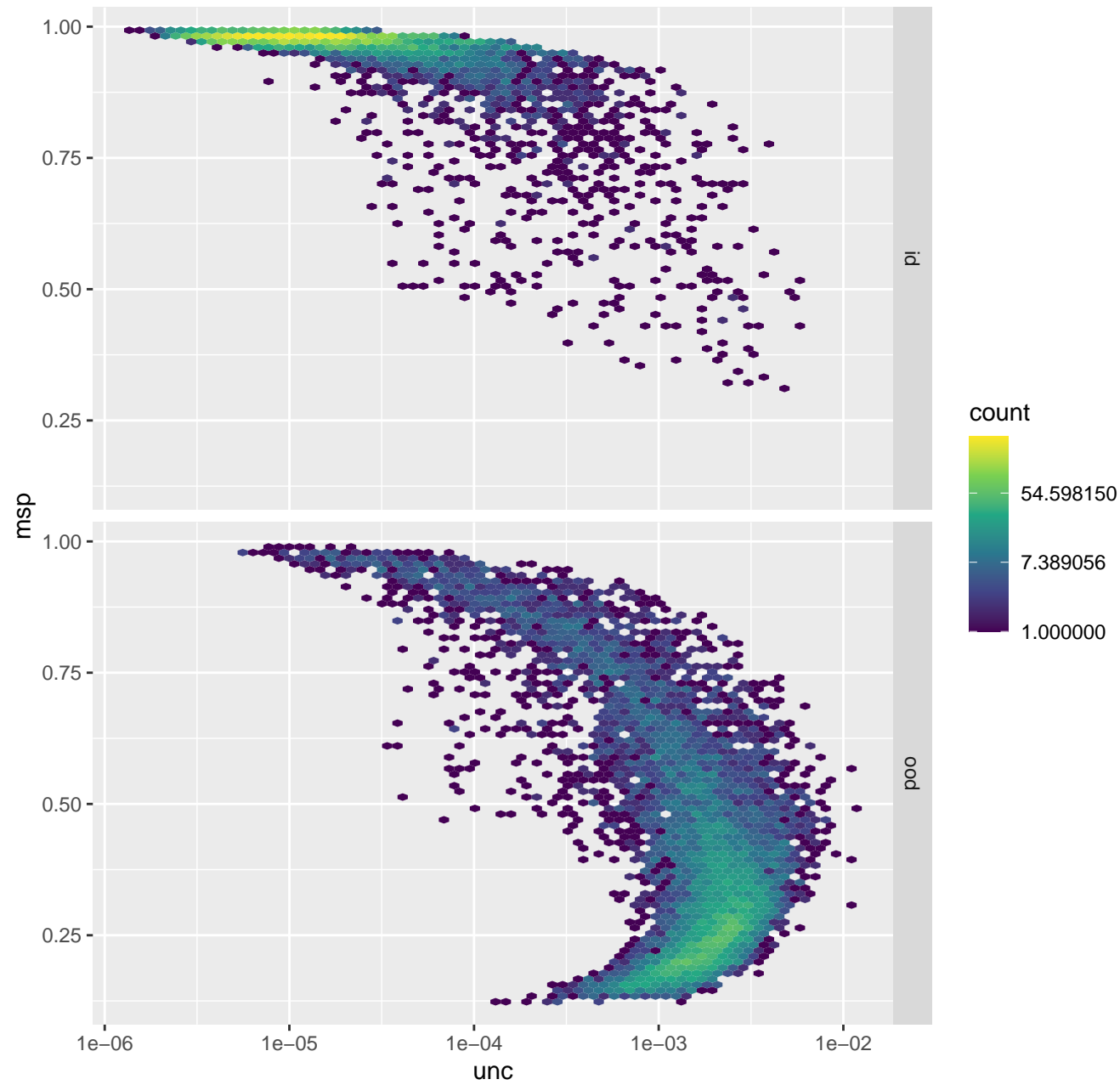
B16+R101x3+R50x1



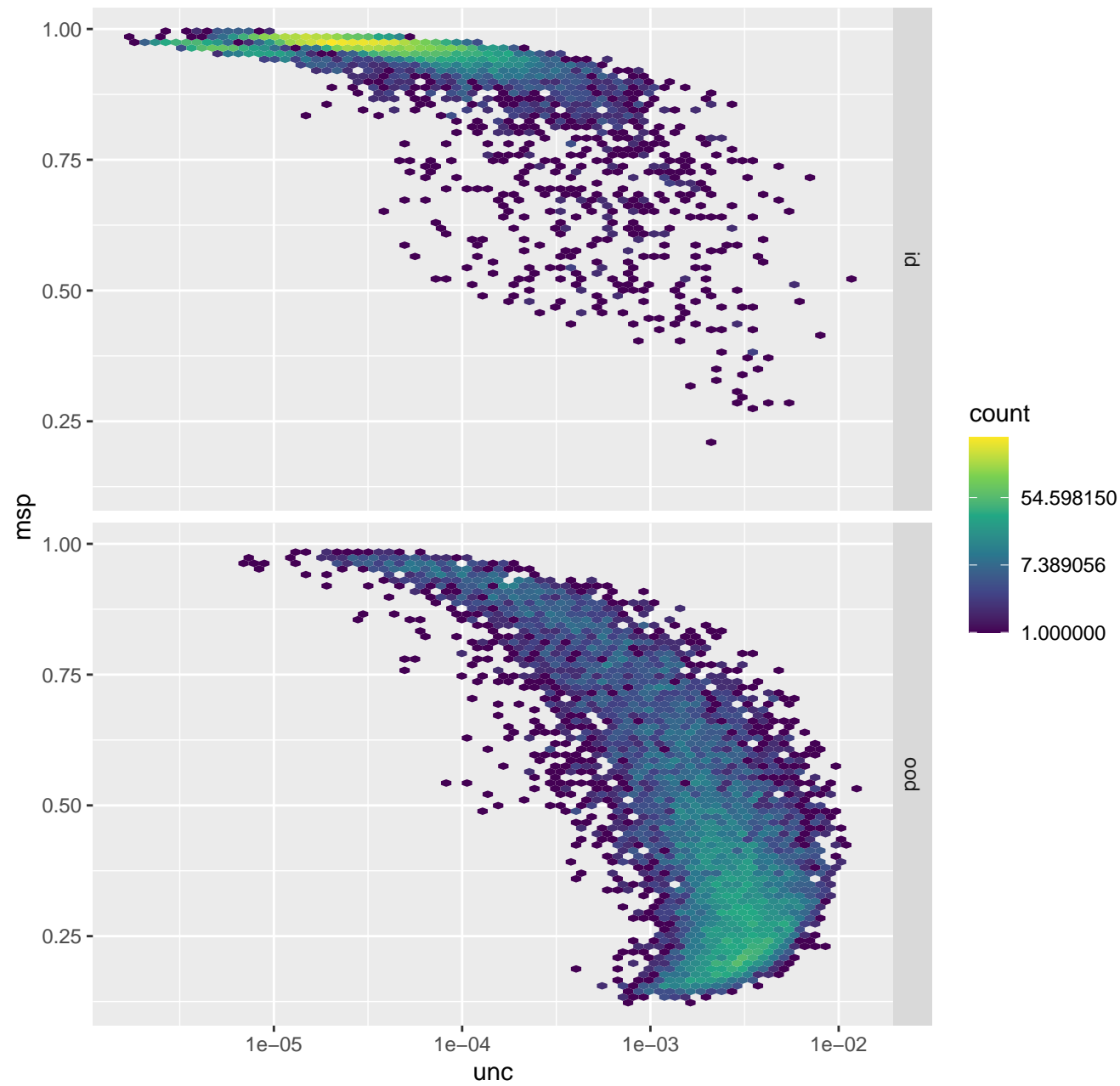
B16+R101x3+R50x1+R50\_B16



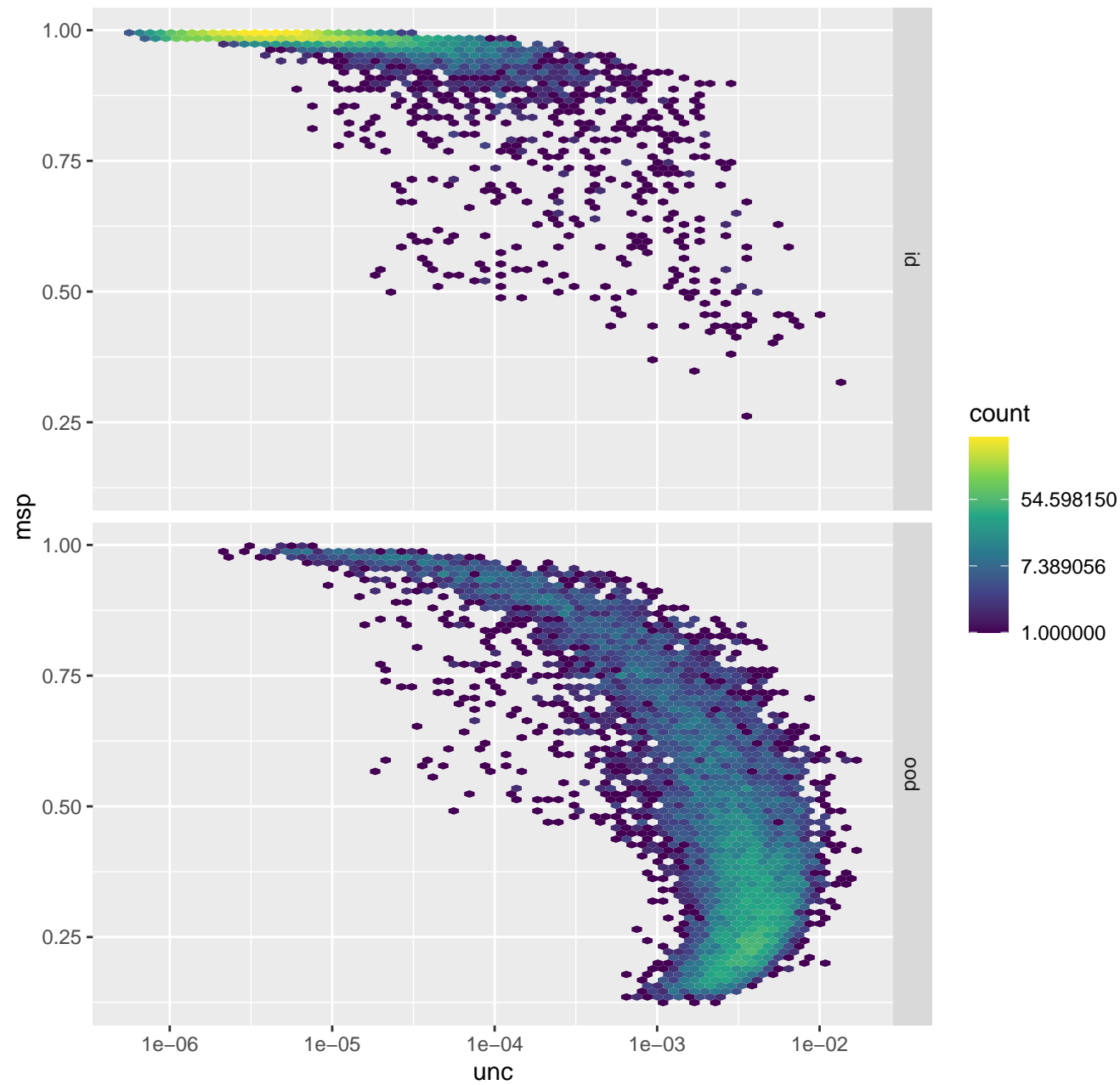
B16+R50\_B16



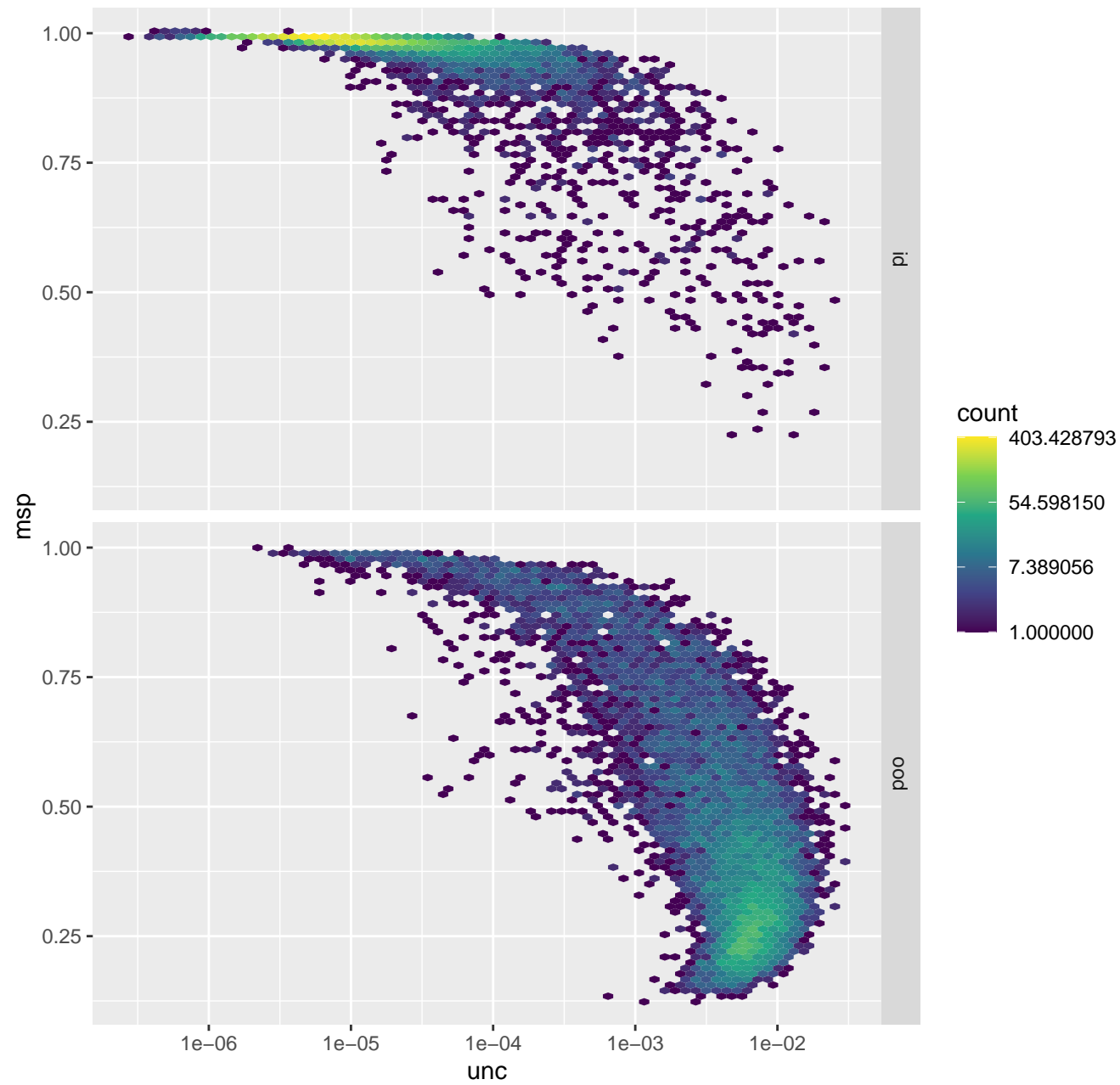
B16+R50x1



B16+R50x1+R50\_B16

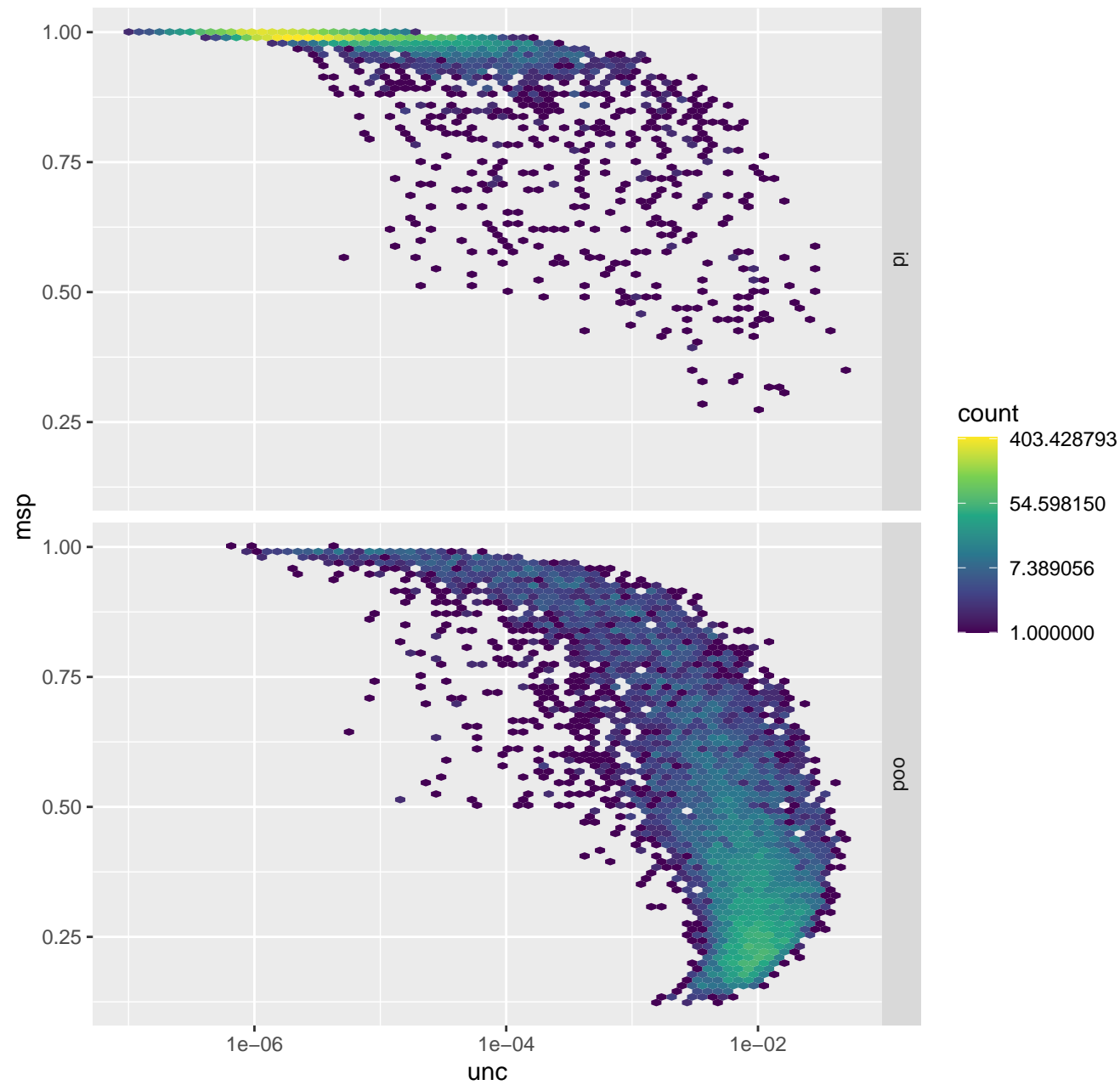


M\_B16+R101x3

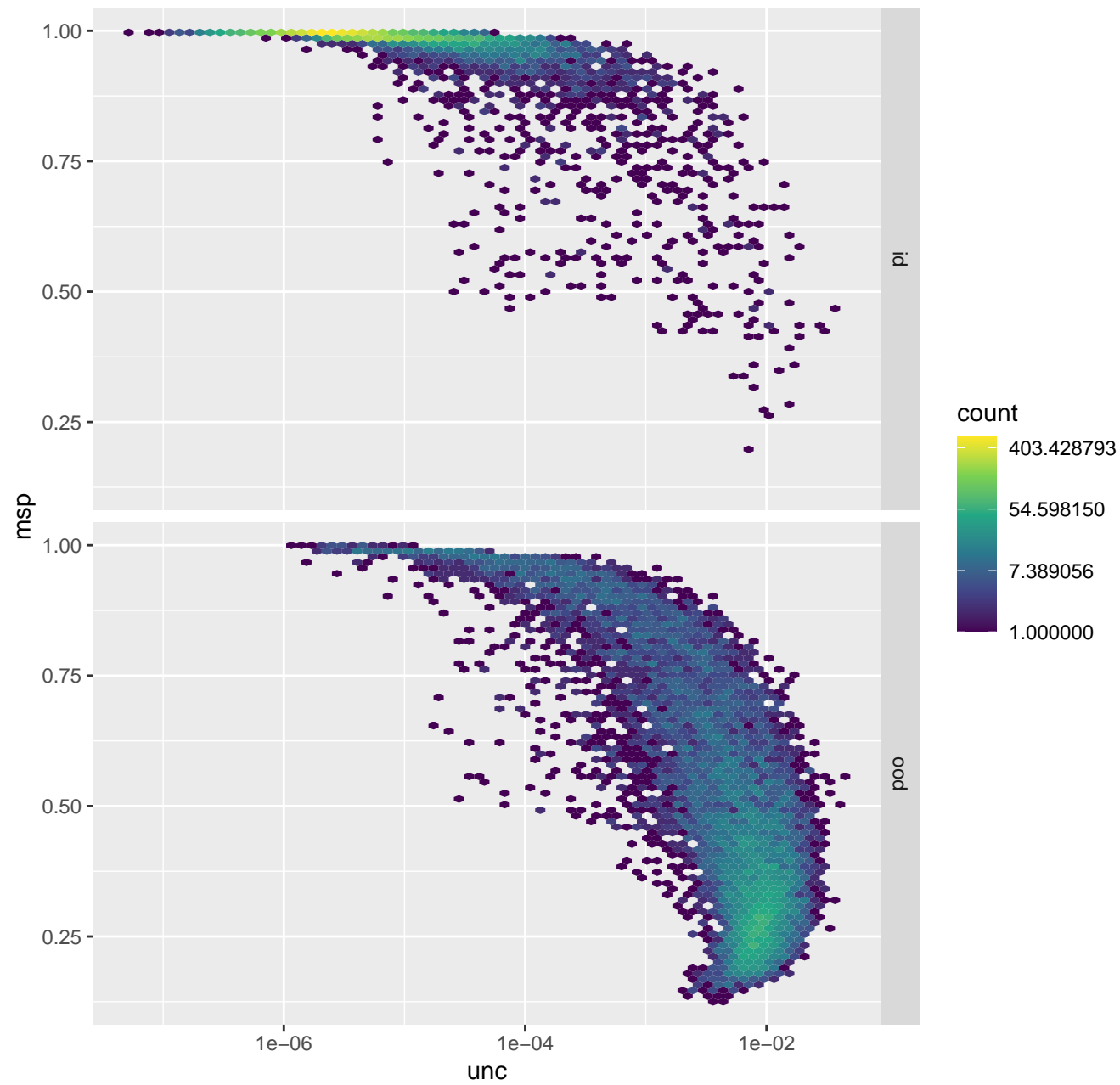




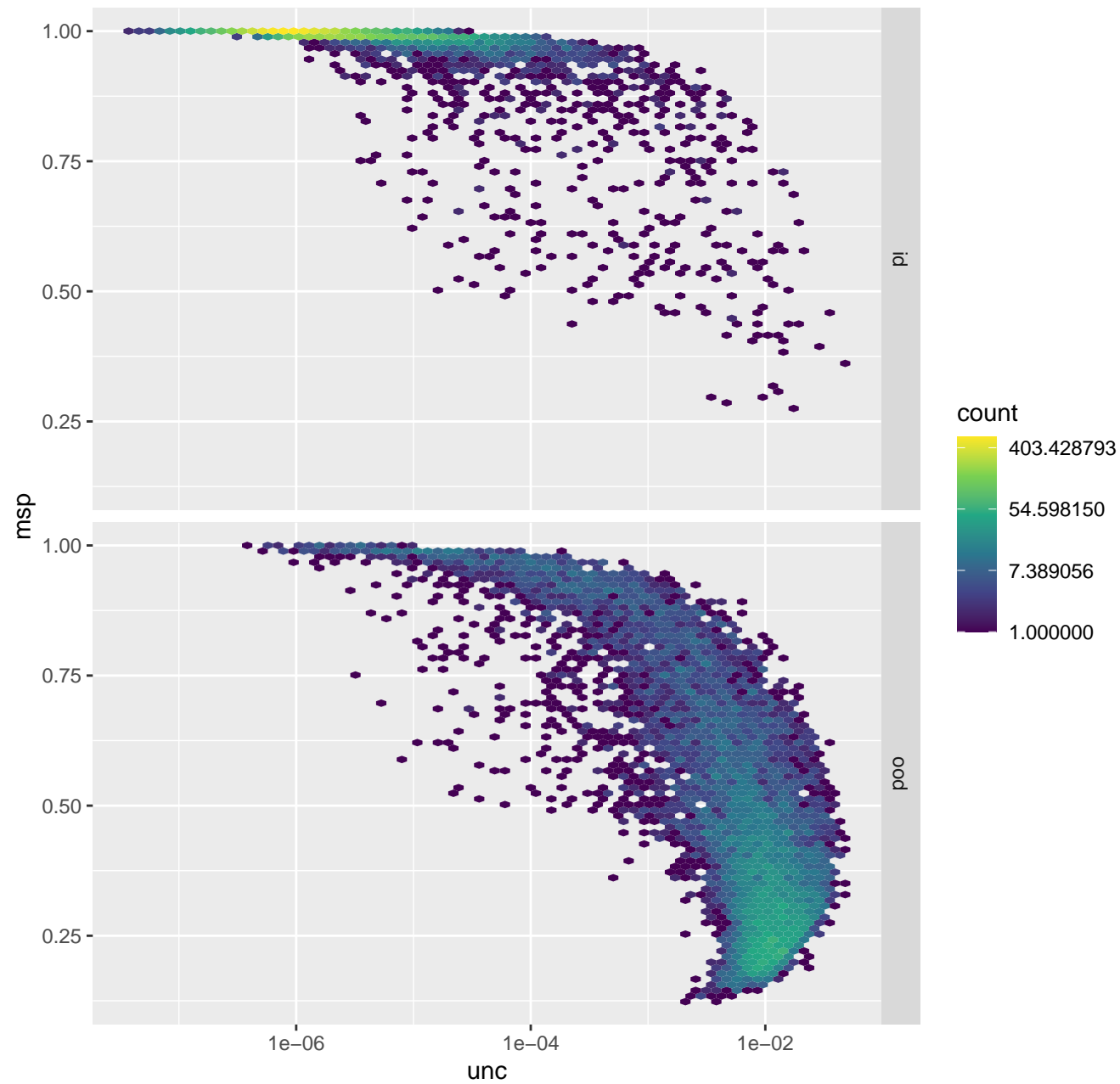
M\_B16+R101x3+R50\_B16



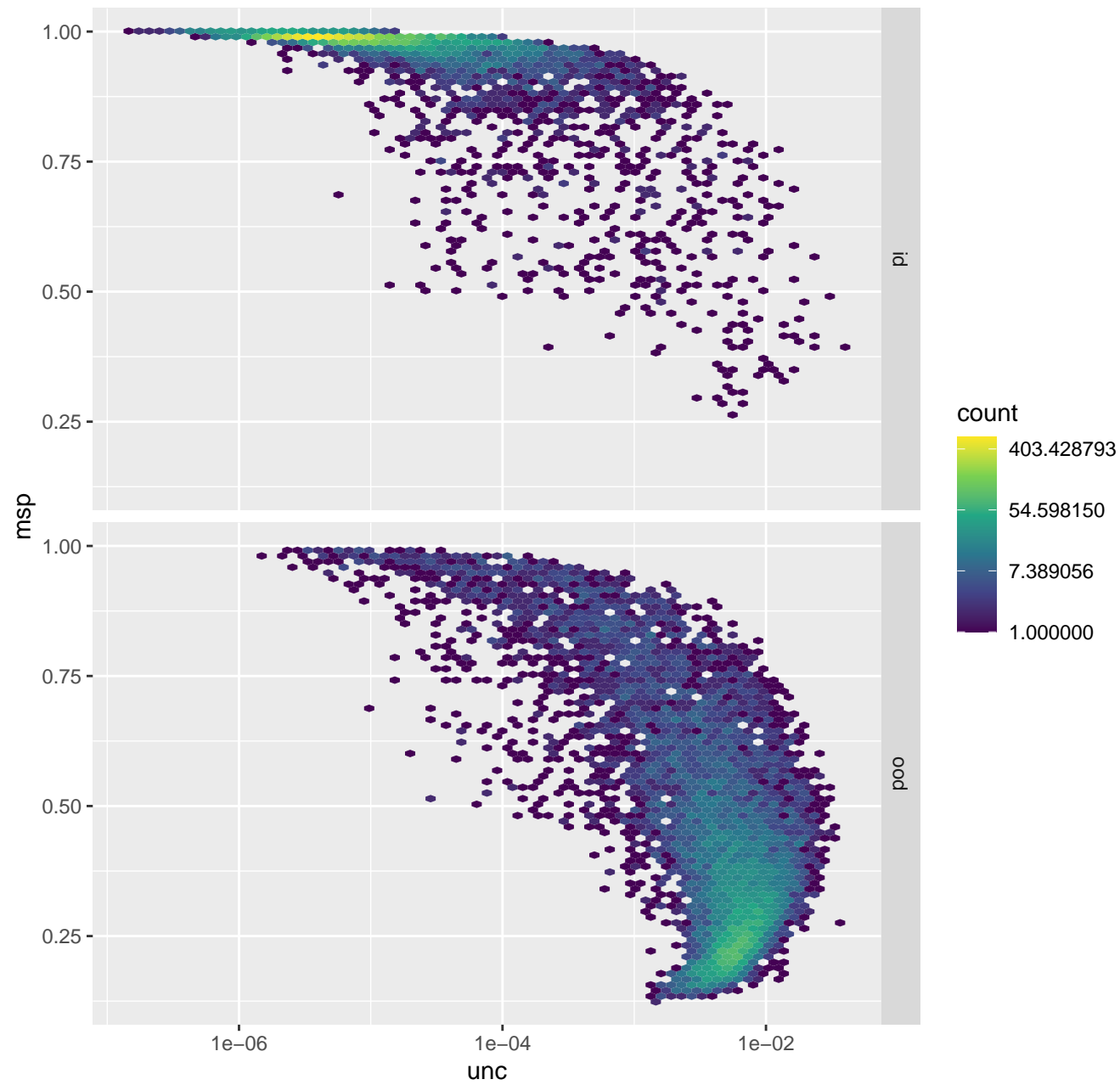
M\_B16+R101x3+R50x1



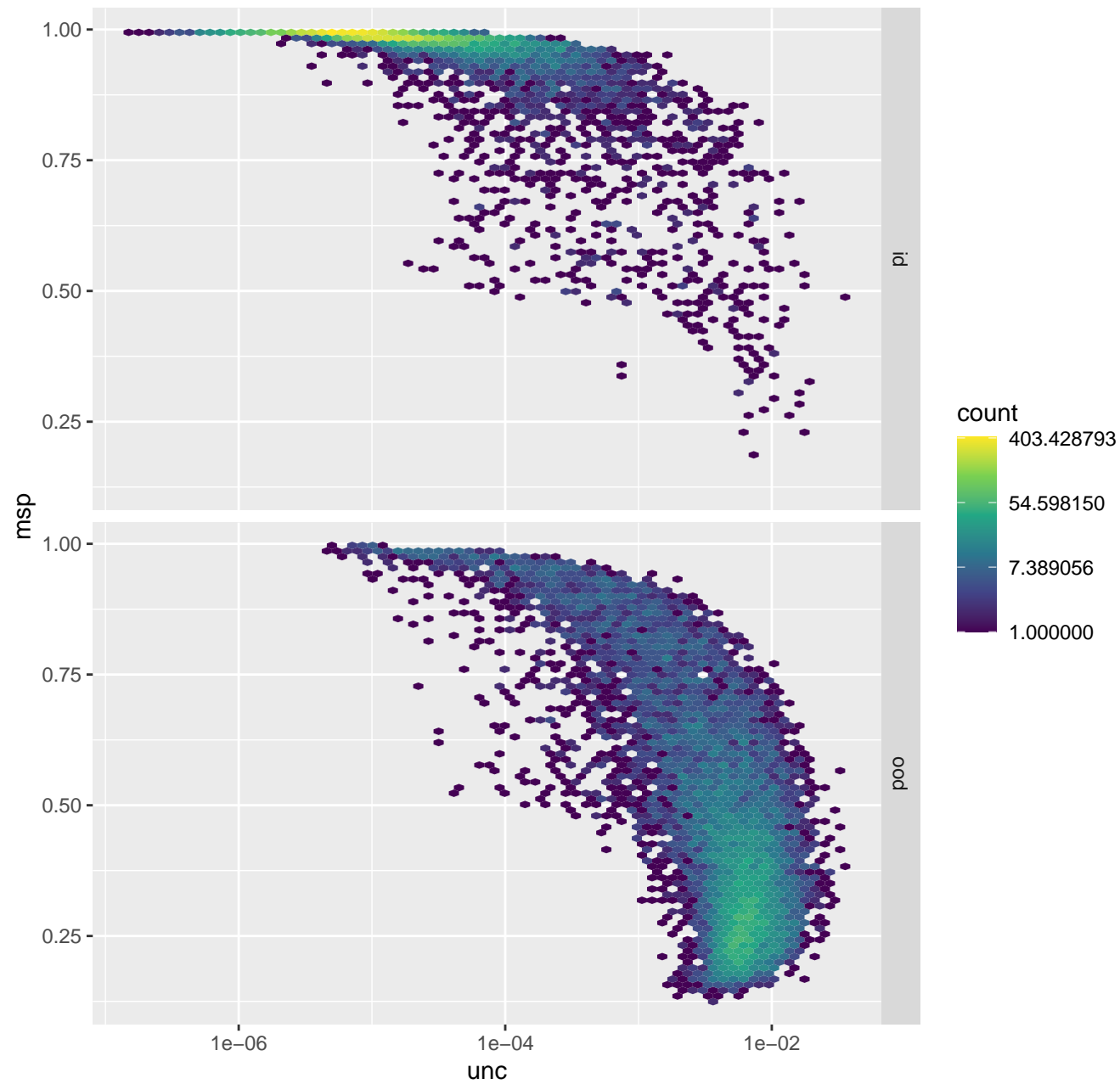
M\_B16+R101x3+R50x1+R50\_B16



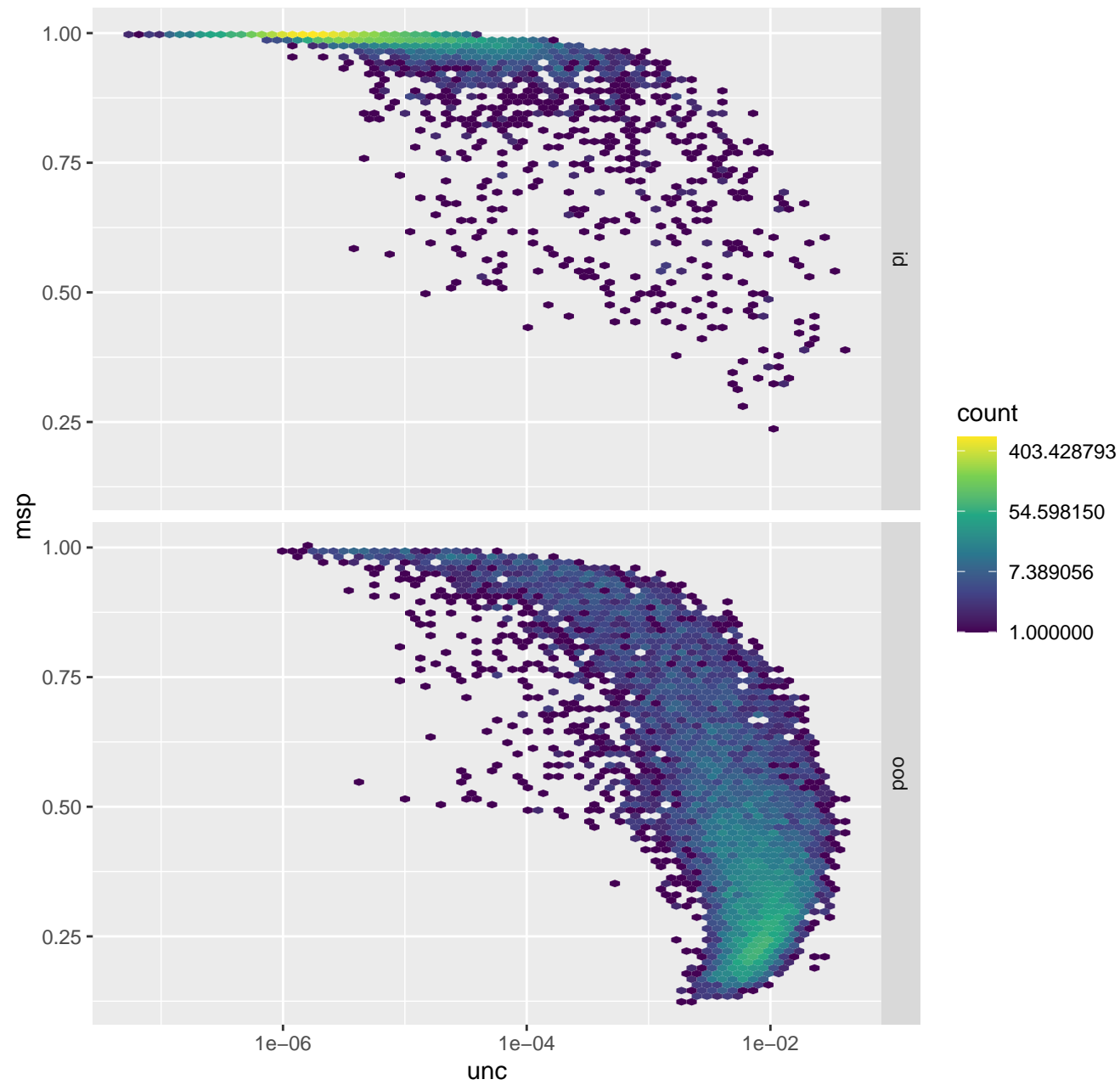
M\_B16+R50\_B16



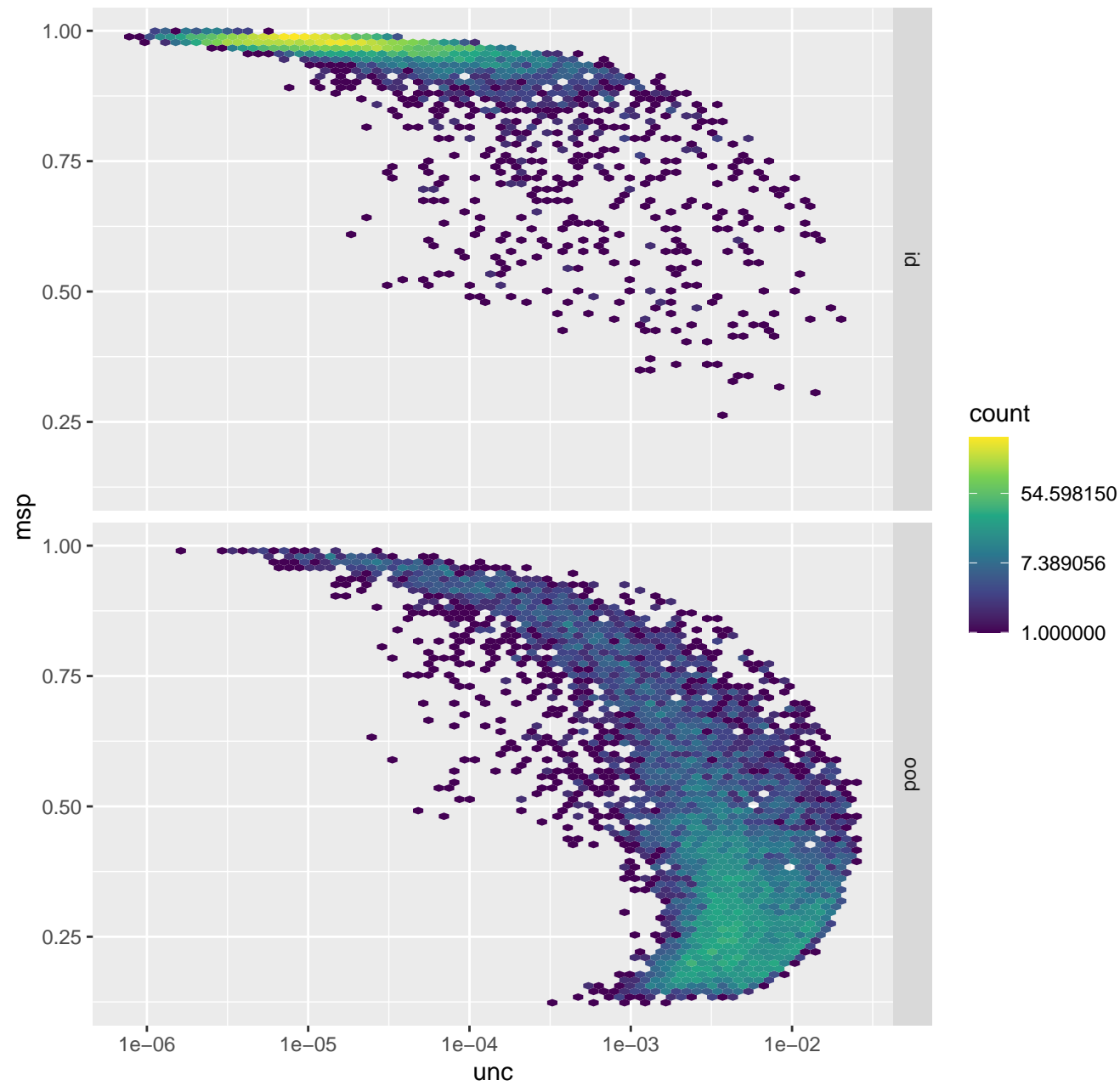
M\_B16+R50x1



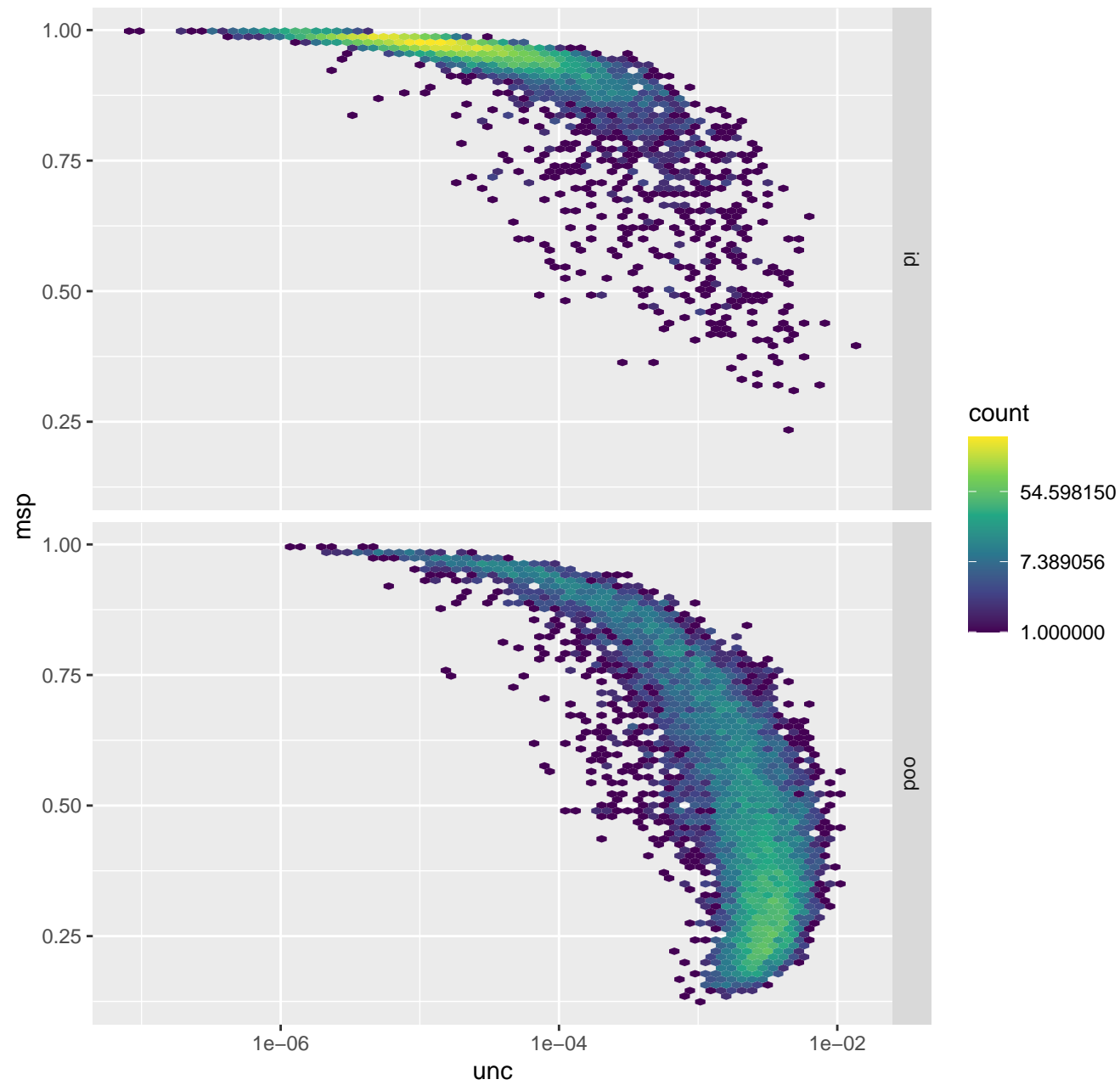
M\_B16+R50x1+R50\_B16



R101x3+R50\_B16

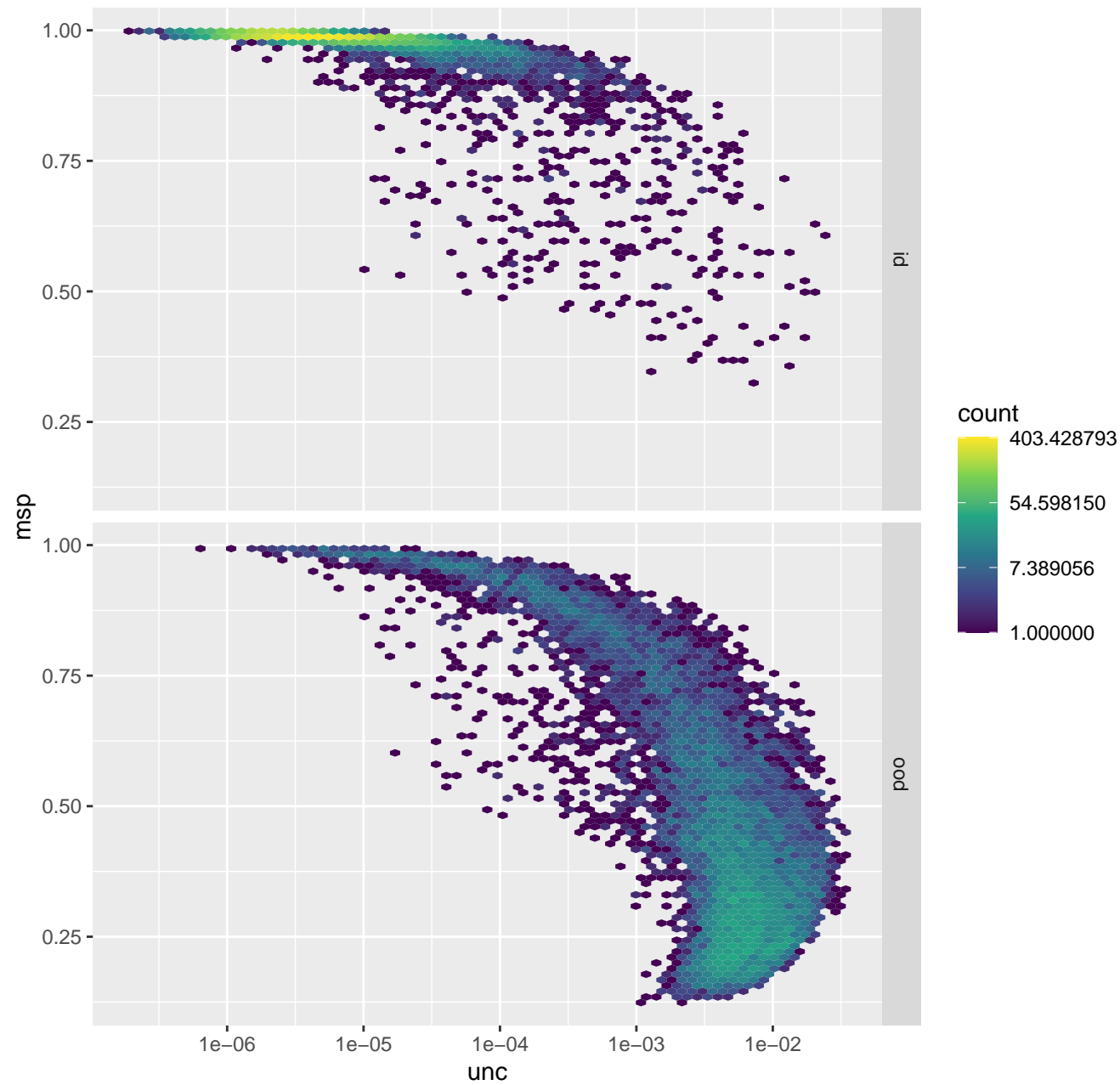


R101x3+R50x1





R101x3+R50x1+R50\_B16



R50x1+R50\_B16

