NOTE: Some LaunchPads & BoosterPacks do not 100% comply with the standard, so please check your specific LaunchPad to ensure pin compatibility. Below are the pins exposed @ the MSP-EXP432P401R BoosterPack connector Also shown are functions that map with the BoosterPack pinout standard. Refer to the MSP432P401R Datasheet for additional details BoosterPack standard | MSP-EXP432P401R Pin map UART RX (-MGU) GPIO Coservec TX (←MCU SCL SYAVI:(0)611 AIO MOSS FOR HICON UCB0CLK UCBISDA MSP-EXP432P401R P2.5 P5.0 **UCBOSOMI** WAS VRIED VERIED CHE Mei Gasason UCB2SDA UCB2SIMO PM MSP-EXP432P401R Pin map | BoosterPack standard UCBOSDA UCBOSCI (I) Denotes I/O pins that are interrupt-capable Some LaunchPads do not have a GPIO here PWM out PWM out SPI CS Display SPI SPI CS Wireless PWM out SPI CS Other PWM out PWM out Timer Capture MOSI MISO GPIO GPIO GPIO GPIO GPIO GPIO **GPIO** GPIO GPIO GPIO CPIO**

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