

NOTES :

Source repository : <https://gitlab.laas.fr/owntech/1leg/-/tree/V1.1.2>

Preliminary PREL, Good For Execution, GFE, As Built ASB
Major revisions are to be described thoroughly when pushed with git push command

Date	Rev.	Creator	Reviewer	Approver	Status
	B				
	A				
	-		Villa		

Title: TWIST V1.2



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File: PowerLeg1.kicad_sch

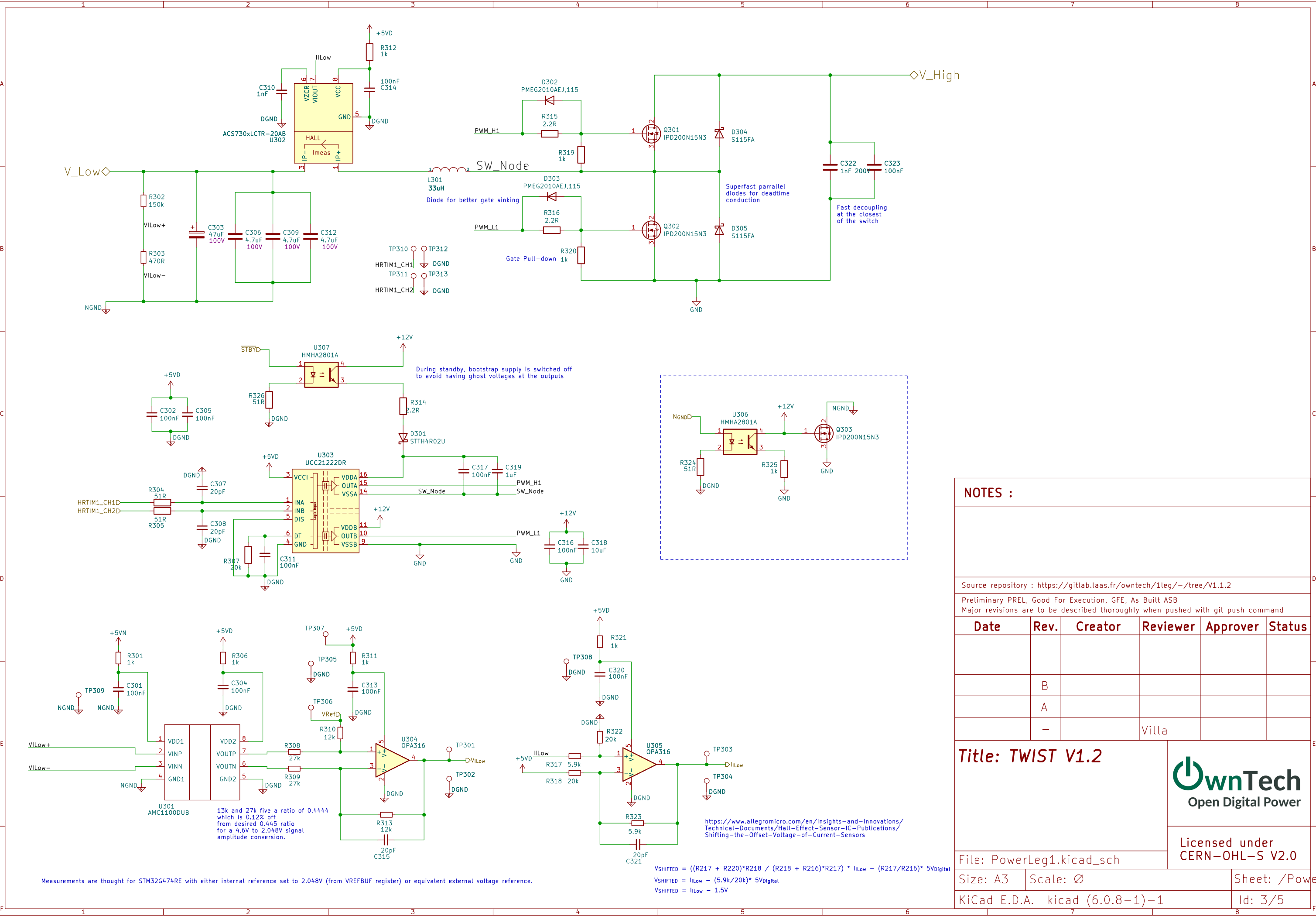
Size: A3

Scale: Ø

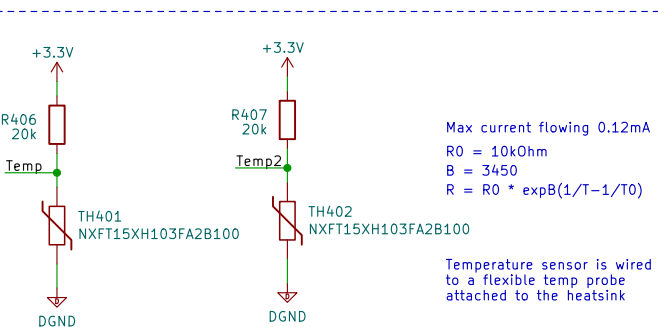
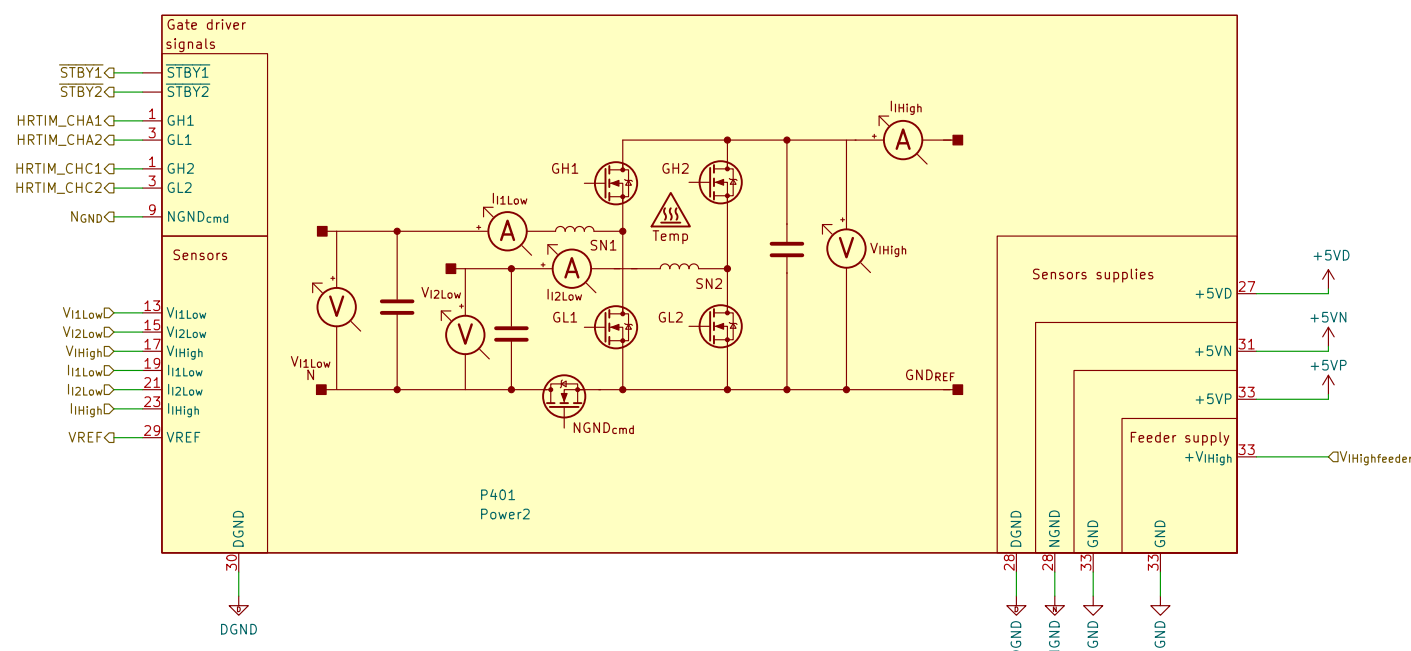
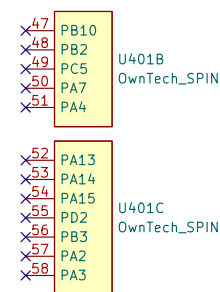
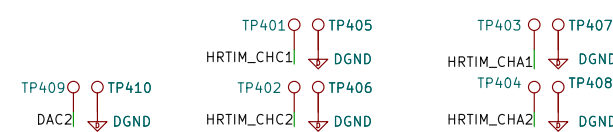
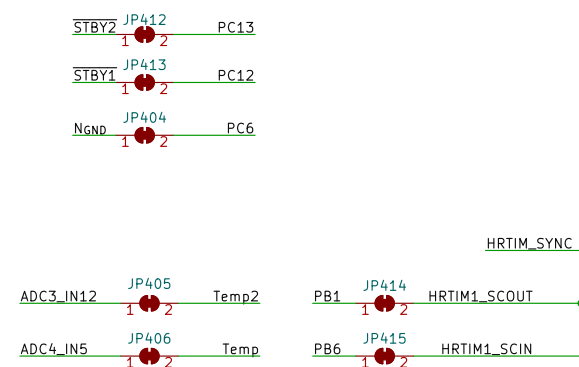
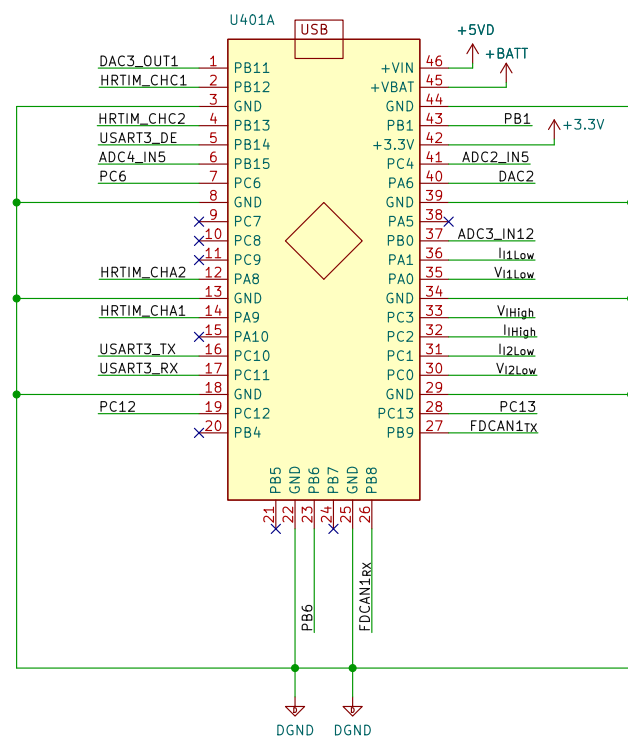
Sheet: /Power Leg

KiCad E.D.A. kicad (6.0.8-1)-1

Id: 2/5



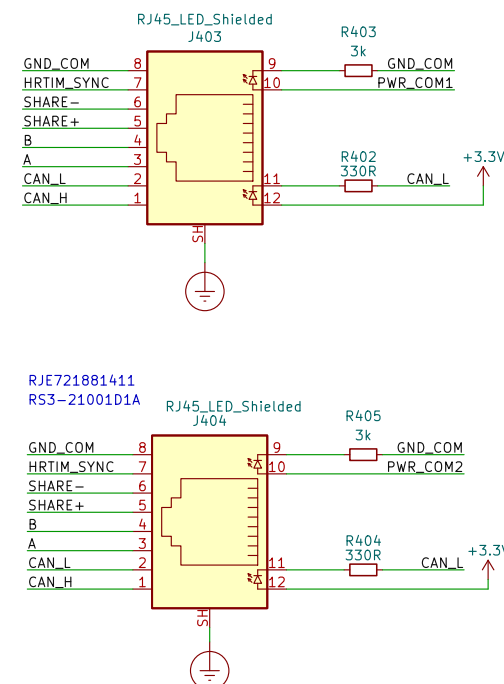
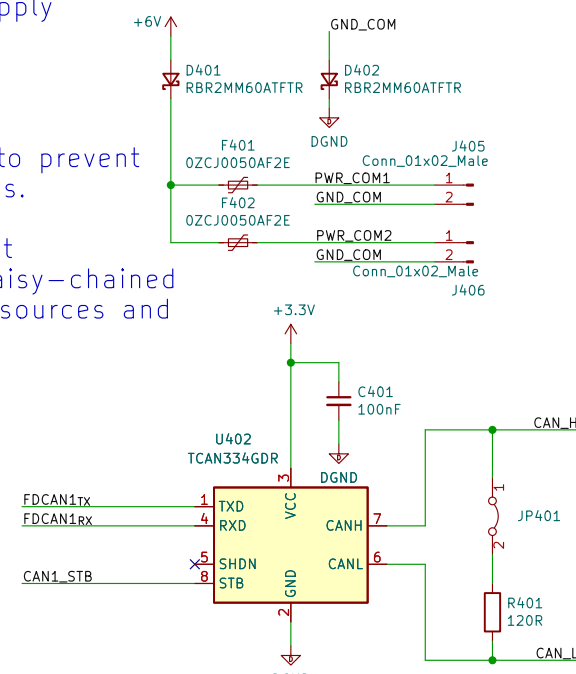
STM32G474RE LQFP64 Pin Mapping



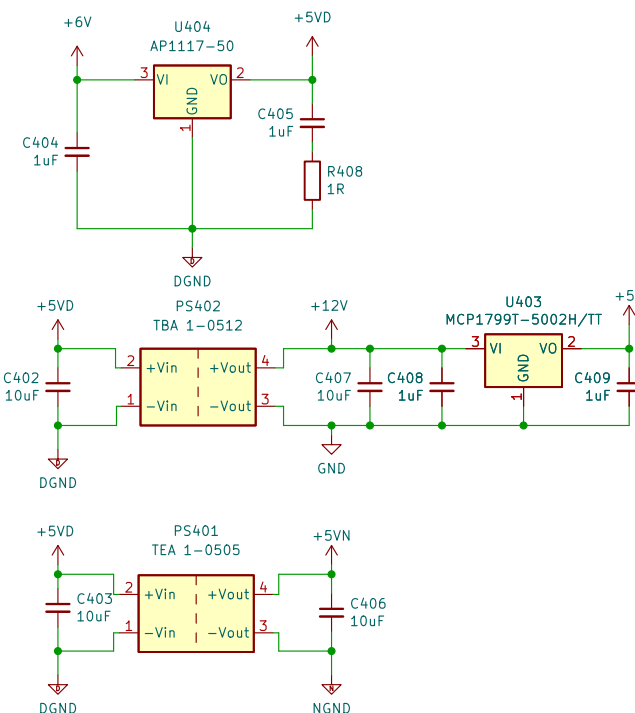
- CAN bus power supply
 - 6V
 - 100mA

Diodes necessary to prevent separate GND loops.

- Polyfuse to prevent over-current in daisy-chained bus with multiple sources and sinks.

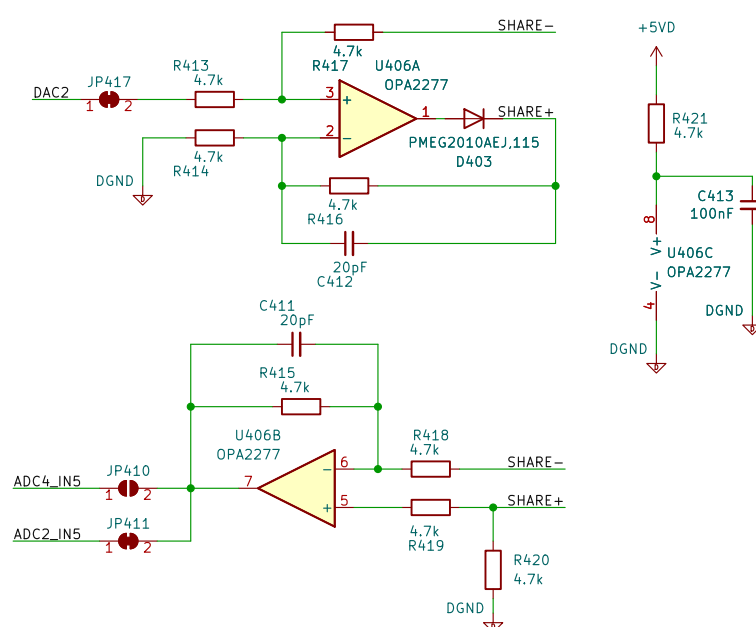
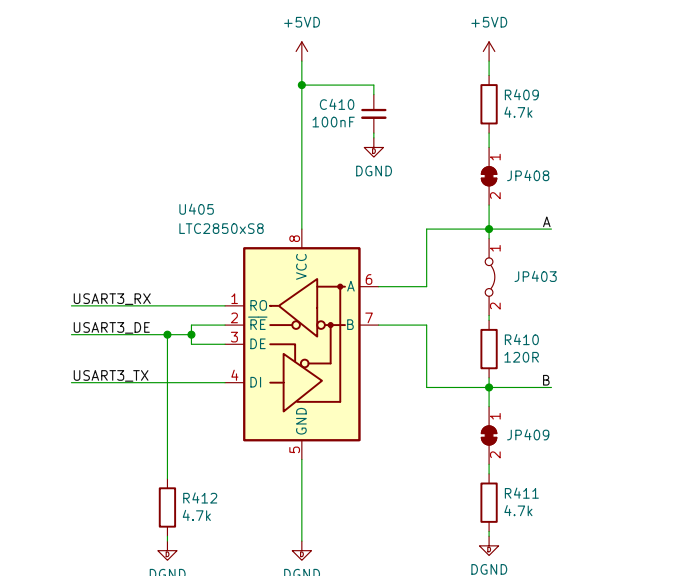


CopyLeft LibreSolar

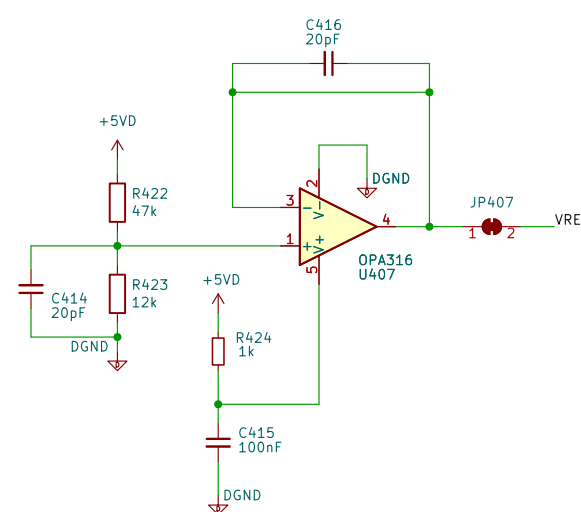


Alternate 1.024V VREF signal from SoC integrated DAC2.

- Conflicts with Analog share bus



Current sharing analog bus. Highest voltage is imposing its voltage to the bus
Simulation available
<https://tinyurl.com/2qg7lcok>



DAC2 can be used to provide a reference if this fails
Rudimentary 1.024V reference for measurement offset
Simulation available
<https://tinyurl.com/mtt5xsmv>

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	B				
	A				
2021-06-04	—	Alinei	Villa	Villa	GFE

Title: TWIST V1.2



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File: Microcontroller.kicad_sch

Size: A2	Scale: Ø
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Scale: Ø

Sheet: /Bottom si

KiCad E.D.A.	kicad (6.0.8-1)-1
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Id: 4/5

