

short suggestion

by Ardin Min

Internal Architecture of 8086 @ 2 ports & figure

16 bit address bus & 20 bit address bus
কাজ কী?

Registers in the 8086 CPU

General purpose of Registers, pointer
index registers / কাজ কী?

Q. the functionality of flag / general purpose
register. / কাজ, Flag → figure

Control bits

most important

Write down any four addressing modes of 8086
[example + figure]

exam Question

Describe the BUS Controller with input and
output signals and connection to 8086 micro-

Signal diagram of 8086 / common signal of

Vaptan 15 8086 description

internal registers 8086 / internal block of 8086

8086 program to move a block of data from one segment to another [find using string from google]

types of processor

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pentium
80486

Solution: Differences

	80486	pentium
data bus (bits)	32	64
Address " "	32	32
Operation speeds (MHz)	25-50	50-100
Memory capacity	4GB	4GB
Memory management	Internal	Internal
Pc type (IBM)	PC-AT	PC AT
Math co processor	Internal	Internal
Introduction	1989	1993

Control Register.

Lecture - 2

Dual mode operation 8086

Pin diagram

Multiplex of data and address line is 8086 [less important]

minimum maximum mode

most important
marks

① Interrupts, types, what do you mean by interrupt vector table, Different functionalities of various interrupt

Address Bank [even / odd]

fig / description DMA / process

previous year Question

pin diagram of 8086

needs DMA / basic working principle of DMA
maximum and minimum modes of 8086 with respect to 8088

odd and even Address Bank / uses of promisor approach.

what is interrupt, need interrupt in 8086 /
Various types of interrupt in 8086 with exam
interrupt vector micro processor / execution
way of interrupt in 8086 with diagram

Lecture-4

Flag Register

overflow \rightarrow unsigned / signed overflow

Instruction affect \rightarrow example

previous year Question

Comparative

analysis of signed/unsigned overflow

Flow Register.

instruction

affect math