

Execution Performance Tester User Guide
for Speculative Approach
to Clipping Line Segments Algorithm

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1 Measurement Environment Overview

The scope of the present execution performance tester (EPT) is an FPGA NIOS II based embedded design for measuring the execution performance of the speculative approach to clipping line segments algorithm. The system complies with the following requirements listed below:

Execution-sensitive parameters: with proper usage of these values it can be guaranteed that the CPU processes the code snippet in 100%.

- CPU instruction and/or data cache - *not implemented*
- Branch prediction - *not implemented*
- Interrupt requests - *disabling all IRQ input lines at measurement*

Cycle counter: that module is similar to the Intel's timestamp cycle counter (TSC) solution; an embedded n-bit counter counts the clock cycles (this module is driven by the same clock source as the CPU). Its value can be read via Avalon or NIOS II custom instruction interface. The counter reset is available only via Avalon bus.

Basic test procedure in pseudo code:

1. Disable all IRQ
2. Calibrate counter: *resetCounter; getCounter(timeStamp0); getCounter(timeStamp1) → offset = timeStamp1 - timeStamp0*
3. Measuring: *resetCounter; getCounter(timeStamp0); [...algorithmToBeMeasured...| getCounter(timeStamp1);*
4. Evaluate the result: *elapsed = timeStamp1 - timeStamp0 - offset; timeMicroSec = elapsed/(systemClock/1000000)*
5. Enable all IRQ

Test Procedure Environment:

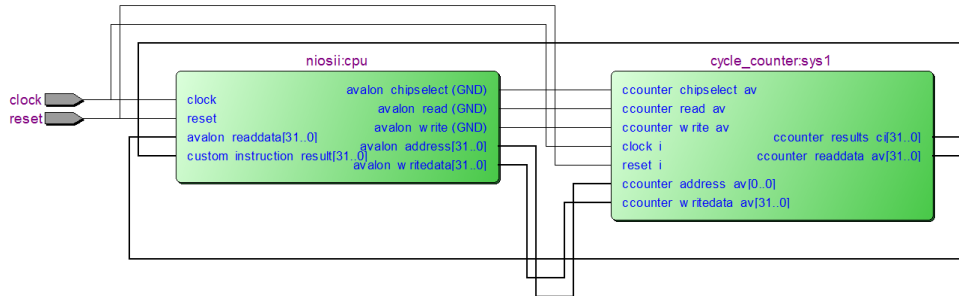
- Run N times the basic procedure and calculate the measurement accuracy → *tolerance = max - min; accuracy = $\frac{resultAverage - tolerance}{resultAverage} \cdot 100\%$*
- The speculative line clipper algorithm contains 11 segments, so a constant test vector array is created based on these parts. The test vectors contain the input arguments for the line clipper functions.
- At performance measurement the basic procedure is executed N times in each test vector. Finally a simple report generator displays the result.

2 Hardware Architecture

It consists of the following parts:

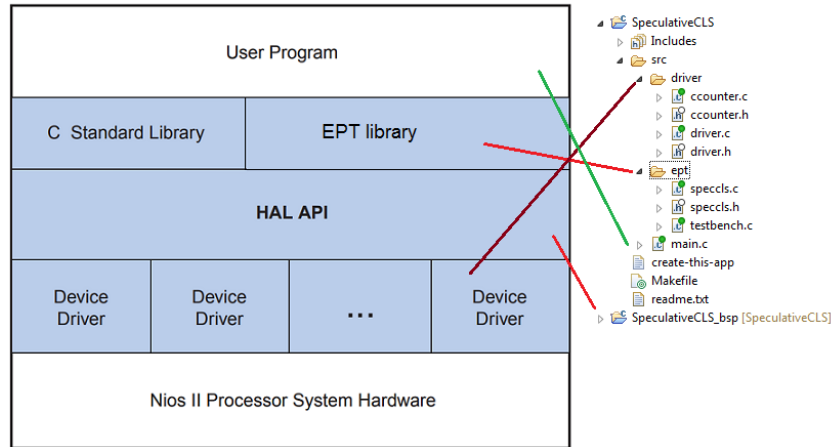
- Clock source: $f_{sys} = 50MHz$
- NIOS II CPU economy core
- Floating point arithmetic and logical custom instruction
- Cycle counter with Avalon and CI interface
- 200 KB ON-chip RAM (minimizing the latency and maximizing the accuracy - SDRAM is not suitable for this)
- JTAG UART interface
- SystemID peripheral

N-bit Cycle counter: The main question is how to determine the value of the N? Should it be implemented as a Byte, a Word, a DWord or may be a QWord counter? Intel TSC uses 64-bit solution but this is overkill for that kind of measurement. Let consider the DWord design: at 50 MHz frequency the maximum execution duration can be $t_{max} = \frac{2^{32}-1}{50 \cdot 10^{-6}} \approx 85.9sec$ without overflow. It should be enough if reset is inserted before each measurement starts.



Cycle counter's implementation

3 Software Architecture



The software architecture

User Program: In this case just the main function.

```

1 int main()
2 {
3     specClsResult_t resultVector[getTestVectorSize()];
4     alt_irq_context context;
5
6     //—Measurement Environment—
7     context = alt_irq_disable_all();
8
9     // Run the measurement based on the Test Vector constants
10    runVectorMeasurement(resultVector);
11
12    alt_irq_enable_all(context);
13    //—Measurement Environment—
14
15    // Report and validation
16    generateReport(resultVector);
17
18    return 0;
19 }

```

1. *Line 7.:* Disable all IRQ
2. *Line 10.:* Execute the vector measurement which wraps the speculative line clipper algorithm. A single point test is being executed 10 times for calculating the measurement accuracy.
3. *Line 12.:* Enable all IRQ
4. *Line 16.:* Generate report from the result vector.

The Test Vector Constant: Contains the input arguments for the clipper.

```
1 // Speculative CLS data type
2 typedef struct specClsData
3 {
4     float x1;
5     float y1;
6     float x2;
7     float y2;
8     float xL;
9     float yB;
10    float xR;
11    float yT;
12    char segment[SEGMENT.CHARLENGTH];
13 } specClsData_t;
14 // Specify vectors for testbench
15 // @ modify the test based on data requirement
16 const specClsData_t testVector[] =
17 {
18     // x1    y1    x2    y2    xL    yB    xR    yT    Segment
19     { 50, 250, 60, 260, 100, 100, 200, 200, "Top-left corner"},
20     { 50, 150, 60, 160, 100, 100, 200, 200, "Left-edge region"},
21     { 50, 50, 60, 60, 100, 100, 200, 200, "Bottom-left corner"},
22     {150, 250, 160, 260, 100, 100, 200, 200, "Top-edge region"},
23     {150, 150, 160, 160, 100, 100, 200, 200, "Window region"},
24     {150, 50, 160, 60, 100, 100, 200, 200, "Bottom-edge region"},
25     {250, 250, 260, 260, 100, 100, 200, 200, "Top-right corner"},
26     {250, 150, 260, 160, 100, 100, 200, 200, "Right-edge region"},
27     {250, 50, 260, 60, 100, 100, 200, 200, "Bottom-right corner"},
28     { 50, 150, 110, 160, 100, 100, 200, 200, "Left-edge region, k=1, C&T wins"},
29     { 90, 190, 110, 240, 100, 100, 200, 200, "Left-edge region, k=2, C&T loses"}
30 };
```

Cycle Measurement: The clipper algorithm itself contains the assembly macros for getting the counter values.

```

1 // Cycle counter data type
2 typedef struct ccounter
3 {
4     alt_u32 timeStamp0;
5     alt_u32 timeStamp1;
6     alt_u32 offset;
7     alt_u32 elapsed;
8 } ccounter_t;
9 //Speculative Algorithm to Clipping Line Segments
10 ccounter_t clip(float x1, float y1, float x2, float y2, float xL, float yB,
11                float xR, float yT)
12 {
13     ccounter_t ccounter;
14     float x;
15
16     // Calibrating the cycle counter
17     ccounter = ccounterCalibration();
18
19     // Start the measurement
20     CCOUNTER_reset; // Reset the cycle counter
21     ccounter.timeStamp0 = CCOUNTER_getValueCI; // Get cycle counter's value via
22     custom instruction macro
23     // The speculative algorithm
24     if (x1 < xL)
25     {
26         if (x2 < xL)
27         {
28             // Stop and evaluate the measurement
29             ccounter.timeStamp1 = CCOUNTER_getValueCI; // Get cycle counter's value
30             via custom instruction macro
31             cycleElapsed(&ccounter); // Calculating the elapsed time
32             return ccounter; // Return with the measured data
33         }
34     }
35     // ... other "else if / else" branches with same cycle counter handling
36 }

```

4 Measurement Results

The report generator prints the measurement results to the NIOS II console including all the 11 segments. The accuracy - based on 10 times test repeating in each segment - values are 100% because of the skipped system related non-deterministic parameters (cache, branch prediction and disabled IRQ's).

1	Measurement Report					
2						
3						
4	Vector	CycleAVG[1]	CycleAVG[%]	TimeAVG[us]	ACC[%]	Segment
5	1.	68	4.878	1.360	100.0	Top-left corner
6	2.	68	4.878	1.360	100.0	Left-edge region
7	3.	68	4.878	1.360	100.0	Bottom-left corner
8	4.	136	9.756	2.720	100.0	Top-edge region
9	5.	408	29.268	8.160	100.0	Window region
10	6.	102	7.317	2.040	100.0	Bottom-edge region
11	7.	136	9.756	2.720	100.0	Top-right corner
12	8.	170	12.195	3.400	100.0	Right-edge region
13	9.	102	7.317	2.040	100.0	Bottom-right corner
14	10.	68	4.878	1.360	100.0	Left-edge region , k
	=1, C&T wins					
15	11.	68	4.878	1.360	100.0	Left-edge region , k
	=2, C&T loses					
16						
17	Total:	1394	100.000	27.880		
18	Average:	126		2.535		
19	Repeated test in each single vector point: 10					
20	System clock: 50 MHz					

Report summary of measurement results