

Introduction to GPU Acceleration



Be Boulder.

View the Slides



https://github.com/ResearchComputing/Intro_GPU_Acceleration





Meet the User Support Team



Layla Freeborn



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Andy Monaghan



Mohal Khandelwal

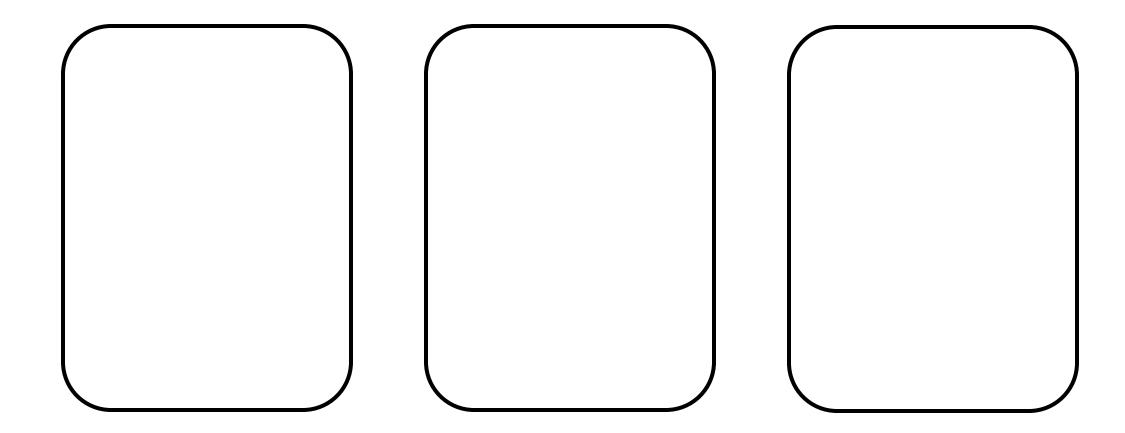


Michael Schneider



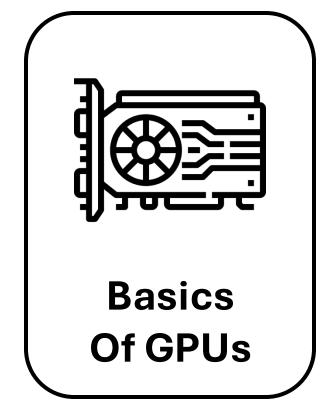
Ragan Lee

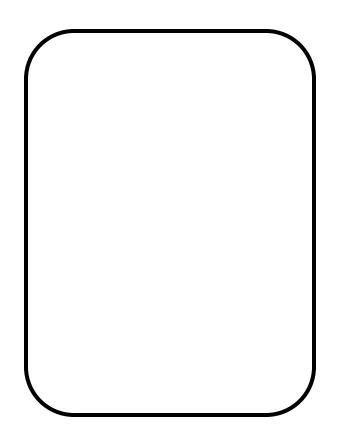


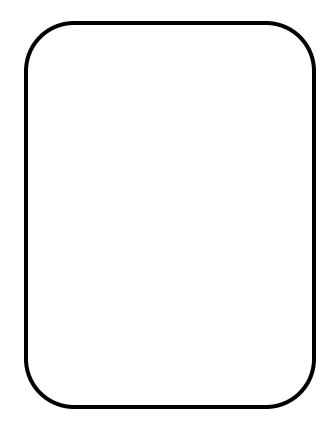


GPU Icon



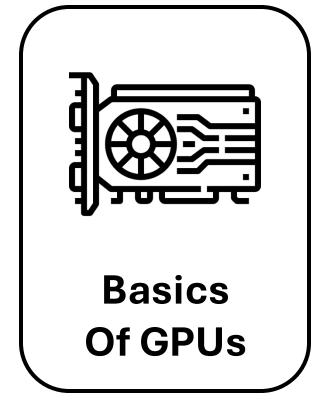


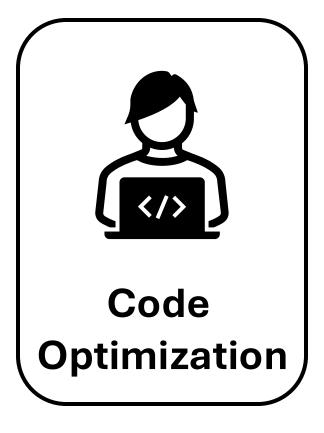


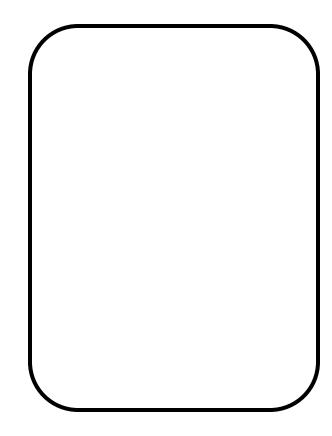


GPU Icon



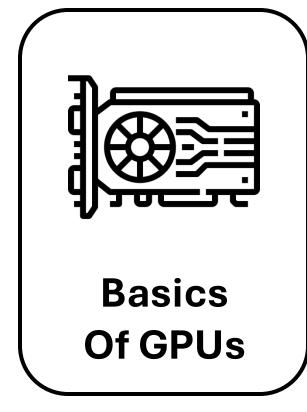


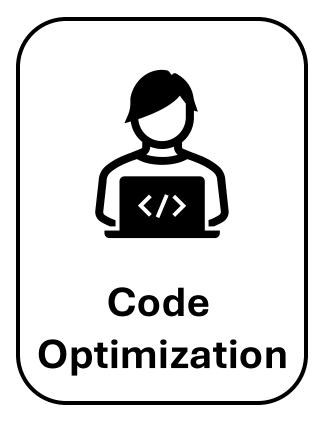


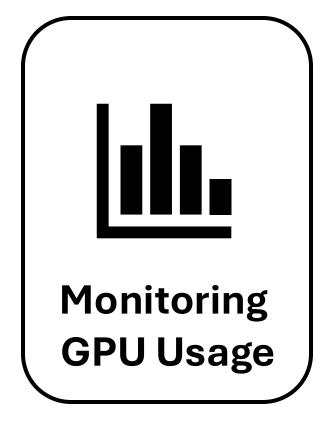


GPU Icon













CPUs vs GPUs



Processing Unit

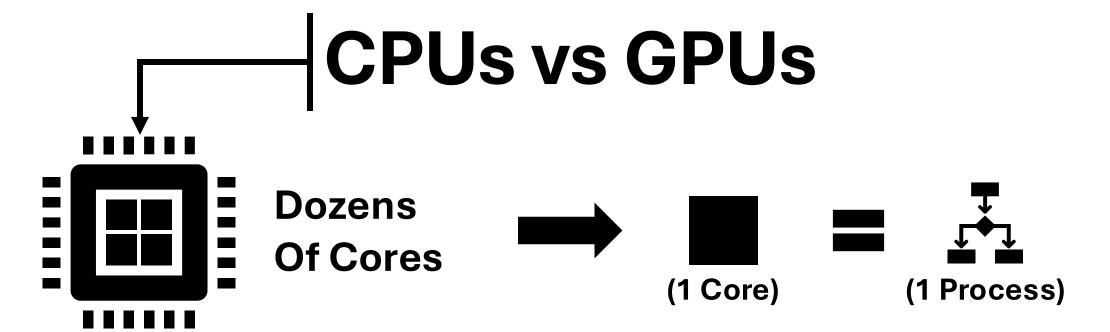
CPUs vs GPUs

Central

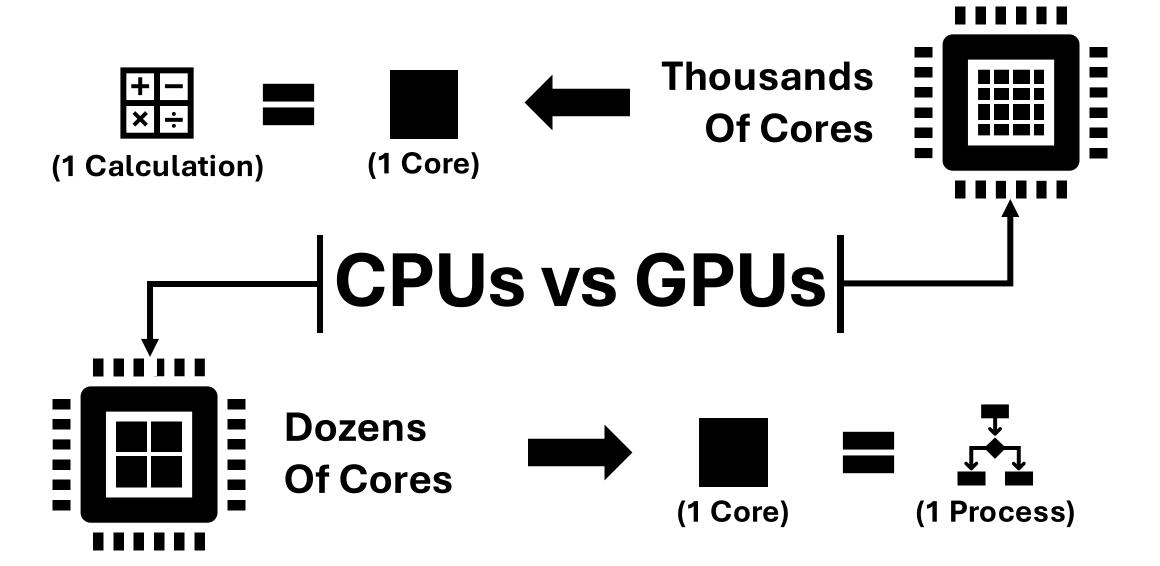
Processing Unit

CPUs vs GPUs

Central Graphics

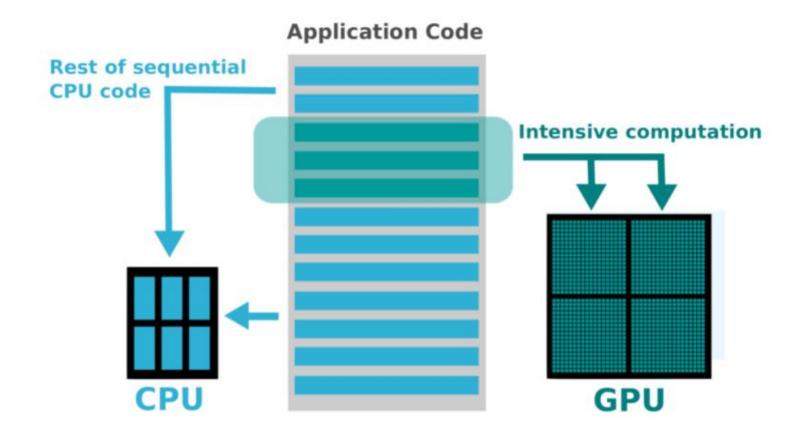








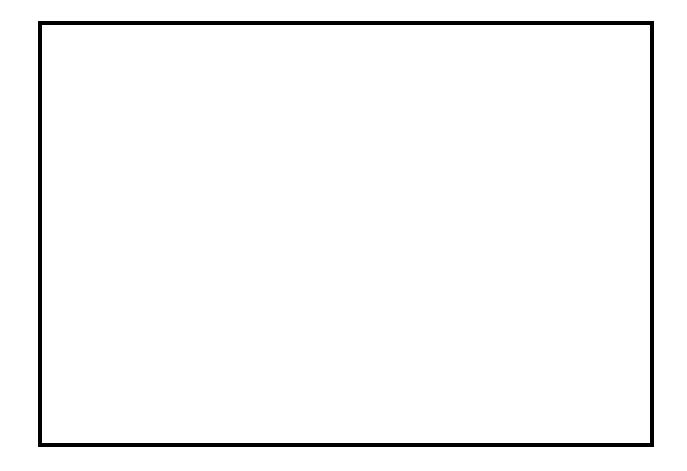
Computational Offloading



Graphic Source







Computational Intensity

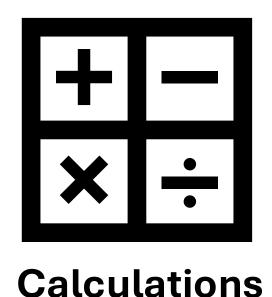
Computational Intensity
Algorithmic Complexity

Computational Intensity
Algorithmic Complexity
Data Type

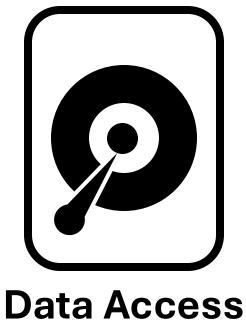
Computational Intensity
Algorithmic Complexity
Data Type
Data Dependency



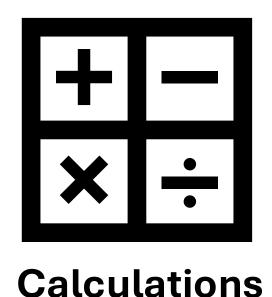
Computational Intensity



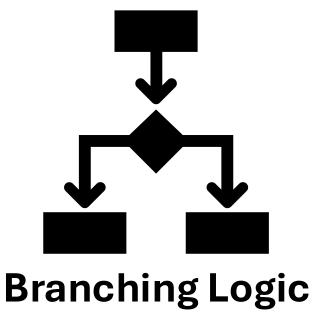




Algorithmic Complexity









Data Type

123

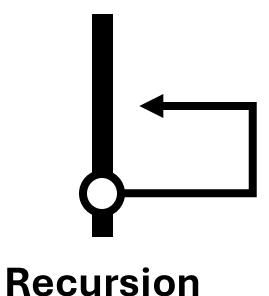
Numeric

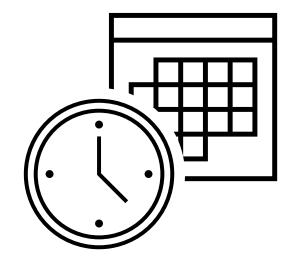
VS

"Text"

Complex Objects

Data Dependency





Temporal Time / Dates

Computational Intensity
Algorithmic Complexity
Data Type
Data Dependency



Alpine GPUs

	NVIDIA			AMD
Type	A100	L40	GH200	MI100
Cores	7k	15K	17k	7.7k
VRAM	40 / 80	48	96	32
Purpose	General	Viz, Al Inference	Al Training, High Data I/O	Scientific





Alpine GPUs

	NVIDIA			AMD
Туре	A100	L40	GH200	MI100
Partition	aa100	al40	gh200*	ami100
Nodes	40 (8) / 80 (4)	3	2	2
GPUs Per Node	3	3	1	3





Requesting GPUs

SLURM Directives:

- --partition= < >
- --gres=gpu:<#>
- --ntasks=<#>

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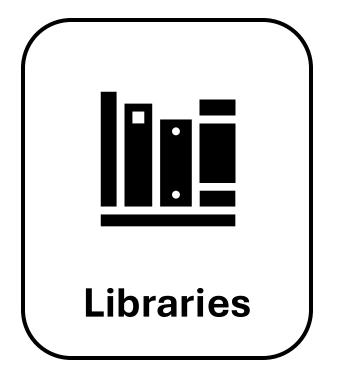
sinteractive --partition=ami100 --gres=gpu:2 --ntasks=20

#SBATCH < directive >

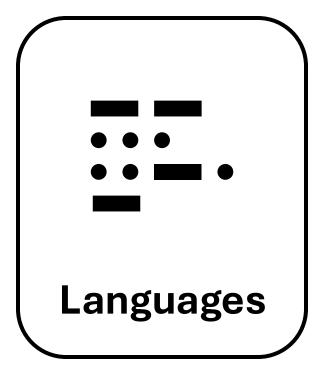




Code Optimization











Key Terms

- Host == CPU
- Device == GPU
- Kernel == Functions launched on GPU

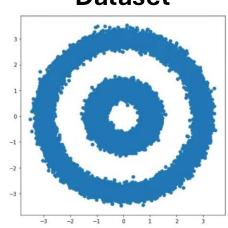
#create dataset with 100,000 points

from sklearn.datasets import make_circles
X, y = make_circles(n_samples=int(1e5), factor=.35, noice=.05)

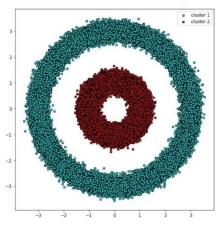
#run DBSCAN clustering algorithm

from sklearn.cluster import DBSCAN
db = DBSCAN(eps=0.6, min_samples=2)
y_db = db.fit_predict(X)

Dataset



Clusters



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import pandas as pd
import cudf
X_df = pd.DataFrame({'fea%d'%i: X[:,i] for i in range(X.shape[1])})
X gpu = cudf.DataFrame.from pandas(X df)
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#run DBSCAN clustering algorithm
                                          #run GPU-accelerated DBSCAN
from sklearn.cluster import DBSCAN
                                          from cuml import DBSCAN
db = DBSCAN(eps=0.6, min samples=2)
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    #run GPU-accelerated DBSCAN
    from cuml import DBSCAN
    db = DBSCAN(eps=0.6, min samples=2)
    y db = db.fit predict(X)
```





GPU-Enabled Frameworks









GPU Compiler Directives





GPU Compiler Directives

Kernel directives

Generate parallel accelerator kernels for the loop following the directive.

```
//Hello_World_OpenACC.c
void Print_Hello_World()
{
    #pragma acc kernels
    for(int i=0; i<5; i++)
    {
        printf("Hello World!\n")
     }
}</pre>
```





GPU Compiler Directives

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void Print_Hello_World()
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        printf("Hello World!\n")
     }
}</pre>
```

Data directives

Generate code to manage specific data operations to support parallelism

```
//Hello_World_OpenACC.c
#pragma acc data copy(a)
{
    #pragma acc kernels
    for(int i=0; i<5; i++)
    {
        printf("Hello World!\n")
     }
}</pre>
```

Languages

- OpenCL (NVIDIA, AMD, & CPUs)
 - Flexible / portable option
- HIP (AMD -> NVIDIA)
 - AMD developed
 - Can convert CUDA code via `hippify`
- CUDA (NVIDIA only)
 - Most robust and largest developer community





Monitoring GPU Usage

- Nvidia-smi
- rocm-smi

 NVIDIA-SMI 510.47.03 Drive	r Version: 510.47.03	CUDA Version: 11.6		
 GPU Name Persistence- Fan Temp Perf Pwr:Usage/Ca 	•	•		
=====================================	=+====================================	•		
1 NVIDIA A100-PCI Off N/A 36C P0 40W / 250W 	00000000:81:00.0 Off 0MiB / 40960MiB	0 0% Default Disabled		
2 NVIDIA A100-PCI Off N/A 37C P0 40W / 250W 				
Processes: GPU GI CI PID Type Process name GPU Memory ID ID Usage 				





Troubleshooting GPU Workflows

• Is your application and/or code GPU accelerated?

Confirm that you installed the GPU accelerated version!

- Does your application or code support multi-GPU acceleration?
- Is your application ROCM- or CUDA-aware?

You can't run CUDA code on AMD GPUs. Not all applications are available for AMD GPUs.

- Can your application "see" the GPU?
- Did you request enough CPUs and RAM?





Documentation



https://curc.readthedocs.io/en/latest/





Survey and feedback



Survey: http://tinyurl.com/curc-survey18



