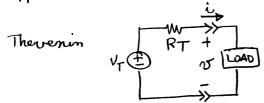
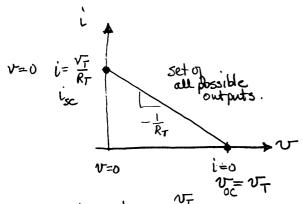
Application to non-linear loads



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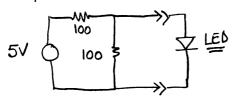


 $\dot{\lambda} = -\frac{1}{R_T} v + \frac{v_T}{R_T}$

called a load line but really is a sounce line

a-point (operating point)

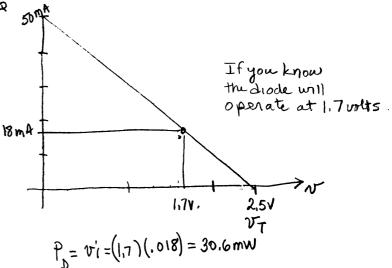
Example 3-18



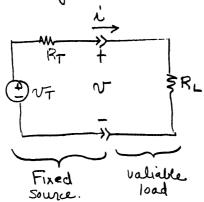
$$V_{\uparrow} = V_{oc} = \frac{100}{100 + 100}$$
, $5 = 2.5$

$$\dot{l}_{N} = \dot{l}_{SC} = \frac{V_T}{RT} = \frac{2.5}{50}$$

= 50mA



3,5 Maximum signal transfer



max output voltage when R= 00 max output current when $R_1 = 0$

Where is maximum power?



Consider the power being delivered to the load.

At the interface
$$V = \frac{R_L}{R_L + R_T} V_T$$

$$\dot{c} = \frac{V_T}{R_L + R_T}$$

$$P = v_i = \frac{R_L v_T}{R_L + R_T} \cdot \frac{v_T}{R_L + R_T} = \frac{R_L v_T^2}{(R_L + R_T)^2}$$

when do we get maximum power to the load, i.e., the value of RL

$$\frac{\partial P}{\partial R_{L}} = \frac{v_{T}^{2}}{(R_{L} + R_{T})^{2}} + \frac{R_{L}v_{T}^{2}(-2)}{(R_{L} + R_{T})^{+3}} = 0.$$

$$\frac{(R_{L}+R_{T}) - 2R_{L}}{(R_{L}+R_{T})^{3}} \sqrt{r}^{2} = 0$$

$$R_{L}+R_{T}-2R_{L}=0$$

$$R_{T}-R_{L}=0$$

given measurements find max output.

$$4 = \frac{50}{50 + R_T} \sqrt{7}$$

$$\frac{2\omega + 4RT}{50} = \sqrt{7}$$

$$4 = \frac{50}{50 + R_T} v_T$$
 $5 = \frac{75}{75 + R_T} v_T$

$$v_T = \frac{375 + 5R_T}{75}$$

$$\frac{200 + 4 R_T}{50} = \frac{375 + 5 R_T}{75}$$

$$(200)(75) + (75)(4) R_{7} = (375)(50) + (50)(5R_{7})$$

$$(65)(4) R_7 - (50)(5) R_7 = (375)(50) - (200)(75)$$

$$R_T = 75$$
 π .

$$4 = \frac{50}{50 + 75}$$

$$\frac{(125)(4)}{50} = v_T = 10.$$

max voltage out = Voc = 10 volts

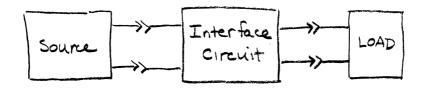
max arrent out =
$$i_{sc} = \frac{10 \text{ V}}{75 \text{ L}} = 133 \text{ mA}$$

max power out occurs at R_= 7572.

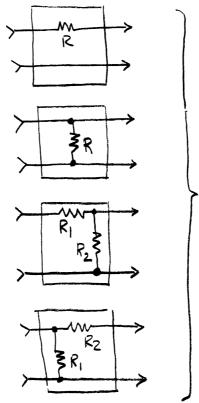
$$V_{OUT} = \frac{75}{75+75}$$
, $10 = 5$ volts.

$$P_{\text{OUT}} = \frac{(v_{\text{NUT}})^2}{R_L} = \frac{(5)^2}{75} = \frac{25}{75} = 333 \,\text{mW}.$$

3-6 Interface Circuit Design



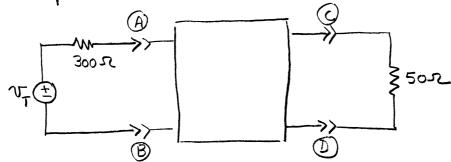
At this point resistors are the only elements we can use to design interface circuits.



Examples of interface circuits.

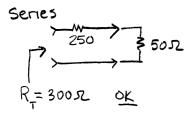
These are often called two-port circuits.

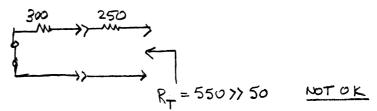
Design Example 3-23



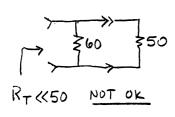
Design the two-port interface circuit so the load "sees" a
Therenin resistance of 502 between terminals @ and D, while
Simultaneously the source "sees" a load resistance of 30052 between A) and B

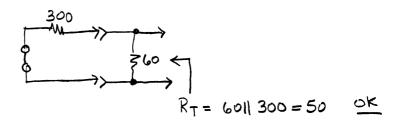
Design We can try different interface circuits



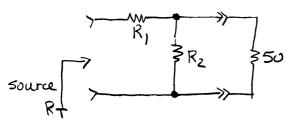


Parallel



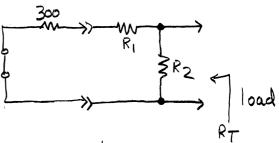


Try two resistor circuits



want source to see larger resistance than 50 so this requires a series R.

Design
$$R_1 + \frac{50 R_2}{R_2 + 50} = 300$$



want load to see a RT smallel than source so there has to be a parallel resistence.

$$\frac{(R_1 + 300) R_2}{R_1 + 300 + R_2} = 50$$

Non-linear equations with solutions $R_1 = 273.9$ and $R_2 = 54.8 \text{ SZ}$.