GAUNTLET

HARDWARE+OPERATING SYSTEM DESC.

COMPANY CONFIDENTAL

DIST.

P. BROSNAN

M.O'ROURKE

B. EGAN

GAUNTLET HARDWARE DESCRIPTION

REATED: REVISED:

May 14, 1985 Sept. 13, 1985

: Ed Logg, Pat McCarthy and Bob Flanagan

I. Microprocessors:

A. 68010 running @ 7.16 MHz.

Program ROM:

- 176K words max. including 32K word of OS and 16k word of SLAPSTICK (see ROM map below.)

Program RAM:

- 4K word fixed, plus 2K word spare video RAM

Interrupt:

- Sound port (from 6502) - level 6

- Vblank - level 4.

6502 runs at 1.78 mhz

- IRO from hardware. (Note. Not the YAMAHA)

- NMI from the 68010.

F. Input/output:

- There are 4 bytes worth of inputs. This allows up to 4 switch iovsticks and 16 buttons.

G. EEPROM:

A 512 byte EEPROM is used as a nonvolatile memory for the 68010. You can read or write the EEPROM just as a RAM, however, after a write to the EEPROM, you should wait a minimum of 10 msec. before accessing (read or write) the EEPROM again (For more info. read the EEPROM data sheet & application notes.) Before writing to the EEPROM, you should enable the write by writting (any data) to UNLOCK (@ 803150). You can not read the EEPROM after you UNLOCK until you have written something somewhere (any data, any location.)

II. Graphics:

- A. Display: 336 x 240 pixels standard res. monitor.

 B. Playfield: Size: 64 stamps x 64 stamps
 Visible screen: 42 stamps x 30 stamps
 Stamp size: 8 pixels x 8 pixels
 Independent smooth scrolling horizontally & vertically (wrap-around).
 - Vertical scroll register does not need to be refreshed.
 - Max capacity: up to 4 banks. Each bank = 12_bits = 4K stamps 4 bits deep.

D15 D14 D13 D12 D11 D10 D9 D8 D7 D6 D5 D4 D3 D2 D1 D0 | H | PALETTES | PIC

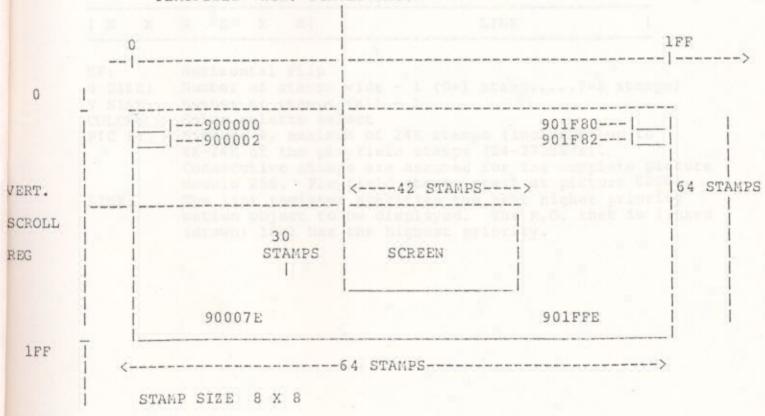
H: Horizontal flip

PALETTES: Palette select (3 bits)

PIC: Picture number

PLAYFIELD COORDINATES:

PLAYFIELD HOR. SCROLL REG.



C. Motion Objects:

- 56 M.O.'s (1 stamp wide) visible on any scan line - M.O. processing is pipelined to allow more than

56 stamps worth to be processed per line.

- "Linked" M.O.'s

- A new starting link pointer (SLIP) for every 8 scan lines of the playfield

- H and V positions relative to the playfield hence scrolling will automatically move objects with the playfield - 1024 motion object descriptors

- stamps are 8 pixels wide x 8 pixels
 - each object can specify up to 8 stamps tall and 8 stamps wide
 - up to 4 bits deep

Each motion object is specified by 4 words in the motion object RAM as follows:

| D15 | D14 | D13 | D12 | Dll | D10 | D9 | D8 | D7 | D6 | D5 | D4 | D3 | D2 | Dl | D0 |
|-----|-----|------|-----|-----|-----|----|----|----|----|----|----|-----|-----|------|----|
| i x | 1 | | P | IC# | | | | | | | | | | | 1 |
| 1 | | | Н: | POS | | | | | X | X | Х | 1 | COL | DR | 1 |
| ī | | 3050 | V | POS | | | | | HF | H | SI | ZΕ | IV | SIZE | 1 |
| 1 x | X | Х | X | Х | X I | | | | | | L | INK | | | 1 |

HF: Horizontal Flip

H SIZE: Number of stamps wide - 1 (0=1 stamp,...,7=8 stamps)
V SIZE: Number of stamps tall - 1

Color palette select COLOR:

PIC #: Picture #, maximum of 24K stamps (including up to

4K-16K of the playfield stamps (24-27256's).

Consecutive stamps are assumed for the complete picture modulo 256. Playfield stamps start at picture 0000.

The link register specifies the next higher priority LINK:

motion object to be displayed. The M.O. that is linked

(drawn) last has the highest priority.

II. Alpha-Numerics:

- 2 bits deep per pixel, up to 32 color palettes.
- Stamp size: 8 pixels x 8 pixels
- Screen size: 42 stamps x 30 stamps

Every alpha stamp is specified by one word in the alpha numeric RAM as follows:

D15 D14 D13 D12 D11 D10 D9 D8 D7 D6 D5 D4 D3 D2 D1 D0

| BG | COLOR | SHAPE |
|----|-------|-------|
| | | |

BG: The whole alpha stamp (including background) will have a

higher priority than M.O.'s or PF. COLOR: Color Palette.

SHAPE: Up to 1024 stamps (1-27128 or 1-27256).

ALPHA COORDINATES <---->

| | -905000 -905080 | | 90504D 9050CD |
|--------------|----------------------|--------|--------------------|
| 30 STAMPS | | SCREEN | ts * 2 d |
| T Sur | 905E80 | | 905ECD |

COLUMN SEC

STAMP SIZE 8 X 8

V. Priority:

- Alpha-numerics have the highest priority.

- If the "BG" bit in the alpha parameter is set, the background color or alpha will have priority over all other graphics.

- Motion object colors 2 thru \$F are next in priority.
- If motion object color 1 is specified then the shadow ram colors are used with the playfield color palettes and picture bits.

- Playfield has the lowest priority.

. COLORAM:

The coloram gets graphics information from three sources: ALPHA, PLAYFIELD, and MOTION OBJECT

ALPHA COLORAM = 910000 +

Pictures bits * 2 + C3-C0 * \$8 + C4 * \$100

where C4 to C0 are the color pallete bits specified in the alpha RAM.

MOTION OBJECT COLOR RAM = 910200 +

Picture bits * 2 + Color palette * \$20

PLAYFIELD COLOR RAM = 910500 +

Picture bits * 2 + Color palette * \$20

SHADOW COLOR RAM= 910400 +

Playfield bits * 2 + Playfield color palette * \$20

Coloram data interpretation:

<D15:D12> Intensity <D11:D8 > Red <D3 :D0 > Blue

In all cases, 0000=OFF llll=highest intensity.

t Euricontal Pos. Spinos Spinos

VI . NOTES:

1. Graphics pixel data is low true!!! PBCONVERT in the PPS utilities will automatically do a 1's complement for you.

2. All motion object pictures using more than one "stamp" must be in the same page. The PB utilities have features that let you blank fill EPROM's.

| AUNTLET 68010 MEMORY-MAP | ADDRESS | R/W | DATA |
|-----------------------------|---------------|-----|-----------------------|
| rogram ROM OS | 000000~00FFFF | R | D15~D0 |
| ogram ROM SLAPSTICK | 038000~03FFFF | R | D15~D0 |
| ogram ROM Main | 040000~07FFFF | R | D15~D0 |
| pare RAM | 800000~801FFF | R/W | |
| EPROM | 802001~802FFF | R/W | D7~D0 |
| layer 1 Input | 803001 | R | D0~D7 |
| Layer 2 Input | 803003 | R | D0~D7 |
| layer 3 Input | 803005 | R | D0~D7 |
| ayer 4 Input | 803007 | R | D0~D7 |
| ayer 4 input | 003007 | ** | D7 Joystick-up |
| 1010 | | | D6 Joystick-down |
| | | | D5 Joystick-left |
| CO SUNCTABLE OF TOTAL | | | D4 Joystick-righ |
| • | | | D3 Spare |
| ayer inputs : | | | |
| | | | D2 Spare D1 Fire |
| • | | | |
| Lose Fances - Lose | | | DO Magic/Start |
| LANK | 803009 | R | D6 Active lo |
| tput-Buffer Full (@ 803170) | 803009 | R | D5 Active hi |
| put-buffer Full (@ 80300F) | 803009 | R | D4 Active hi |
| lf-test | 803009 | R | D3 Active lo |
| ad Sound Processor (6502) | 80300F | R | D7~D0 |
| tch Dog | 803100 | M | xx (128 msec. timeout |
| D-1 | 803121 | W | DO LOW ON |
| D-2 | 803123 | W | DO LOW ON |
| D-3 | 803125 | W | DO Low ON |
| D-4 | 803127 | W | DO Low ON |
| und Processor Reset | 80312F | W | DO Low Reset |
| lank Acknowledge | 803140 | W | xx |
| lock EEPROM | | | |
| ite Sound Processor (6502) | | W | |
| ayfield RAM | 900000~901FFF | R/W | D15~D0 |
| tion Object Picture | 902000~9027FF | | |
| tion Object Horizontal Pos. | | | |
| tion Object Vertical Pos. | | | |
| tion Object Link | 903800~903FFF | R/W | D15~D0 |
| are RAM | 904000~904FFF | R/W | D15~D0 |
| | 905000~905FFF | P/W | D15~D0 |
| ayfield Vertical Scroll | 905F6E.F | R/W | D15~D7 |
| ayfield RON bank select | 905F6F | | D0,D1 |
| IP pointers | 905F80 | R/W | |
| lor PAM Alpha | 010000~010100 | | |
| lor RAM Alpha | 910000~9101FF | | |
| lor RAM Motion Object | 910200 9103FF | R/W | D15 D0 |
| ior RAM Playfield Shadow | 910400~9104FF | R/W | D12.D0 |
| lor RAM Playfield | 910500~9105FF | R/W | D15 D0 |
| olor RAM (spare) | 910600~9107FF | R/W | D15"D0 |
| ayrield Horizontal Scroll | 930000.1 | 147 | D8~D0 (word mode on |

| 6502 MEM-HAP | ADDRESS | R/W | DATA | | |
|------------------------------------------------------------------------------------------|----------------------------------------------|------------------------|-------------------------------|--------------------------------------------------------------------|--|
| Program RAM | 0000~0FFF | R/W | D7~D0 | | |
| Write 68000 Port Read 68000 Port | 1000 1010 | W R | D7~D0 D7~D0 | Output buffer Input buffer | |
| Audio mix : | 1020 1020 1020 | W W | D7~D5 D4,D3 D2~D0 | Speech mix Effects mix Music mix | |
| Coin 1 (left) Coin 2 Coin 3 Coin 4 (right) | 1020 1020 1020 1020 | R R R | D3 D2 D1 D0 | | |
| Data available (@ 1010) Dutbuf full (@ 1000) Speech ready Self Test | 1030 1030 1030 1030 | R R R | D7 D6 D5 D4 | Hi active Hi active Low active Low active | |
| Nusic reset Speech Write Speech reset Speech Squeak Coin Counter Right Coin Counter Left | 1030 1031 1032 1033 1034 1035 | W W W W W | D7 D7 D7 D7 D7 | Low Reset Low Active Low Active Low=650KHz clk Hi active Hi active | |
| Effects Music Speech Interrupt acknowledge | 1800~180F 1810~1811 1820 1830 | R/W R/W W R/W | D7~D0 D7~D0 D7~D0 xx | | |
| Program ROM | 4000~FFFF | R | D7~D0 | 48k bytes. | |

Notes: NO 6502 watchdog (see 68010)

NO NMI mask for the NMI coming from the 68010. NMI is automatically acknowledged when the 6502 reads the input data buffer at location 1010

```
2.5V : 2.5 volt audio amplifier reference
+5AUD
                : 5 volt audio amplifier reference
10.3V
               : Power-on-reset control voltage
+12V
               : +12 volts regulated
               : +15 volts unregulated
+15V
-15V : -15 volts unregulated
-5V
               : -5 volts regulated
1H ~ 256H : screen horizontal address counter chain 1V ~ 128V : screen vertical address counter chain : inverted 4H signal
4HD3, /4HD3 : 4H signal delayed 3 clock cycles
'4HDD : 4H signal delayed 2 clock cycles
'4HDL : 4H signal delayed 2 clock cycles
'4HDL : 4H signal delayed one clock cycle 68KBUF : 68010 output buffer full (to 6502)
Al~A23 : 68010 address bus unbuffered
               : 'A' line buffer RAMs chip select
'ACS
ALC3, ALC4 : alphanumerics pallet data bits 3 & 4
'ALHI, /ALLO : alphamunerics RAM chip selects
               : alphamunerics pixel data
APIXO, APIX1
                : 68010 address strobe
AUDIO-L, AUDIO-R : left & right audio outputs (5v pk-pk)
               : 6502 buffered phase 2
 BAS
                : puffered address strobe (see /AS)
                : 'B' line buffer RAMs chip select
/BCS
BLU0 ~ BLU3
               : plue color RAM data
                : blue analog video output
BOUTO ~ BOUT3
               : blue latched digital video output
               : 68010 read/write control, buffered
               : swap 'A' & 'B' line buffers, clear LB counter chain
/BUFCLR
BW//R : 68010 read/write inverted, buffered CAS, CA7 : color RAM address bits 5 & 7
CCTR1, CCTR2 : coin counter outputs 1 & 2
               : clear line buffer 'A' address counters : clear line buffer 'B' address counters
/CL RA
/CLRB
/COIN
          : coin input buffer chip select
COIN1-L, COIN2, COIN3, COIN4-R: 4 coin switch inputs
/COMPSYNC : negative composite sync output
CRAO CRA9 : Color RAM address
               : 68010 address decode for color RAM
/CRAM, CRAM
                : Color RAM write enable
/CRAMWR
D0 ~ D15
               : 68010 data bus, unbuffered
 DOWN-1 ~ DOWN-4 : joystick down switch inputs, player 1-4
                : electrically erasable PROM chip select
EEPRON
/END
                : current motion object finished
               : system clock inverted phase
FIRE-1 ~ FIRE-4 : fire switch inputs, player 1-4
         : line buffer 'A' fill control
/FLBA
/FLBB
                : line buffer 'B' fill control
```

```
GCS0 ~ GCS5 : graphics ROMs chip select
 /GLD
                  : graphics load (to SLAGS chips)
 GND
                  : system ground
 · GOUTO ~ GOUT3 : green latched digital video output
 GPO ~ GP14 : graphics picture address
 GP1V, GP2V, GP4V : graphics picture stamp sub-address
              : graphics picture enable
 GREEN
                e: green analog video output
                : graphics ROM hi/lo select (Al4 on a 27256)
 GRH/L
  GRNO ~ GRN3
                  : green color RAM data
 HO3 : alphanumerics load (to shift registers)
 HFLP
            : graphics stamp horizontal flip
                  : latch motion object horizontal data and pallet data
HPOSO "HPOS8 : motion object horizontal position data
 HORZDL, /HORZDL: HORZ delayed one clock cycle
                  : latch playfield horizontal scroll data
 HSIZO "HSIZ2 : motion object horizontal size
HSYNC, /HSYNC : horizontal sync output
 /INPUT
                   : 68010 miscellaneous inputs buffer select
 INTO ~ INT3 : intensity color RAM data
 IOUT0 ~ IOUT3 : intensity latched digital video output
           : 68010 miscellaneous latched outputs chip select
 /LATCH
 LAUD
                   : summed left channel audio
 LBA0 ~ LBA8 : line buffer 'A' address bus
 LBB0 ~ LBB8 : line buffer 'B' address bus
 LBCKF
                 : line buffer clock inverted phase
                  : line buffer clock
 LBCKR
LBDA0 ~ LBDA7 : line buffer 'A' data bus
LBDB0 ~ LBDB7 : line buffer 'B' data bus
           : load line buffer 'A' address counters
/LDA
                 : load line buffer 'A' or 'B' address counters
/LDAB
                  : load line buffer 'B' address counters
/LDB
/LDS
                  : 68010 lower data strobe
LED1 ~ LED4 : LED outputs, player 1-4
LEFT-1 ~ LEFT-4 : joystick left switch inputs, player 1-4
            : latch motion object link data
/LINK
 'LMPD : stop motion object processing for line buffer changeover LNK0 LNK9 : motion object link data
MA1 MA14 : 68010 address bus buffered
/LMPD
 MATCH
                 : motion object H & V data matches current
                 playfield position
MATCHDL : previous MATCH state

MBUS : 68010 'M' data bus buffers enable

MCO, MCl : motion object parameter control select

MCEN : motion object parameter control enable

MCKF : master clock, inverted phase

MCKR : master clock

MDO " MD15 : 68010 'M' data bus

MFLP : motion object horizontal flip parameter

/MIX : latch audio mix data
/MIX : latch audio mix data
MO//PF : 'motion-object' or 'playfield' picture select
/MIX
/MOHI, /MOLO : motion object RAM chip selects
```

```
MOSRO ~ MOSR3 : M.O. pixel data, before the line buffers
MOSR4 ~ MOSR7 : M.O. pixel pallet data, before the line buffers MPIC0 ~ MPIC7 : the lower 8 bits of M.O. picture adress
MPX0 ~ MPX7 : M.O. pixel data, after the line buffers
               : motion object stamp horizontal flip state
MREFL
              : music chip select
/ NEWMO
               : start a new motion object
            : next line
NXL, /NXL
 (/NXL*)
               : NXL one clock cycle early
NXLDL
                : NXL delayed one clock cycle
/PF1LD ~ /PF256LD : latched playfield horizontal scroll data
PF1V ~ PF256V : playfield vertical address counter chain
PF8H ~ PF256H : playfield horizontal address counter chain
PFBANKO, PFBANK1 : playfield picture bank select
/PFHI, /PFLO
               : playfield RAM chip selects
                : playfield scroll control
/PFHST
PFSR0 ~ PFSR3
               : playfield pixel data, before PFHS
PFSR4 ~ PFSR6 : playfield pixel pallet data, before PFHS
PFX0 ~ PFX6 : playfield pixel data after PFHS
PICSTO ~ PICST7 : motion object picture start address
/PICT
               : latch motion object picture data
            : PICT delayed one clock cycle
PICTDL
PKAUD
              : effects chip audio
/PL1 ~ /PL4
PM0 ~ PM2
               : player inputs chip selects, players 1-4
            : effects audio mix control bits
               : effects chip select
POKEY
PR1 ~ PR6
             : pullup resistors
            : 68010 read/write control, unbuffered
: 68010 working RAM chip selects
RAMO, RAM1
RAUD
              : summed right channel audio
RCLOCK
           : system clock
             : 68010 ROM data bus
RD0 ~ RD15
/RD68K
               : 6502 read 68010 output buffer
               : red analog video output
RED
REDO ~ RED3
              : red color RAM data
RIGHT-1 ~ RIGHT-4 : joystick right switch inputs, player 1-4
               : 68010 ROM data bus enable
               : 68010 program ROM chip selects
/ROMO ~ /ROM4
 ROMH/L
               : 68010 program ROM hi/lo select (Al4 on a 27256)
ROUTO ~ ROUT3 : red latched digital video output
 SAO ~ SA15
              : 6502 address bus unbuffered
SBA0 ~ SBA13 : 6502 buffered address bus
SBD0 ~ SBD7
              : 6502 buffered data bus
SBR//W
              : 6502 buffered read/write control
SBW//R
               : 6502 buffered read/write control inverted
 SD0 ~ SD7
              : 6502 data bus unbuffered
/SELFTEST
              : self-test switch input test pad
              : serial in data
              : 6502 miscellaneous inputs read control
/SIORD
/SIOWR
               : 6502 outputs latch control
             : 6502 interrupt acknowledge
/SIRQACK
             : slapstick chip select
SLAPSTK
SMO ~ SM2
              : speech audio mix control bits
```

```
SNDBUF : 6502 output buffer full (to 68010)

(SNDINT : 68010 interrupt from 6502

(SNDIRQ : 6502 4 millisecond interrupt

(SNDNNI : 6502 non maskable interrupt

(SNDRD : 68010 read buffer from 6502

(SNDRES : 6502 master reset (controlled by 68010)

(SNDWR : 68010 write to output buffer (to 6502)

SOD : serial output data

(SPHRDY : speech chip ready

(SPHRES : speech chip reset

(SPHWR : speech chip write

SQUEAK : speech chip operating frequency control
 SQUEAK
/SRD
                                : speech chip operating frequency control
                               : 6502 read phase
  START-1 ~ START-4 : start switch inputs, player 1-4
STEST : self-test switch input
                         : 6502 write phase
  /SWR
 /SYSRES : system reset (power up)
/UDS : 68010 upper data strobe
/UNLOCK : EEPROM write enable control
  UP-1 ~ UP-4 : joystick up switch inputs, player 1-4
  VASO, VAS1 : video RAM address control (VASC*), (VAS1*): VAS0&1 before being latched VBD0 ~ VBD15 : video RAM buffered data bus
 /VBKACK : vertical blank interrupt acknowledge
/VBKINT : vertical blank interrupt
/VBLANK : vertical blank
VBUS : 68010 | VI | bus crable (for widee BAM)
                       : 68010 'V' bus enable (for video RAM)
 VBUS
 VCC : system VCC (5 volts regulated)

VCPU : 68010 to video RAM syncronisation control

VERT : latch M.O. vertical data and size data
 VERTDL, /VERTDL: VERT delayed one clock cycle
 /VIDBLANK : video blank (horizontal and vertical blank mixed)
/VNATCH : motion object vertical parameter matches
                                 : motion object vertical parameter matches current playfield vertical position
  VOICE
                               : speech chip select
  VPOSO ~ VPOS8 : motion object vertical position data
 VRAO ~ VRAll : video RAM address bus
/VRAM : 68010 address decode for video RAM /VRAMRD : 68010 read from video RAM /VRAMWE : 68010 write to video RAM
 VRD0 ~ VRD15 : video RAM data bus, unbuffered
 VRDTACK : video RAM to 68010 data acknowledge VSIZO ~ VSIZ2 : motion object vertical size parameter VSYNC, /VSYNC : vertical sync
/WDOG : watchdog control

/WH : 68010 write hi byte

/WL : 68010 write lo byte

/WR68K : 6502 write to output buffer (to 68010)

/YAMRES : music chip reset

YMO ~ YM2 : music audio mix control bits

ZREF : intensity reference for video output.
                             : intensity reference for video output.
```