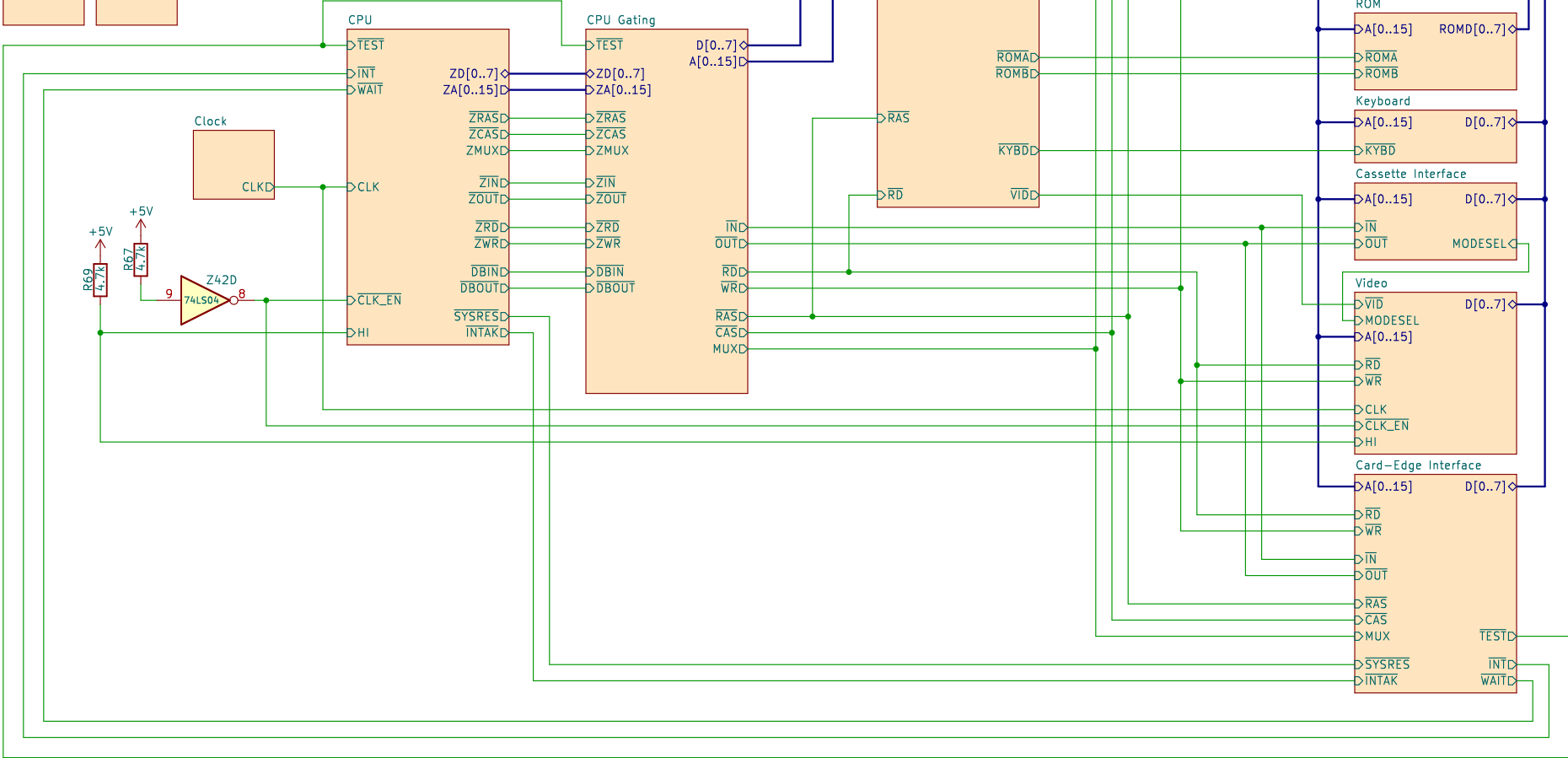
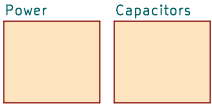
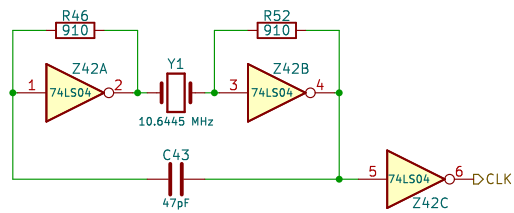


TRS-80 Model I

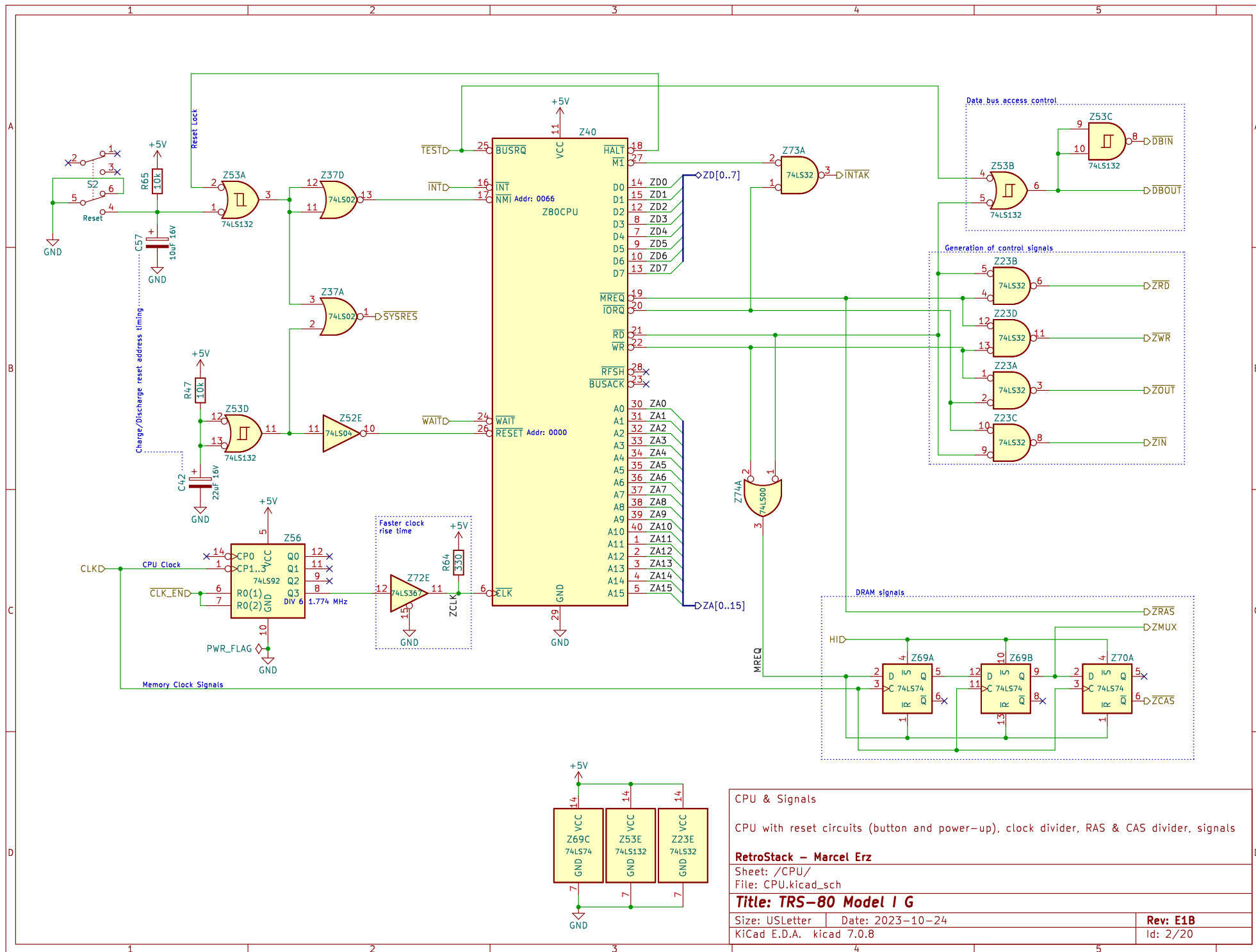
1700069 G E1

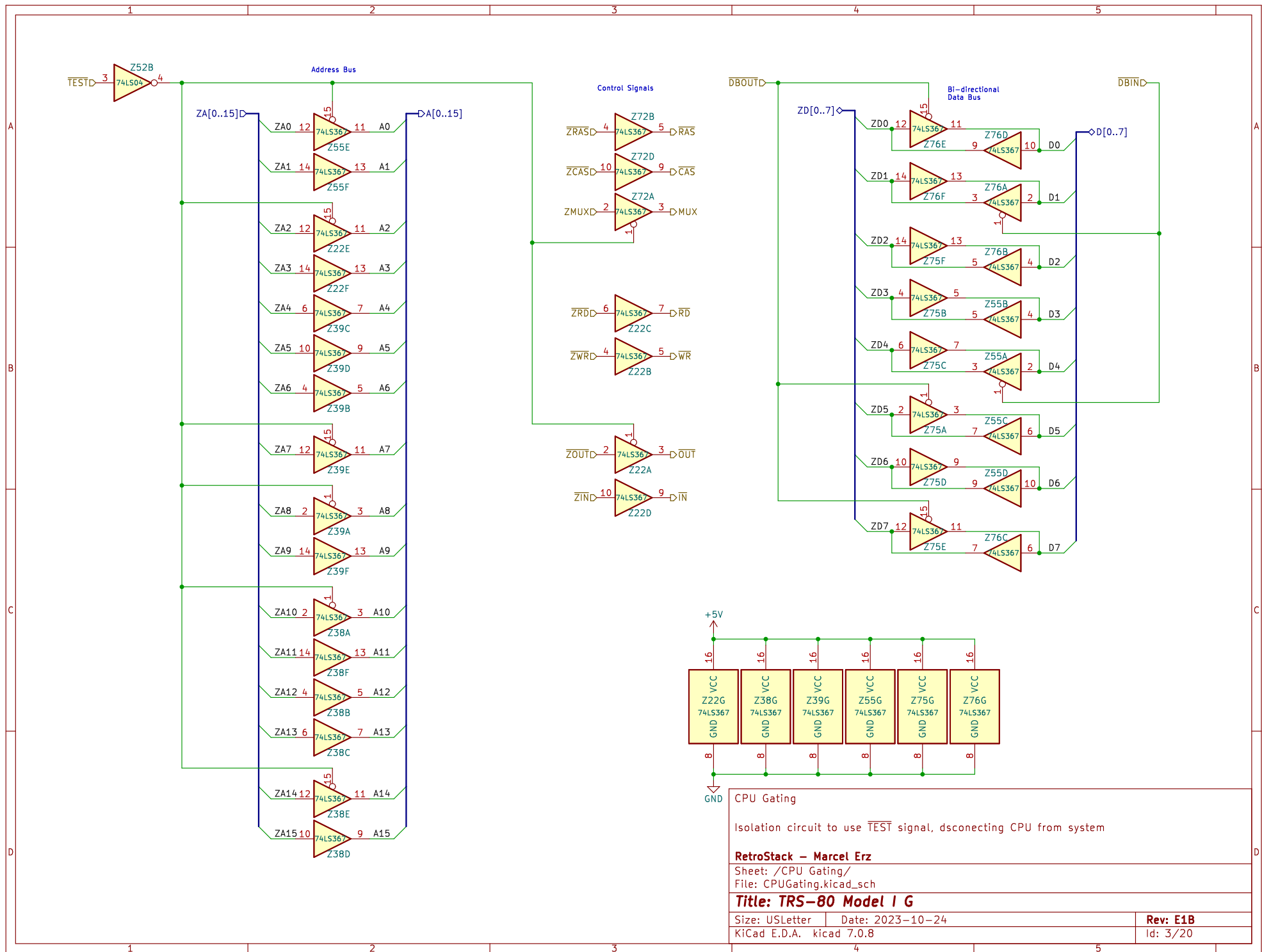


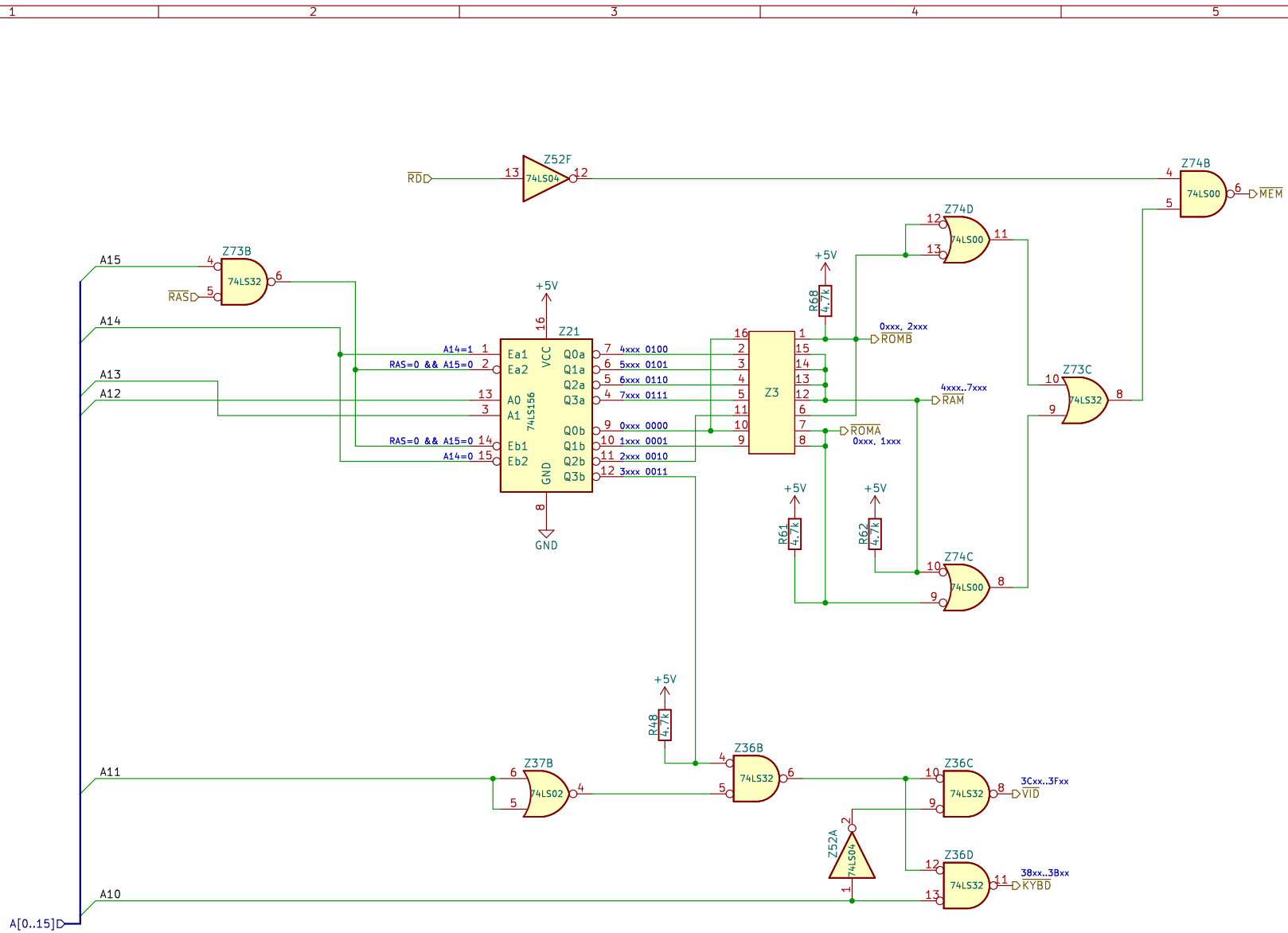
System Overview	
Full overview of the system	
RetroStack – Marcel Erz	
Sheet: /	
File: TRS80_ModelI_G_E1.kicad_sch	
Title: TRS-80 Model I G	
Size: USLetter	Date: 2023-12-21
KiCad E.D.A. kicad 7.0.8	Rev: E1B
	Id: 1/20



System Clock	
RetroStack – Marcel Erz	
Sheet: /Clock/ File: Clock.kicad_sch	
Title: TRS-80 Model I G	
Size: USLetter	Date: 2023-10-24
KiCad E.D.A. kicad 7.0.8	Rev: E1B Id: 4/20







Address Decoder

Address decoder circuit for main board components (except IO)

RetroStack – Marcel Erz

Sheet: /Address Decoder/

File: AddressDecoder.kicad_sch

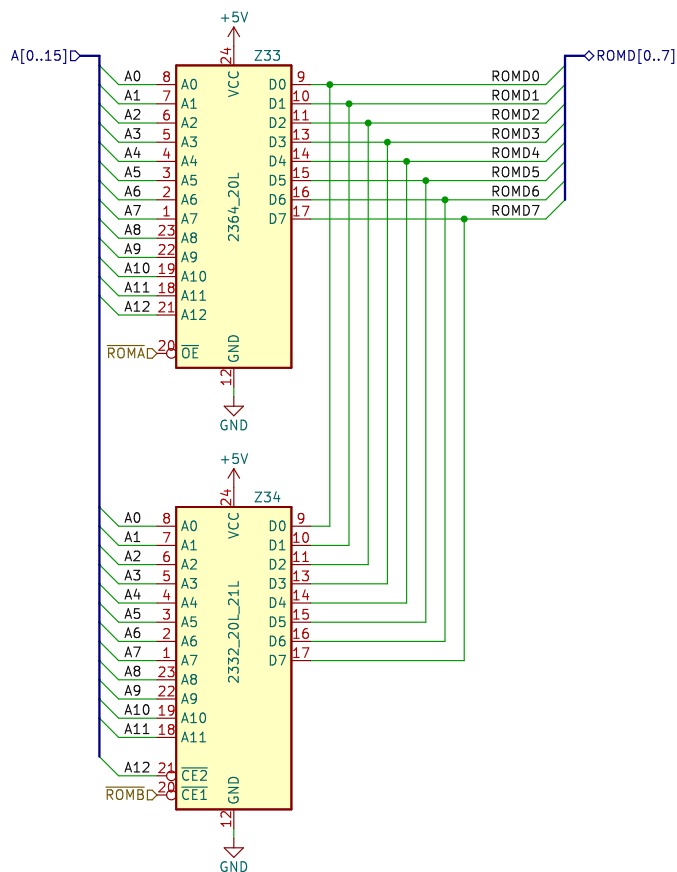
Title: TRS-80 Model I G

Size: USLetter Date: 2023-10-24

KiCad E.D.A. kicad 7.0.8

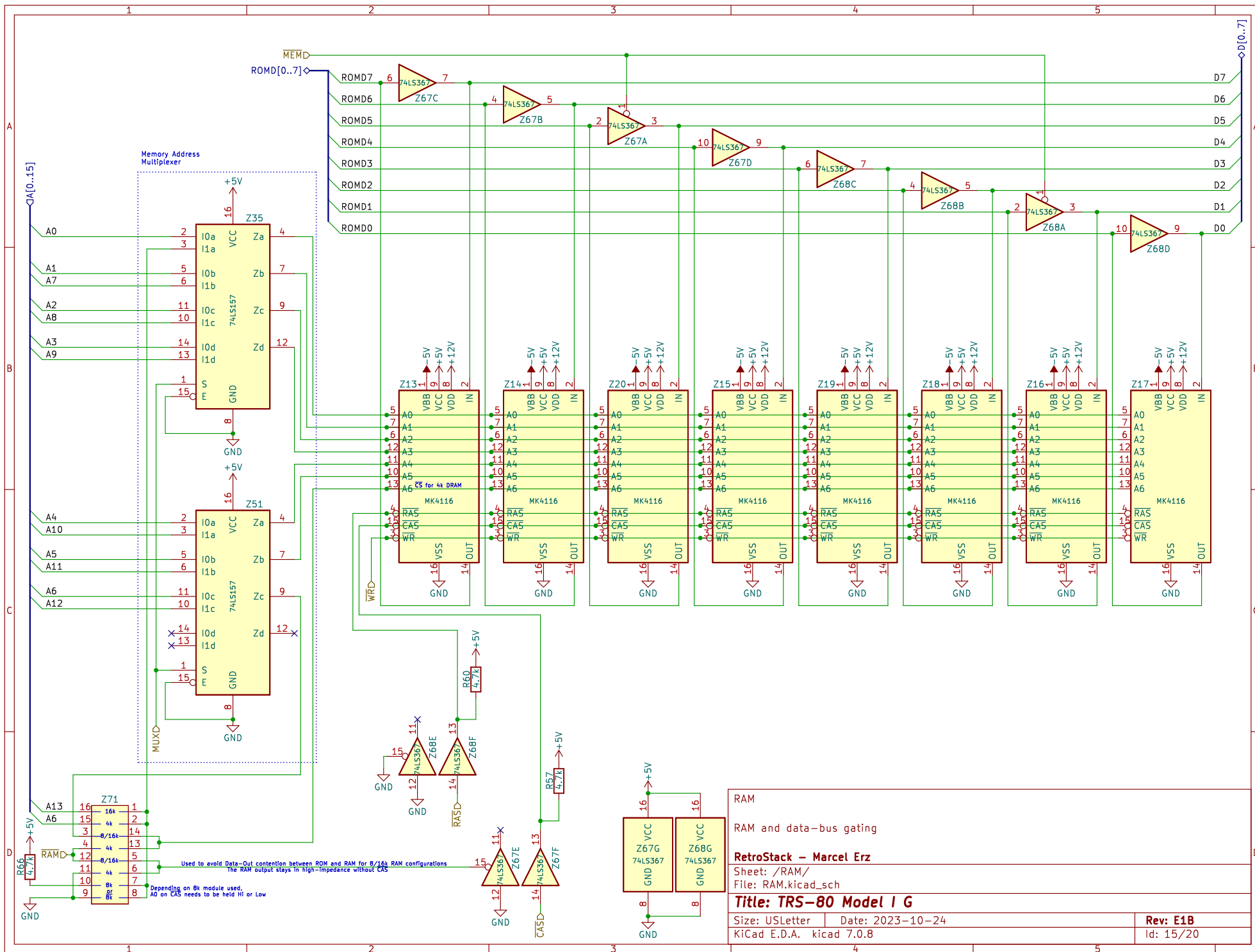
Rev: E1B

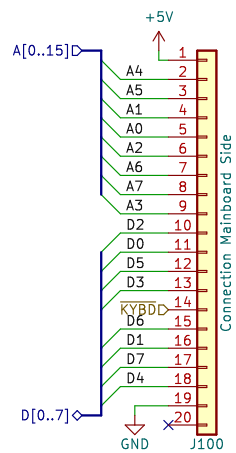
Id: 17/20



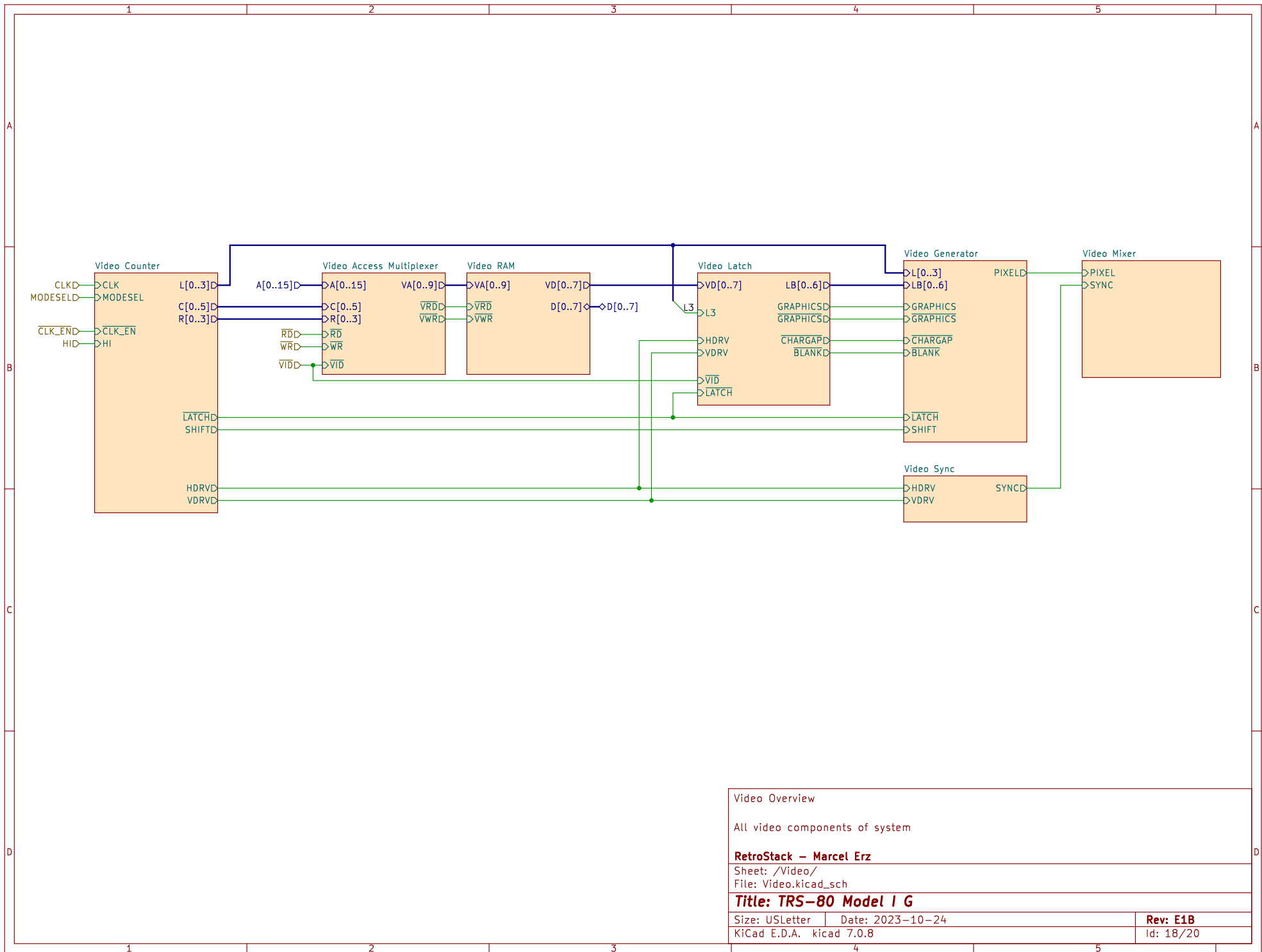
- Level I ROMs:
- Intel 2616 EPROM (White Crystal Window)
 - National MM2316 ROM (Colored Ceramic) (R/D=ROM A, S/D=ROM B)
 - Motorola 7800 Series ROMs (7807=ROM A, 7804=ROM B)
 - Motorola 7800 Series ROMs (7809=ROM A & B)
- Level II ROMs:
- Generic 3 ROMs on separate board with logic chip (each 32kbit/4kb)
 - NEC 8043364 & 8043732 (64kbit/8kb & 32kbit/4kb) (early 1979)
 - NEC 8044364 & 8044732 (64kbit/8kb & 32kbit/4kb) (early 1980)

ROM	
ROM circuit	
RetroStack – Marcel Erz	
Sheet: /ROM/ File: ROM.kicad_sch	
Title: TRS-80 Model I G	
Size: USLetter	Date: 2023-10-24
KiCad E.D.A. kicad 7.0.8	Rev: E1B Id: 14/20

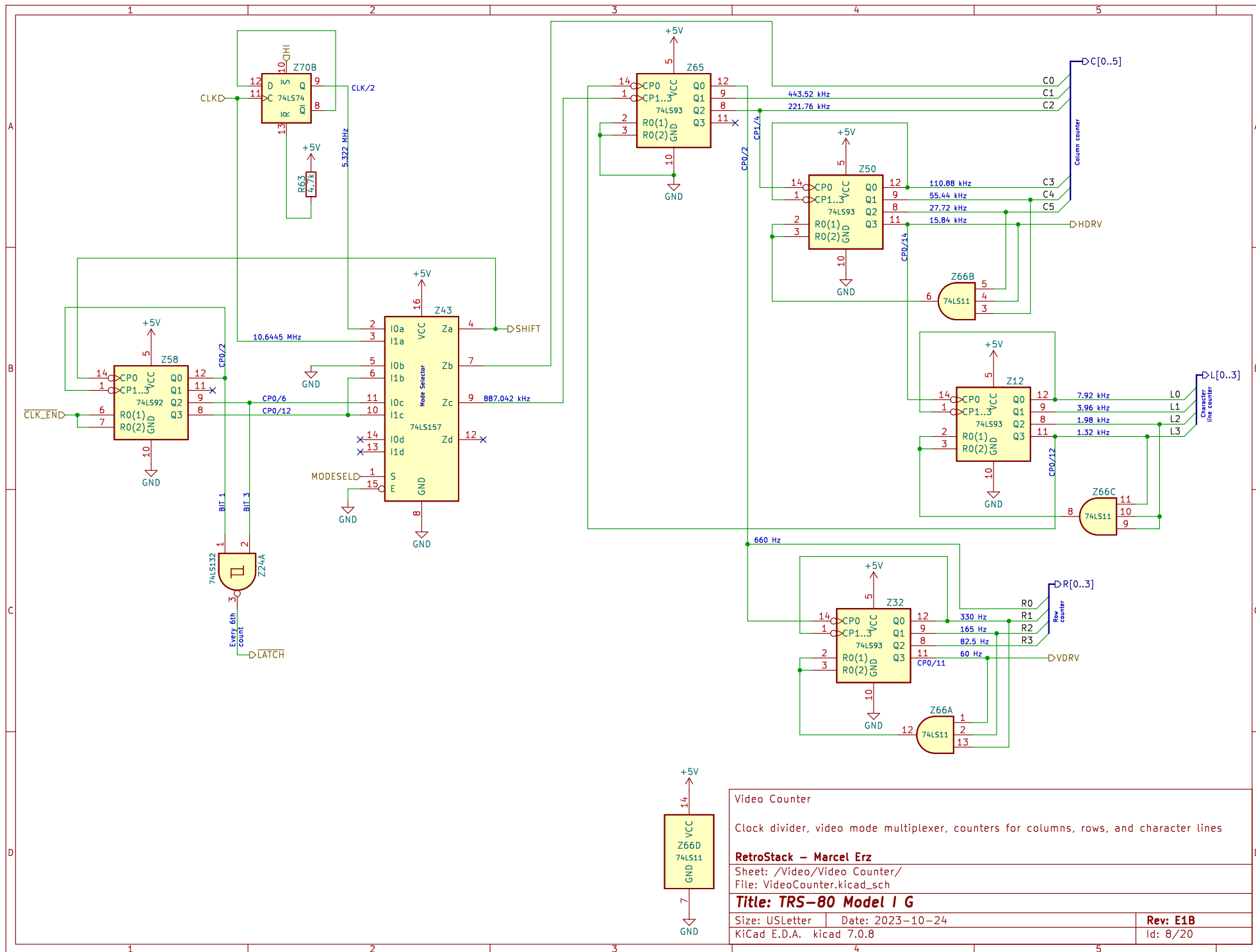




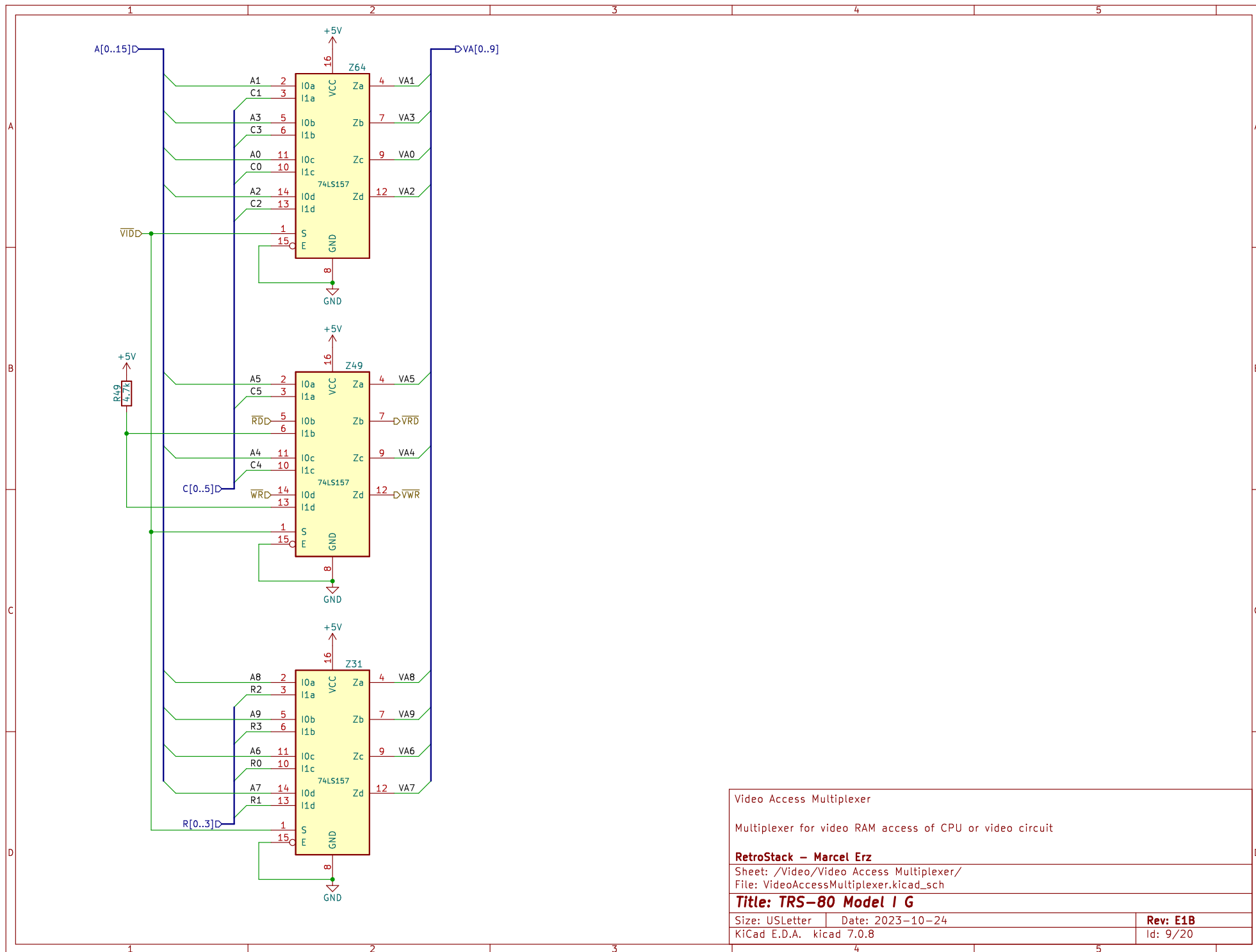
Keyboard & Interface			
Keyboard interface circuit			
RetroStack – Marcel Erz			
Sheet: /Keyboard/ File: Keyboard.kicad_sch			
Title: TRS-80 Model I G			
Size: USLetter	Date: 2023-10-24	Rev: E1B	
KiCad E.D.A.	kicad 7.0.8	Id: 16/20	



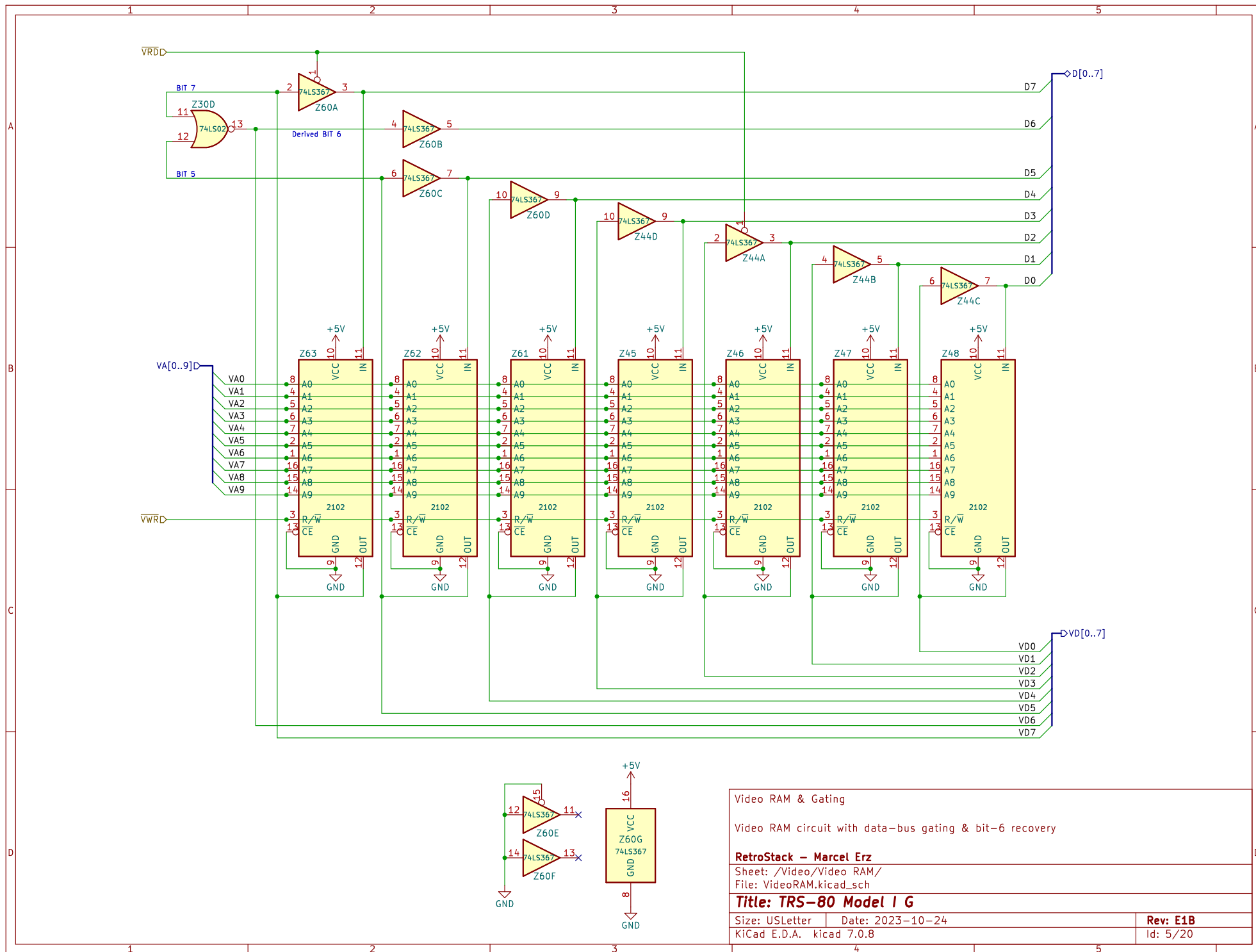
Video Overview			
All video components of system			
RetroStack – Marcel Erz			
Sheet: /Video/ File: Video.kicad_sch			
Title: TRS-80 Model I G			
Size: USLetter	Date: 2023-10-24	Rev: E1B	
KiCad E.D.A.	kicad 7.0.8	Id: 18/20	

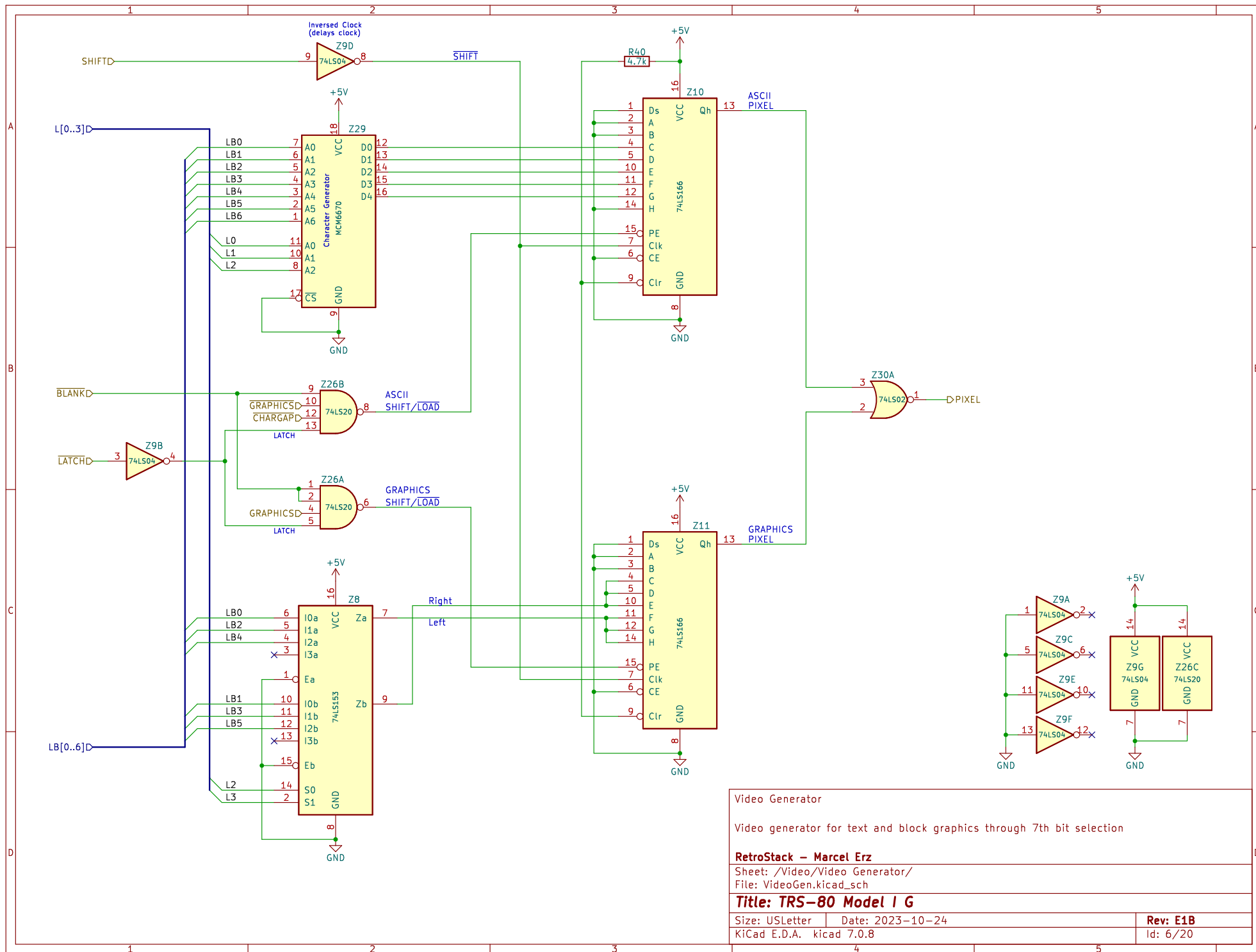


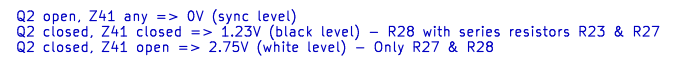
Video Counter		
Clock divider, video mode multiplexer, counters for columns, rows, and character lines		
RetroStack – Marcel Erz		
Sheet: /Video/Video Counter/		
File: VideoCounter.kicad_sch		
Title: TRS-80 Model I G		
Size: USLetter	Date: 2023-10-24	Rev: E1B
KiCad E.D.A.	kicad 7.0.8	Id: 8/20



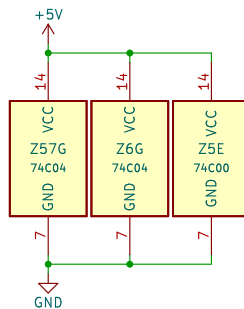
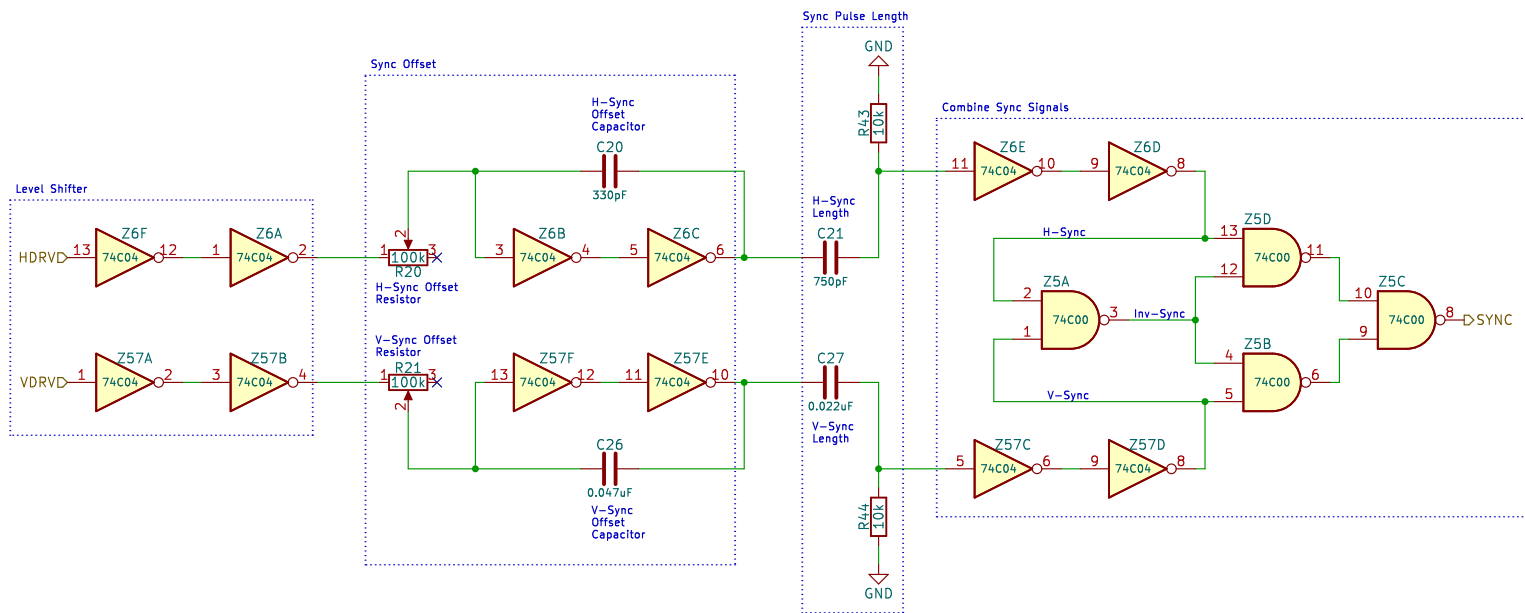
Video Access Multiplexer	
Multiplexer for video RAM access of CPU or video circuit	
RetroStack – Marcel Erz	
Sheet: /Video/Video Access Multiplexer/ File: VideoAccessMultiplexer.kicad_sch	
Title: TRS-80 Model I G	
Size: USLetter	Date: 2023-10-24
KiCad E.D.A. kicad 7.0.8	Rev: E1B Id: 9/20







Rev: E1B
Id: 7/20



Video Sync

Circuit to combine sync signals and defining front- and back-porches in video signal

RetroStack – Marcel Erz

Sheet: /Video/Video Sync/

File: VideoSync.kicad_sch

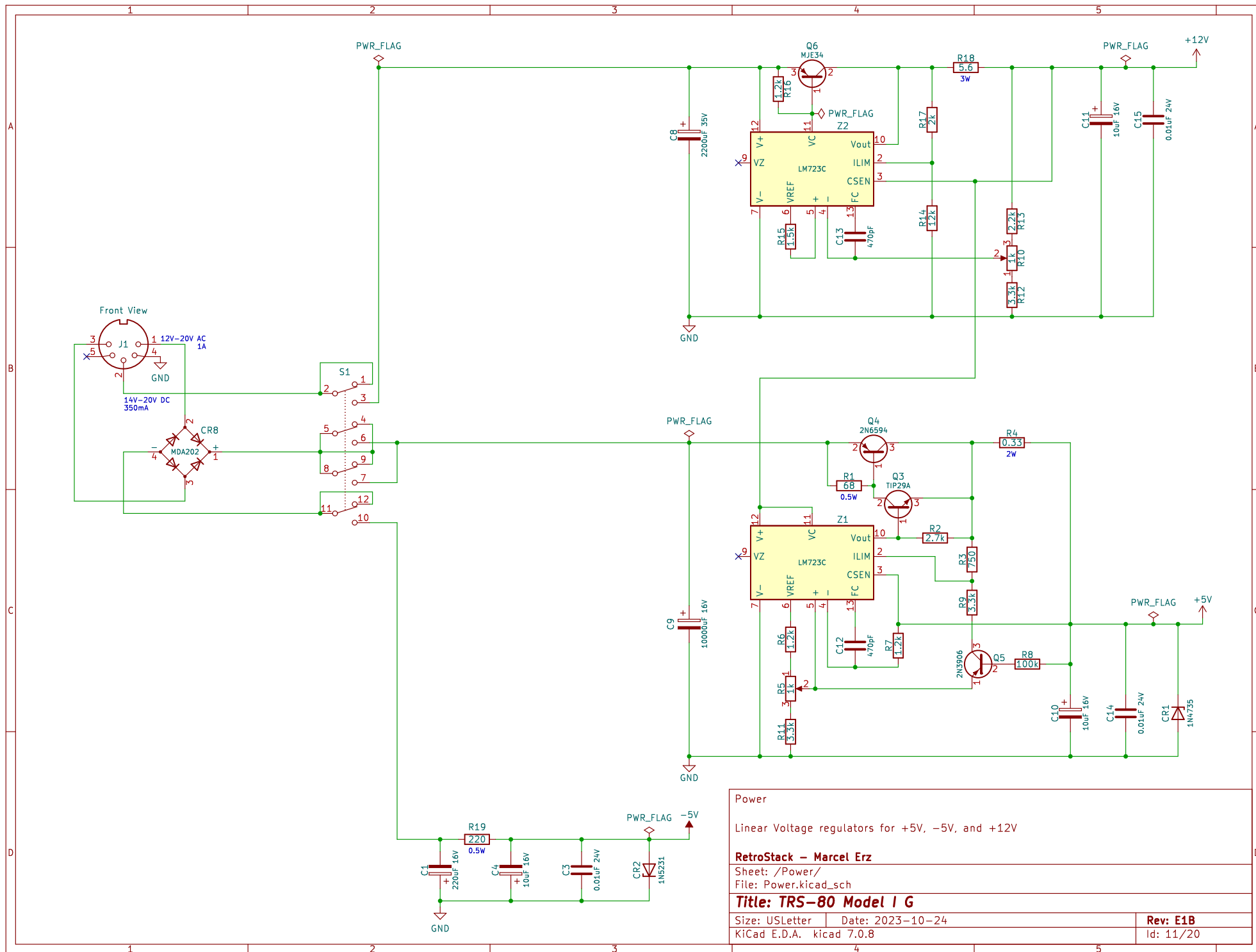
Title: TRS-80 Model I G

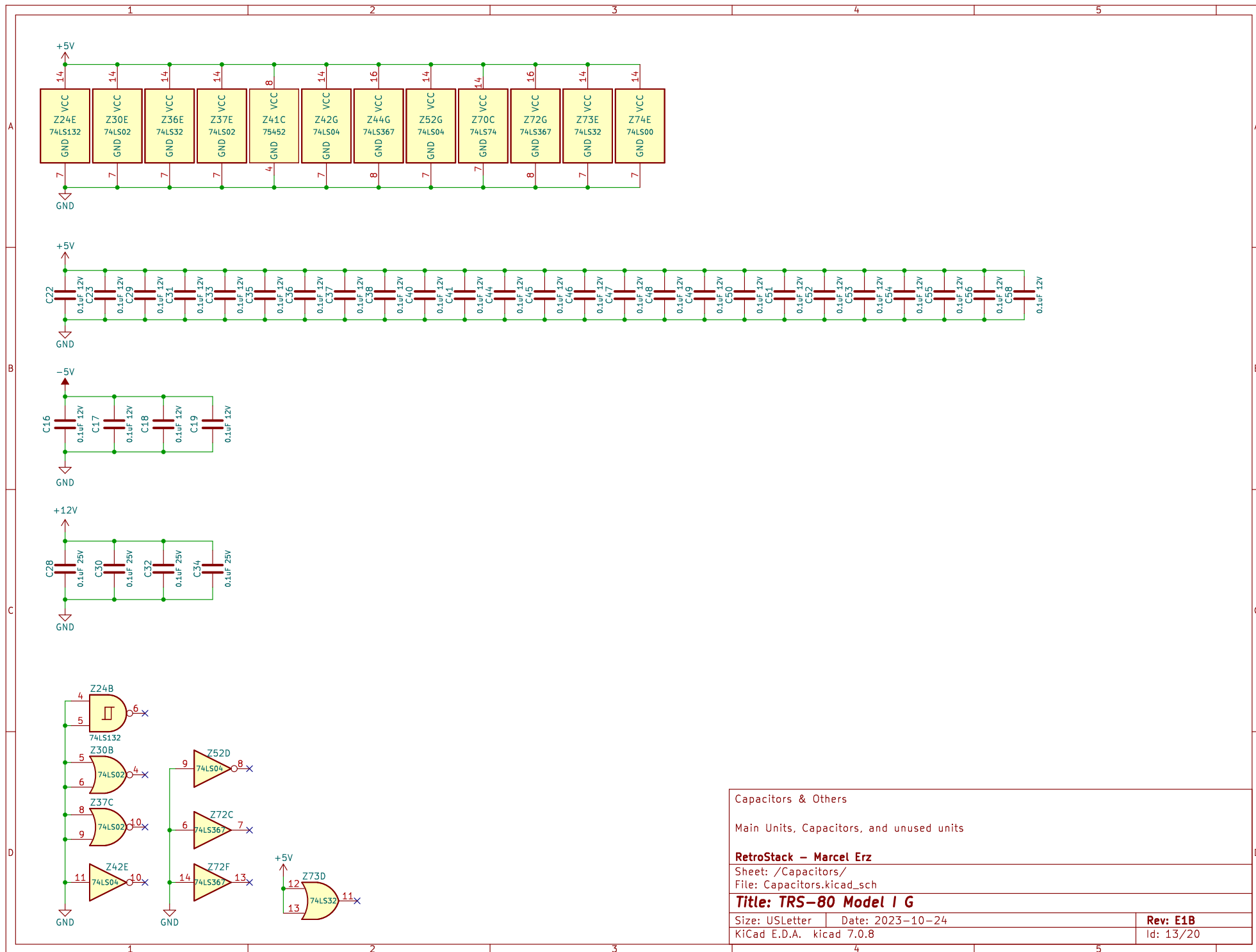
Size: USLetter Date: 2023-10-24

KiCad E.D.A. kicad 7.0.8

Rev: E1B

Id: 10/20





Capacitors & Others	
Main Units, Capacitors, and unused units	
RetroStack – Marcel Erz Sheet: /Capacitors/ File: Capacitors.kicad_sch	
Title: TRS-80 Model I G	
Size: USLetter	Date: 2023-10-24
KiCad E.D.A. kicad 7.0.8	Rev: E1B
	Id: 13/20

