

⇒ ES 215 Assignment 2  
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② Program A: CPU Time = 6s      CPI = 6      Core 1  
 Program B: CPU Time = 5s      CPI = 5      Core 2  
 Clock Rate = 1 GHz (Both cores)

↳ Instruction Count (IC) =  $\frac{\text{CPU Time} \times \text{Clock Rate}}{\text{CPI}}$

↳  $IC_A = \frac{6 \times 10^9}{6} = 10^9$

↳  $IC_B = \frac{5 \times 10^9}{5} = 10^9$

As program runs  
 parallelly on cores 1 & 2  
 Execution time = 6s

↳ Combined Throughput =  $\frac{\text{Total Instruction Count}}{\text{Execution Time}}$   

$$= \frac{IC_A + IC_B}{6} = \frac{2 \times 10^9}{6} = \underline{\underline{3.33 \times 10^8}}$$

∴ Combined throughput of processor =  $3.33 \times 10^8$

③ Processor X  
 Clock Rate = 2 GHz  
 $IC_A = 10 \times 10^9$   
 $CPI_A = 3$

} CPU Time =  $\frac{IC_A \times CPI}{\text{Clock Rate}}$   

$$= \frac{10 \times 10^9 \times 3}{2 \times 10^9} = \underline{\underline{15s}}$$

∴ Program A takes 15s to complete in Processor X



③ Processor Y

$$\left. \begin{array}{l} \text{Clock Rate} = 4 \text{ GHz} \\ \text{IC}_A = 7 \times 10^9 \\ \text{CPI} = 5 \end{array} \right\} \text{CPU Time} = \frac{7 \times 10^9 \times 5}{4 \times 10^9} = \underline{\underline{8.75 \text{ s}}}$$

$$\therefore \text{Speed up} = \frac{\text{CPU Time (Processor X)}}{\text{CPU Time (Processor Y)}} = \frac{15}{8.75} = \underline{\underline{1.714}}$$

$\therefore$  Program A runs 1.714 times faster on Y than X

④

$$\begin{array}{lll} \text{Clock Rate} = \text{CR} & & \\ \text{CR}_{\text{old}} = 1 \times 10^9 \text{ Hz} & \text{IC} = 9 \times 10^9 & \text{CPI} = 1.5 \\ \text{CR}_{\text{new}} = 2 \times 10^9 \text{ Hz} & \text{IC} = 9 \times 10^9 & (\text{Assuming same}) \end{array}$$

$$\hookrightarrow \text{CPU Time}_{\text{old}} = \frac{9 \times 10^9 \times 1.5}{10^9} = \underline{\underline{13.5 \text{ s}}}$$

$$\hookrightarrow \text{CPU Time}_{\text{new}} = \frac{13.5}{2} = \underline{\underline{6.75 \text{ s}}}$$

$$\begin{aligned} \therefore \text{CPI}_{\text{new}} &= \frac{\text{CPU Time}_{\text{new}} \times \text{CR}_{\text{new}}}{\text{IC}_{\text{new}}} \\ &= \frac{6.75 \times 2 \times 10^9}{9 \times 10^9} \\ &= \underline{\underline{0.75}} \end{aligned}$$

$\therefore$  Average CPI of Program A on new design = 0.75



⑤ Total power = 80 W  
 $CR = 2 \times 10^9 \text{ Hz}$   
 Voltage = 5 V

$\Rightarrow$  Assuming  $\frac{\text{Dynamic Power}}{\text{Static Power}} = \frac{4}{1}$

$4x + x = 80 \rightarrow x = 16$   
 $DP_{old} = 64 \text{ W}$   $SP_{old} = 16 \text{ W}$

(a) Dynamic Power  $\propto$  frequency

$\sim N * C * V^2 * f * A$

$DP_{old} = 64 \text{ W}$  (4:1 ratio)

$f_{R \text{ new}} = 5 \times 10^9 \text{ Hz}$

$\therefore \text{Dynamic Power}_{\text{consumed}} = \left( \frac{5 \times 10^9}{2 \times 10^9} \right) \times 64$   
 $= \underline{\underline{160 \text{ W}}}$

(b) Voltage<sub>new</sub> = 2 V

$\hookrightarrow$  Static Power  $\propto$  Voltage

$\sim N * V * e^{-V_t}$

$\therefore \text{Static Power}_{\text{(New)}} = \left( \frac{2}{5} \right) \times 16$   
 $= \underline{\underline{6.4 \text{ W}}}$

$\hookrightarrow$  Dynamic Power  $\propto (\text{Voltage})^2$

$\therefore \text{Dynamic Power}_{\text{(New)}} = \frac{4}{25} \times 64$   
 $= \underline{\underline{10.24 \text{ W}}}$

$\therefore \text{Total Power} = 10.24 + 6.4$   
 $= \underline{\underline{16.64 \text{ W}}}$

$\therefore \text{Fraction of Static Power} = \frac{6.4}{16.64} = 0.3846 = \underline{\underline{38.46\%}}$



# ① MIPS Code : Github Repository

↳ Assumptions (Array initialized by us)

↳ In the code, I have initialized the array for size 10.