

Gate Question

SECTION A

January 31, 2024

1. In the circuit shown, the clock frequency, i.e., the frequency of the clk signal, is 12 KHz. The frequency of the signal at **Q2** is ____ KHz. (GATE-EC2019,25)

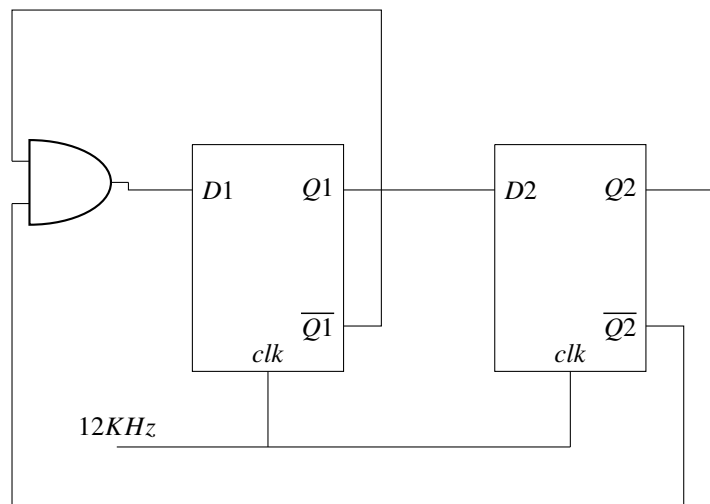


Figure 1: Circuit Daigram