



# REVANTH SAI NANDAMURI

## OBJECTIVE

Seeking entry level full-time opportunities to apply my knowledge and skill in the industry

## CONTACT

**Mobile:**  
+919581262000

**Email:**  
[revanthsai.nandamuri@gmail.com](mailto:revanthsai.nandamuri@gmail.com)

**GitHub:**  
<https://revanthnandamuri1341b0.github.io/>

**LinkedIn:**  
<https://www.linkedin.com/in/revanth-nandamuri/>

**Portfolio:**  
<https://github.com/RevanthNandamuri1341b0>

**Location:**  
Hyderabad, Telangana, India

## EDUCATION:

**B-Tech (Electronics and Communication Engineer)**  
VIT-AP University, Amaravati, Andhra Pradesh  
2018 - 2022  
8.4 CGPA

## STRENGTHS:

- Adaptability
- Self-learner
- Leadership skills

## AREA OF INTEREST:

- Home Automation
- Automation using Python
- Digital Logics

## HOBBIES:

- Watching and Playing Football
- Experimenting New Cuisine
- Making use of unused
- Gardening

## PROJECTS

- 1. Design and Verification Environment for Memory Model (2021)**
  - **Domain:** Verilog, System Verilog and UVM
  - **Description:** Designed a Simple Synchronous Memory Model and Developed SV and UVM Verification Environment for this Synchronous read/write Memory model in which With Implementation of Functional Coverage.
- 2. Verification Environment for Router 4x4 Design (2021)**
  - **Domain:** System Verilog and UVM
  - **Description:** Developed SV and UVM Verification Environment for Router 4x4 DUT in which Out-of-order scoreboard and functional coverage
- 3. Design and Verification Environment for ALU Design (2021)**
  - **Domain:** Verilog, System Verilog and UVM
  - **Description:** Designed an ALU of 4-bit Operator of 16 possible operations and Developed Verifications Environment Using SystemVerilog and UVM and verified using DPI-C Reference Model.
- 4. Design and Verification Environment for UART Design (2021)**
  - **Domain:** Verilog, System Verilog and UVM
  - **Description:** Designed an UART Using Verilog that works at 115200 baud. With a start bit and stop bit. And a parity bit to check the error in data transmission. And Created Verification Environment using both System Verilog and UVM.
- 5. Class Attending bot using Python (2021)**
  - **Domain:** Python
  - **Description:** Developed a bot Using Python that attends the Online classes in the Microsoft Teams Platform As per Users' schedule at pinpointed time, to reduce the issue of not been able to attend on time.
- 6. HOME+ 3.0**
  - **Domain:** Embedded Systems and IoT
  - **Description:** A better and upgraded version of HOME+2.0 which is developed for Controlling Domestic Electrical Utilities using ALEXA. This is developed by interfacing utility switches with ALEXA via NodeMCU, which is programmed in Embedded C Language.

## WORK EXPERIENCE

### Software Development Intern at VISTEON CORP

Jan 2022 – Current

Remote Automation and Testing using Arduino Nano and Raspberry Pi Pico and creating an Interface Using Python.

## INNOVATIONS

- ✓ Self-initiated idea titled "**Portable UV-C Sanitizer**" has been accepted and published as an '**Indian Patent**' with an application number **202041033151**.
- ✓ Self-initiated idea titled "**Smart Electrical Measurement System**" has been accepted and published as an '**Indian Patent**' with an application number **202141026739**.
- ✓ Self-initiated idea titled "**Multimeter on Gloves**" has been accepted and published as an '**Indian Patent**' with an application number **202141030637**.
- ✓ Secured **1st prize**, in **Code-a-thon** contest a 24 hours **Hardware Descriptive Language Hackathon**. Target achieved to the Given Problem statement in **20 Hours**.

## SKILLS

- |                                 |   |
|---------------------------------|---|
| ✓ Programming Languages         | : C, Python   |
| ✓ Methodologies                 | : Universal Verification Methodologies                      |
| ✓ Hardware Descriptive Language | : Verilog, SystemVerilog                                    |
| ✓ Embedded Systems              | : Arduino, NodeMCU, Raspberry Pi                            |
| ✓ Editors/Simulators            | : VSCode, Xilinx Vivado, Arduino IDE<br>ModelSim, QuestaSim |