SECTION NAME	ID	RULE TEXT	DOS NE	G COMMENTARY
AADL Specifications	p41n1	(N1) An AADL specification has one global namespace. The package and property set identifiers		0 Checked when processing package and property set nodes
AADL Specifications	p41n2	(N2) These package and property set identifiers qualify the names of individual elements contained		0
AADL Specifications	p41n3	(N3) Package declarations represent labeled namespaces for component type, component impler		0 0
AADL Specifications	p41n3	(N4) Property set declarations represent labeled namespaces for component type, component imple		0 0
AADL Specifications	p41n5	(N5) Packages and property sets may be separately stored. Those packages and property sets are	-	0 Provided by parser
AADL Specifications	p41n6	(N6) Defining identifiers in AADL must not be one of the reserved words of the language (see Sec	_	0 Provided by parser
AADL Specifications	p41n7	(N7) The AADL identifiers and reserved words can be in upper or lower case (or a mixture of the t	_	0 Provided by realization of AADLIdentifer class
AADL Specifications	p41117	(N8) The AADL does not require that an identifier be declared before it is referenced.		0 Provided by realization of Arabitide ruless
AADE Opecifications	PTITIO	(NO) THE ANDE does not require that an identifier be declared before it is referenced.	0	
Packages	p42n1	(N1) A defining package name consists of a sequence of one or more package identifiers separate	0	0 Checked by counting private and public package declarations
Packages	p42n1	(N2) The public and private section of a package may be declared in separate package declaratio		0 Provided by parser
	p42n3			0 Provided by parser
Packages	p42n3	(N3) Associated with every package is a package namespace that contains the names for all the	_	0 0
Packages		(N4) The package namespace is divided into a public section and a private section. Items declare		
Packages	p42n5	(N5) The reference to an item declared in another package must be an item name qualified with a		0 Can be checked after all possible references are known
Packages	p42n6	(N6) The reference to a property other than predeclared properties must be an property name qua		0 Can be checked after all possible references are known
Packages		(N7) The package name in a import_declaration must exist in the global name space.		0 Checked when processing package imports
Packages	p42n8	(N8) The property set identifier in a import_declaration must exist in the global name space.		0 Checked when processing package imports
Packages	p42n9	(N9) Items declared in the private section of the package can only be referenced from within the p	_	0 Can be checked after all possible references are known
Packages	p42n10	(N10) If the qualifying package identifier of a qualified reference is missing, the referenced compor		0
Packages	p42n11	(N11) The package name referenced in an alias_declaration must exist in the global namespace a	_	0 Checked when processing package aliases
Packages	p42n12	(N12) The classifier referenced by the alias_declaration must exist in the name space of the public		0 Checked when processing classifier aliases
Packages	p42n13	(N13) The classifier referenced by the alias declaration must refer to a component type or a feature		0 Provided by parser
Packages	p42n14	(N14) The defining identifier of an alias_declaration must be unique in the namespace of the packa	_	Checked when processing package and classifier aliases
Packages	p42n15	(N15) The alias_declaration makes the publicly visible identifier of classifiers declared in another p	_	Checked when processing classifier aliases
Packages	p42n16	(N16) If the alias_declaration renames all publicly visible identifiers of component types and feature		0
Packages	_	(N17) The identifiers introduced by the alias_declaration are only accessible within the package. W		Can be checked after all poible references are known
Packages	_	(N18) The alias declared for a component type can be used instead of a qualified component type		0
Packages	p42l1	(L1) The defining package name following the reserved word end must be identical to the defining		0 Provided by parser
Packages	p42l2	(L2) For each package there may be at most one public section declaration and one private section		Checked with p42n1
Packages	p42l3	(L3) A component implementation may be declared in both the public and private part of a packag		Should be checked in component implementations
Packages	p42l4	(L4) The component category in an alias declaration must match the category of the referenced co	0	Checked when processing classifier aliases
Component Types	p43n1	(N1) The defining identifier for a component type must be unique in the namespace of the packag	_	Checked when creating local namespaces of the packages, BUT: currently no checking of possible intersection between public and private
Component Types	p43n2	(N2) Each component type has a local namespace for defining identifiers of prototypes, features,	0	Checked when creating local namespaces of component types
Component Types	p43n3	(N3) The component type identifier of the ancestor in a component type extension, i.e., that appear	0	
Component Types	p43n4	(N4) When a component type extends another component type, a component type namespace in	0	
Component Types	p43n5	(N5) A component type that extends another component type does not include the identifiers of th	0	
Component Types	p43n6	(N6) The defining identifier of a feature, flow specification, mode, mode transition, or prototype mu	0	0
Component Types	p43n7	(N7) The refinement identifier of a feature, flow specification, or prototype refinement refers to the	0	0
Component Types	p43n8	(N8) The prototypes referenced by prototype binding declarations must exist in the local namespa	0	0
Component Types	p43n9	(N9) Mode transitions declared in the component type may not refer to event or event data ports of	0	0
Component Types	p43l1	(L1) The defining identifier following the reserved word end must be identical to the defining identi		0 Provided by parser
Component Types	p43l2	(L2) The prototypes, features, flows, modes, and properties subclauses are optional. If a subclaus		0 Provided by parser(kinda, error is - "No viable alternative")
Component Types	p43l3	(L3) The category of the component type being extended must match the category of the extending	0	0
Component Types	p43l4	(L4) The classifier being extended in a component type extension may include prototype bindings	0	0
Component Types	p43l5	(L5) A component type must not contain both a requires_modes_subclause and a modes_subclau		0 Provided by parser(kinda, error is - extraneous input 'requires' expecting {'ANNEX', 'END', 'PROPERTIES', IDENTIFIER})
Component Types	p43l6	(L6) If the extended component type and an ancestor component type in the extends hierarchy cor	0	
Component Implementations	p44n1	(N1) A component implementation name consists of a component type identifier and a component	0	0
Component Implementations	p44n2	(N2) The defining identifier of the component implementation must be unique within the local nam	0	
Component Implementations	p44n3	(N3) Every component implementation defines a local namespace for all defining identifiers of pro	0	0

Component Implementations	p44n4	(N4) This local namespace inherits the namespace of the associated component type, i.e., definin 0 0
Component Implementations	p44n5	(N5) Refinement identifiers of features must exist in the namespace of the associated component 0 0
Component Implementations	p44n6	(N6) In a component implementation extension, the component type identifier of the component in 0 0
Component Implementations	p44n7	(N7) When a component implementation extends another component implementation, the local ne 0 0
Component Implementations	p44n8	(N8) Within the scope of the component implementation, subcomponent declarations, connections 0 0
Component Implementations	p44n9	(N9) The prototype referenced by the prototype binding declaration must exist in the local names 0 0
Component Implementations	p44l1	(L1) The pair of identifiers separated by a dot (ε̄ъъ.ε̄ъκ̂) following the reserved word end must be 0 0
Component Implementations	p44l2	(L2) The prototypes, subcomponents, connections, calls, flows, modes, and properties subclauses 0 0
Component Implementations	p44l3	(L3) The category of the component implementation must be identical to the category of the comp 0 0
Component Implementations	p44l4	(L4) If the component implementation extends another component implementation, the category o 0 0
Component Implementations	p44l5	(L5) The classifier being extended in a component implementation extension may include prototyg 0 0 0
Component Implementations	p44l6	(L6) If the component type of the component implementation contains a requires_modes_subclau 0 0
Component Implementations	p44I7	(LT) If modes are declared in the component type, then modes cannot be declared in component 0 0
Component Implementations	p44l8	(L8) If modes or mode transitions are declared in the component type, then mode transitions can 1 0 0
	_	
Component Implementations	p44I9	(L9) The category of a subcomponent being refined must match the category of the refining subcd 0 0
Component Implementations	p44l10	(L10) For all other refinement declarations the categories must match (see the respective sections). 0 0
Component Implementations	p44111	(L11) Component implementations and component implementation extensions must not refine pro 0 0 0
O the control of		
Subcomponents	p45n1	(N1) The defining identifier of a subcomponent declaration placed in a component implementation 0 0
Subcomponents	p45n2	(N2) The defining identifier of a subcomponent refinement must exist as a defining subcomponent 0 0
Subcomponents	p45n3	(N3) The component type identifier or the component implementation name of a component class 0 0
Subcomponents	p45n4	(N4) The prototype identifier of a prototype reference must exist in the local name space of the co 0 0 0
Subcomponents	p45n5	(N5) The prototype referenced by the prototype binding declarations must exist in the local names 0 0
Subcomponents	p45n6	(N6) The modes named in the in modes statement of a subcomponent must refer to modes in the 0 0
Subcomponents	p45l1	(L1) The category of the subcomponent declaration must match the category of its corresponding 0 0
Subcomponents	p45l2	(L2) The component classifier reference of a subcomponent declaration may include prototype bir 0 0
Subcomponents	p45l3	(L3) In a subcomponent refinement declaration the component category may be refined from abst 0 0
Subcomponents	p45l4	(L4) The Classifier_Substitution_Rule property specifies the rule to be applied when a refinement 0 0
Subcomponents	p45l5	(L5) In the case of a signature match, the component type of the subcomponent being refined must 0 0 0
Subcomponents	p45l6	(L6) The component category and optional component classifier or prototype reference can be foli 0 0
Subcomponents	p45l7	(L7) The array size specification for the dimensions is optional. In this case the array declaration is 0 0
Subcomponents	p45l8	(L8) When refining a subcomponent array the number of dimensions of the array cannot be chang 0 0
Subcomponents	p45l9	(L9) When the subcomponent is declared as an array with array dimension sizes then a list of con 0 0
Subcomponents	p45l10	(L10) Selecting index ranges in one or more dimensions of an array is only possible if the size of tt 0 0
Subcomponents	p45l11	(L11) An array element implementation list is valid only if (a) the subcomponent classifier is a com; 0 0
Subcomponents	p45c1	(C1) The classifier of a subcomponent cannot recursively contain subcomponents with the same o 0 0
Abstract Components	p46I1	(L1) An abstract component type declaration can contain feature declarations (including abstract f 0 0 0
Abstract Components	p46l2	(L2) An abstract component implementation can contain subcomponent declarations of any cated 0 0 0
Abstract Components	p46l3	(L3) An abstract component implementation can contain a modes subclause, a connections subcl 0 0
Abstract Components	p46l4	(L4) An abstract subcomponent can be contained in the implementation of any component catego 0 0
Abstract Components	p46l5	(L5) If an abstract subcomponent is refined to a concrete category, the concrete category must be 0 0
Abstract Components	p46l6	(L6) An abstract subcomponent can be declared as an array of subcomponents. 0 0
Abstract Components	p46l7	(LT) If an abstract component type is refined to a concrete category, the features, modes, and flow 0 0
Abstract Components	p46l8	(L8) If an abstract component implementation is refined to a concrete category, the relatives, modes, and now 0 0 0
Abstract Components	P+OIO	Lety in an abstract component implementation is relined to a condition discountry in a subcomponent of the
Drototypoo	n47-4	(N1) The prototype identifier on the left hand side of a prototype hinding must exist in the level on
Prototypes	p47n1	(N1) The prototype identifier on the left-hand side of a prototype binding must exist in the local nat 0 0
Prototypes	p47n2	(N2) The prototype identifier on the right-hand side of a prototype binding, if present, must exist in 0 0
Prototypes	p47n3	(N3) Unique component classifier references must exist in the public section of the package being 0 0
Prototypes	p47n4	(N4) Unique feature group type references must exist in the public section of the package being id 0 0 0
Prototypes	p47I1	(L1) The component category declared in the component prototype binding must match the comp 0 0
Prototypes	p47l2	(L2) The component category of the optional component classifier reference in the prototype dect 0 0

sortings p.27 S. 15. We are compared problems included a component full contents of problems p.27 S. 15. The compared for component missanches in the contents of problems p.27 S. 15. The compared for component missanches in the contents of problems p.27 S. 15. The compared for the compared for the contents of problems p.27 S. 15. The compared for the compared for the contents of problems p.27 S. 15. The compared for the compared for the contents of problems p.27 S. 15. The content of these problems p.27 S. 15. The content of the p.27 S. 15. The content of the problems p.27 S. 15. The content of the problems p.27 S. 15. The content of the p.27 S.		
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Accepted positions of the companies always of the description effective in a positivity time of the companies of an appropriate processing and appropriate processing appropriate processing and appropriate processing appropriate processing appropriate processing and appropriate processing	Prototypes p47l8	(L8) A classifier supplied in a feature prototype binding must match the classifier of the prototype 0 0
Appear part 1 (a.1) if a section is specified from a baskent decision is protective decision to the decision of protection of the protecti	Prototypes p47l9	
Arring Schoolses and Annex Library School (1). The annex steedings must only be disrepted entropy and the production of	Prototypes p47l	0 (L10) The component category of the classifier reference or prototype reference in a prototype bin 0 0
interest Suddanses and Annex Library (ASC) ACC Subclasses and Annex Library (ASC) ACC Acceptored Interestation reprocess that not one same to progress the progress and subclasses and Annex Library (ASC) ACC ACCEPTANCE AND ACCEPTANCE	Prototypes p47l	(L11) If a direction is specified for an abstract feature in a prototype declaration, then the direction 0 0
nes Subclausers and Annex Library (MD) 1. No The annex described would be the name of an approved annex or a propertie secolic identified. 2. The model described in the	Prototypes p47l	Component prototype bindings must only bind component prototypes, feature group prototyp 0 0
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new Subclauses and Annex Library 648 0 12). Annex present place component plac	Annex Subclauses and Annex Librarie p48n	2 (N2) The mode identifiers in the in_modes statement must refer to modes in the component type of 0 0
interest Subclasses and Annex Library 483	Annex Subclauses and Annex Librarie p481	(L1) Annex subclauses can only be declared in component types, component implementations, ar 0 0
tes (4) 11 (4) A data type declaration may contain at most one annex (trany declaration for each annex (5) 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	Annex Subclauses and Annex Librarie p4812	(L2) A component type, component implementation, or feature group type declaration may contain 0 0
to position of the program and Subprogram Calls position (A). The defining identifier of a subprogram and subprogram and Subprogram Calls position (A). The defining identifier of a subprogram and subprogram call subprogram Calls position (A). The defining identifier of a subprogram call sequence declaration must be unique within the local name become and Subprogram Calls position. Subprograms and Subprogram Calls position. (A) The defining identifier of a subprogram call sequence declaration must be unique within the local name become and Subprogram Calls position. Subprograms and Subprogram Calls position. (A) The subprogram calls position (A) in the subprogram call sequence and subprogram call subprogram call sequence and subprogram call subprogram call sequence call subprogram call subprogram calls position. (A) The subprogram calls position (A) in the subprogram call sequence call subprogram subprogram subprogram subprogram subprogram subprogram subprogram subprogram calls position. (A) Sill the called subprogram call sequence call position. (A) Sill the called subprogram call sequence call position. (A) Sill the called subprogram call sequence call position. (A) Sill the called subprogram call sequence call position. (A) Sill the called subprogram call sequence call position. (A) Sill the called subprogram call sequence call position. (A) Sill the called subprogram call sequence call position can contain better called subprogram call sequence call position. (A) Sill the called subprogram call sequence call position can contain better call position called	Annex Subclauses and Annex Librarie p4813	(L3) Annex libraries must be declared in packages.
policy (2) A data type declaration must not contain a flow specification or mode a subclause. policy (3) A data implementation can contain a flow specification or mode a subclause. programs and Subprogram Calis (5) 14 (3) A data implementation must not contain a flow implementation, an evide end flow specification or programs and Subprogram Calis (5) 14 (3) A data implementation must not contain a flow implementation, an evide end flow specification or programs and Subprogram Calis (5) 20 (2) The defining stendifier of a subprogram call sequence declaration must be unique within the 5 (2) The defining stendifier of a subprogram call sequence declaration must be unique within the 5 (2) The defining stendifier of a subprogram call sequence declaration must be unique within the 5 (2) The defining stendifier of a subprogram call sequence declaration must be unique within the 5 (2) The defining stendifier of a subprogram call sequence declaration must be unique within the 5 (2) The defining stendifier of a subprogram call sequence declaration must be unique within the 5 (2) The defining stendifier of a subprogram call sequence declaration must be unique within the 5 (2) The defining stendifier of a subprogram call sequence declaration must be unique within the 5 (2) The defining stendifier of a subprogram call sequence of the subprogram call sequence of the subprogram call sequence of the subprogram program call subprogram calls subprogram call sequence on a pulp to a subprogram call sequence on a subprogram call sequence of the subprogram calls subprogram call sequence calls subprogram calls subprogram calls subprogram calls subp	Annex Subclauses and Annex Librarie p48l4	(L4) A package declaration may contain at most one annex library declaration for each annex. 0 0
policy (2) A data type declaration must not contain a flow specification or modes subclience. policy (3) A data implementation can contain a flow specification or modes subclience. programs and Subprogram Calis (5) A data implementation must not contain a flow implementation, an enable and thus specification or programs and Subprogram Calis (5) A data implementation must not contain a flow implementation must be unique within the lost name or programs and Subprogram Calis (5) California (5) California (6) Cal		
13 (3) A data implementation can contain a before, data and subprogram subcomponents, access of the base of the ba	Data p511	(L1) A data type declaration can contain provides subprogram access declarations as well as prog. 0 0
programs and Subprogram Calls posses and Subprogram Call posses and Subpr	Data p5112	2 (L2) A data type declaration must not contain a flow specification or modes subclause. 0 0
programs and Subprogram Calls p52n2 (N2) The defining identifier of a subprogram call declaration must be unique within the local name programs and Subprogram Calls p52n2 (N3) If the called subprogram mane is a subprogram call declaration must be unique within the local name programs and Subprogram Calls p52n3 (N3) If the called subprogram mane is a subprogram call subprogram Calls p52n5 (N3) If the called subprogram mane is a subprogram call subprogram Calls p52n5 (N4) If the called subprogram mane is a subprogram call subprogram Calls p52n5 (N4) If the called subprogram mane is a subprogram subcomponent reference, the subprogram type reference, 0.0 (N4) If the called subprogram mane is a require subprogram subcomponent reference, the subprogram of the subprogram calls p52n5 (N4) If the called subprogram mane is a subprogram subcomponent reference, the subprogram is subprogram and subprogram Calls p52n5 (N4) If the called subprogram mane is a subprogram subcomponent reference, the subprogram is p52n5 (N4) If the called subprogram mane is a subprogram subcomponent reference, the subprogram is p52n5 (N4) If the called subprogram mane is a subprogram pass subcomponent reference, the subprogram is p52n5 (N4) If the called subprogram mane is a subprogram pass subprogram p52n5 (L2) A subprogram gas subprogram pass subprogram	Data p51K	3 (L3) A data implementation can contain abstract, data and subprogram subcomponents, access c 0 0
bipograms and Subprogram Calls 5202 (N2) The defining identifier of a subprogram claus program and Subprogram Calls 5254 (N4) The subprogram name is a subprogram data may be a subprogram type reference. It is composed to the control of the called subprogram name is a subprogram data may be a subprogram type reference. It is composed to the control of the called subprogram name is a subprogram data may be a subprogram type reference. It is control of the called subprogram name is a subprogram and may be a subprogram type reference. It is control of the called subprogram name is a subprogram account of the called subprogram name is a subprogram account of the called subprogram name is a subprogram account of the called subprogram name is a subprogram account of the called subprogram name is a subprogram account of the called subprogram name is a subprogram account of the called subprogram name is a subprogram account of the called subprogram name is a subprogram account of the called subprogram name is a subprogram account of the called subprogram name is a subprogram account of the called subprogram name is a subprogra	Data p51k	(L4) A data implementation must not contain a flow implementation, an end-to-end flow specificati 0 0
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bprograms and Subprogram Calls pp.52nd (N4) The subprogram classifier reference of a subprogram subcomponent reference, the subprogram subcomponent subcomponent reference, the subprogram subcomponent subcomponent reference, the subprogram subcomponent subcomponent reference, the requires subtraction of the subprogram subcomponent reference, the requires subtraction of the subprogram subcomponent subcomponent subcomponent subcomponent subcomponent subcomponent subcomponent subcomponent subprogram subcomponent subcomponent subprogram subprogram subcomponent subprogram subprogram subcomponent subprogra	Subprograms and Subprogram Calls p52n	2 (N2) The defining identifier of a subprogram call declaration must be unique within the local name 0 0
programs and Subprogram Calls p526 (N5) If the called subprogram name is a subprogram subcomponent reference, the subprograms and Subprogram Calls p526 (N5) If the called subprogram name is a subprogram access reference, the requires sub; 0 0 portions and Subprogram Calls p521 (L1) A subprogram injeries subprogram access reference, the requires sub; 0 0 portions and Subprogram Calls p522 (L2) A subprogram injeries subprogram access reference, the requires sub; 0 0 portions and Subprogram Calls p523 (L3) Only one subprogram call sequence can apply to a given mode. **Bed of the called subprogram call sequence can apply to a given mode. **Bed of the called subprogram call sequence can apply to a given mode. **Bed of the called subprogram call sequence can apply to a given mode. **Bed of the called subprogram call sequence can apply to a given mode. **Bed of the called subprogram call sequence can apply to a given mode. **Bed of the called subprogram call sequence can apply to a given mode. **Bed of the called subprogram call sequence can apply to a given mode. **Bed of the called subprogram call sequence can apply to a given mode. **Bed of the called subprogram call sequence can apply to a given mode. **Bed of the called subprogram call sequence can apply to a given mode. **Bed of the called subprogram call sequence can apply to a given mode. **Bed of the called subprogram call sequence can apply to a given mode. **Bed of the called subprogram call sequence can apply to a given mode. **Bed of the called subprogram call sequence can apply to a given mode. **Bed of the called subprogram call sequence can apply to a given mode. **Bed of the called subprogram call sequence can apply to a given mode. **Bed of the called subprogram call sequence can apply to a given mode. **Bed of the called subprogram call sequence can apply to a given mode. **Bed of the called subprogram call sequence can apply to a given mode. **Bed of the called subprogram call sequence can apply to a given mode. **Bed of the ca	Subprograms and Subprogram Calls p52n	3 (N3) If the called subprogram name is a subprogram classifier reference, its component type iden 0 0
programs and Subprogram Calls p5216 (N5) If the called subprogram name is a requires subprogram access reference, the requires subprograms and Subprogram Calls p5211 (L1) A subprogram type declaration can contain parameter, out event port, out event data port, and port port of the program of Subprogram Calls p5213 (L2) A subprogram implementation can contain abstract, subprogram, and data subcomponents, and subprogram Calls p5233 (L3) Only one subprogram call sequence can apply to a given mode. Uniformly programs and Subprogram Calls p5210 (C1) The reference to a provides subprogram access of a processor in a subprogram call process of a processor in a subprogram call proc	Subprograms and Subprogram Calls p52n	4 (N4) The subprogram classifier reference of a subprogram call may be a subprogram type reference. 0 0
bprograms and Subprogram Calls p52!1 (L.1) A subprogram type declaration can contain parameter, out event data port, and programs and Subprogram Calls p52!2 (L.2) A subprogram injetementation can contain abstract, subprogram, and data subcomponents, and contain abstract, subprogram, and data subcomponents, and contain abstract, subprogram, and data subcomponents, and contain abstract, subprogram, and subprogram Calls p52:1 (L.3) Only one subprogram call supprogram calls p52:2 (L.3) Only one subprogram group provides a local namespace of a subprogram group port of the subprogram declaration in the package names D.3 Only one subprogram p53:3 (N.1) The defining identifier of a subprogram group type extension includes the defining identifiers of D.3 Only one subprogram p53:3 (N.3) The local namespace of a subprogram group type extension includes the defining identifiers of D.3 Only one subprogram p53:3 (N.3) The local namespace of a subprogram group type extension includes the defining identifiers of D.3 Only one subprogram p53:3 (N.3) The local namespace of a subprogram group type extension includes the defining identifiers of D.3 Only one subprogram p53:3 (N.3) The local namespace of a subprogram group type extension includes the defining identifier of D.3 Only one	Subprograms and Subprogram Calls p52n	5 (N5) If the called subprogram name is a subprogram subcomponent reference, the subprogram su
bprograms and Subprogram Calls p5212 (L2) A subprogram implementation can contain abstract, subprogram, and data subcomponents, e 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	Subprograms and Subprogram Calls p52n	6 (N6) If the called subprogram name is a requires subprogram access reference, the requires subp 0 0
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bprogram Groups and Subprogram p53n1 (N1) The defining identifier of a subprogram group type must be unique within the package names 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	Subprograms and Subprogram Calls p52l3	(L3) Only one subprogram call sequence can apply to a given mode.
bprogram Groups and Subprogram p53n1 (N1) The defining identifier of a subprogram group type must be unique within the package names 0 0 bprogram Groups and Subprogram p53n2 (N2) Each subprogram group provides a local namespace. The defining subprogram identifiers of 0 0 0 por por groups and Subprogram p53n3 (N3) The local namespace of a subprogram group type extension includes the defining identifiers 0 0 por groups and Subprogram p53n4 (N4) The defining subprogram identifiers of subprogram access feature declarations in feature group type groups and Subprogram p53n5 (N5) The package name of the unique subprogram group type reference must refer to a package 0 0 por groups and Subprogram p53n1 (L1) A subprogram group type can contain provides and requires subprogram group, and subprogram group p53n2 (L2) A subprogram group implementation can contain abstract, data, subprogram group, and subprogram group and Subprogram g53n3 (L3) A subprogram group type or implementation may contain zero or more subcomponent declar 0 por groups and Subprogram g53n3 (L3) A subprogram group type or implementation may contain zero or more subcomponent declar 0 por group group group group group group type or implementation may contain zero or more subcomponent declar 0 por group group group group group group group group, requires data access declarations, 0 por group group group group group group group group group, requires data access declarations, 0 por group	Subprograms and Subprogram Calls p52c	1 (C1) The reference to a provides subprogram access of a processor in a subprogram call (process 0 0
bprogram Groups and Subprogram p53n2 (N2) Each subprogram group provides a local namespace. The defining subprogram identifiers of bprogram Groups and Subprogram p53n3 (N3) The local namespace of a subprogram group type extension includes the defining identifiers of bprogram Groups and Subprogram p53n3 (N4) The defining subprogram identifiers of subprogram access feature declarations in feature group bprogram Groups and Subprogram p53n5 (N5) The package name of the unique subprogram group type reference must refer to a package bprogram Groups and Subprogram p53l1 (L1) A subprogram group type can contain provides and requires subprogram access, and provide bprogram Groups and Subprogram p53l2 (L2) A subprogram group implementation can contain abstract, data, subprogram group, and subprogram p53l3 (L3) A subprogram group type or implementation may contain zero or more subcomponent declar of the position of the posi	Subprograms and Subprogram Calls p52c	2 (C2) A subprogram call may reference a subprogram classifier. A project may enforce a consistent 0 0
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bprogram Groups and Subprogram p53n4 (N4) The defining subprogram identifiers of subprogram access feature declarations in feature group por program Groups and Subprogram p53n5 (N5) The package name of the unique subprogram group type reference must refer to a package program Groups and Subprogram p53l1 (L1) A subprogram group type can contain provides and requires subprogram access, and provide program Groups and Subprogram p53l2 (L2) A subprogram group implementation can contain abstract, data, subprogram group, and subprogram group p53l3 (L3) A subprogram group type or implementation may contain zero or more subcomponent declar p54l1 (L1) A thread type declaration can contain port, feature group, requires data access declarations, possible por possible po	Subprogram Groups and Subprogram p53n	2 (N2) Each subprogram group provides a local namespace. The defining subprogram identifiers of 0 0
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bprogram Groups and Subprogram p53l1 (L1) A subprogram group type can contain provides and requires subprogram access, and provide 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	Subprogram Groups and Subprogram p53n	4 (N4) The defining subprogram identifiers of subprogram access feature declarations in feature grd 0 0
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borogram Groups and Subprogram p53l3 (L3) A subprogram group type or implementation may contain zero or more subcomponent declar 0 0 reads p54l1 (L1) A thread type declaration can contain port, feature group, requires data access declarations, 0 0	Subprogram Groups and Subprogram p531	(L1) A subprogram group type can contain provides and requires subprogram access, and provide 0 0
reads p54l1 (L1) A thread type declaration can contain port, feature group, requires data access declarations, 0 0	Subprogram Groups and Subprogram p53l2	(L2) A subprogram group implementation can contain abstract, data, subprogram group, and subt
	Subprogram Groups and Subprogram p53l3	(L3) A subprogram group type or implementation may contain zero or more subcomponent declar 0 0
eads Ip54(2 I/L2). A thread component implementation can contain abstract, data, subprogram, and subprogram = 0 0		
reads p5413 (L3) The Complete out event port, and Error out event data port are predeclared, i.e., are implicitly 0 0	Threads p54K	(L3) The Complete out event port, and Error out event data port are predeclared, i.e., are implicitly 0 0
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reads p54c4 (C4) The Period property must have a value if the Dispatch_Protocol property value is periodic, sp 0 0	Threads p54d	4 (C4) The Period property must have a value if the Dispatch_Protocol property value is periodic, sp 0 0
read Groups p5511 (L1) A thread group component type can contain provides and requires data access, as well as pd 0 0		

Thursd Occurs	- 5510	(C) A thread area and the large state of the		
Thread Groups	p55l2	(L2) A thread group component implementation can contain abstract, data, subprogram, subprogr	0	7
Thread Groups	p55l3	(L3) A thread group implementation can contain a connections subclause, a flows subclause, a m	0	4
Thread Groups	p55l4	(L4) A thread group must not contain a subprogram calls subclause.	0	0
Drassess	- FOI4	(14) A process component true can contain part for the contain and the contain		
Processes	p56l1	(L1) A process component type can contain port, feature group, provides and requires data acces	_	0
Processes	p56l2	(L2) A process component implementation can contain abstract, data, subprogram, subprogram g	0	0
Processes	p56l3	(L3) A process implementation can contain a connections subclause, a flows subclause, a modes	0	4
Processes	p56l4	(L4) A thread group must not contain a subprogram calls subclause.	0	U
Processes	p56c1	(C1) The complete source text associated with a process component must form a complete and le	_	0
Processors	p61I1	(L1) A processor component type can contain port, feature group, provides subprogram access, p	_	4
Processors	p61l2	(L2) A processor component implementation can contain declarations of memory, bus, virtual bus	0	0
Processors	p61l3	(L3) A processor implementation can contain a modes subclause, flows subclause, and a properti	0	이
Processors	p61l4	(L4) A processor implementation can contain bus access, subprogram access, subprogram group	0	의
Processors	p61l5	(L5) A processor implementation must not contain a subprogram calls subclause.	0	0
				┙
Virtual Processors	p62l1	(L1) A virtual processor component type can contain port, feature group, provides subprogram ac	0	0
Virtual Processors	p62l2	(L2) A virtual processor component implementation can contain declarations of virtual bus, virtual	0	0
Virtual Processors	p62l3	(L3) A virtual processor implementation can contain a modes subclause, flows subclause, and a p	0	0
Virtual Processors	p62l4	(L4) A virtual processor implementation must not contain a subprogram calls subclause.	0	0
Virtual Processors	p62l5	(L5) A virtual processor implementation can contain subprogram access, subprogram group access	0	0
Virtual Processors	p62c1	(C1) In a fully bound system every virtual processor must be directly or indirectly bound to, or directly	0	0
Virtual Processors	p62c2	(C2) In a fully deployed system a requires virtual bus binding of a virtual processor specified by the	0	0
Memory	p63l1	(L1) A memory type can contain bus access declarations, feature groups, a modes subclause, an	0	0
Memory	p63l2	(L2) A memory implementation can contain abstract, memory, and bus subcomponent declaration	_	0
Memory	p63l3	(L3) A memory implementation can contain a modes subclause and property associations.	0	<u> </u>
•	p63l4		0	
Memory Memory	p63l5	(L4) A memory implementation can contain bus access connection declarations.	_	+
wemory	hooio	(LS) A memory implementation must not contain nows subclause, or supprogram calls subclause.	-	4
Pugge	n6.414	((4) A hus hing can have required hus concerned a territorial and a substance of the same		
Buses	p64I1	(L1) A bus type can have requires bus access declarations, a modes subclause, and property ass	0	븻
Buses	p64l2	(L2) A bus type must not contain any flow specifications.		0
Buses	p64l3	(L3) A bus implementation can contain virtual bus and abstract subcomponent declarations.	0	미
Buses	p64l4	(L4) A bus implementation can contain a modes subclause and property associations.	0	의
Buses	p64l5	(L5) A bus implementation must not contain flows subclause, or subprogram calls subclause.	0	0
Virtual Buses	p65l1	(L1) A virtual bus type can have property associations.	0	0
Virtual Buses	p65l2	(L2) A virtual bus type must not contain flow specifications.	0	0
Virtual Buses	p65l3	(L3) A virtual bus implementation can contain virtual bus subcomponent declarations.	0	0
Virtual Buses	p65l4	(L4) A virtual bus implementation can contain a modes subclause and property associations.	0	0
Virtual Buses	p65l5	(L5) A virtual bus implementation must not contain a connections subclause, flows subclause, or s	0	0
Virtual Buses	p65c1	(C1) In a fully deployed system virtual buses must be directly or indirectly bound to processors or t	0	0
				\neg
Devices	p66l1	(L1) A device type can contain port, feature group, provides subprogram access, provides subpro	0	0
Devices	p66l2	(L2) A device component implementation must not contain a subprogram calls subclause.	0	1
Devices	p66l3	(L3) A device component implementation must not contain a subprogram cans subclause.	0	=
Devices	poolo	(LO) A device implementation can contain abstract, data, virtual bus, and bus subcomponents, bus		4
Systems	p7111	(/ 1) A gyatam companent type can centain subpregram group data and two secret		+
Systems	p71l1	(L1) A system component type can contain subprogram, subprogram group, data and bus access		0
Systems	p71l2	(L2) A system component implementation can contain abstract, data, subprogram, subprogram gr		0
Systems	p71I3	(L3) A system implementation can contain a modes subclause, a connections subclause, a flows	-	0
Systems	p71l4	(L4) A thread group must not contain a subprogram calls subclause.		0
Systems	p71n1	(N1) The defining identifier of a feature must be unique within the namespace of the associated co		0
Systems	p71n2	(N2) Thread features may not be declared using the predeclared ports names Complete or Error.	0	0

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Systems	p71n3	(N3) Each refining feature identifier that appears in a feature refinement declaration must also app			-
Systems	p71n4	(N4) A feature is referenced in one of two ways. Within the component implementations for a com	_		0
Systems	p71n5	(N5) The path of a contained property association for a feature must refer to an element of a feature	_		
Systems	p71l1	(L1) Each feature can be refined at most once in the same type extension.	0		0
Systems	p71l2	(L2) A feature refinement declaration of a feature and the original feature must both be declared a	_		0
Systems	p71I3	(L3) Feature arrays must only be declared for threads, devices, and processors.	0	(0
Systems	p71l4	(L4) If the feature refinement specifies an array dimension, then the feature being refined must ha	0	(0
Systems	p71I5	(L5) If the refinement specifies an array dimension size, then the feature being refined must not have	0	(0
Systems	p71l6	(L6) A contained property association must only be used when the feature is a feature group.	0	(0
Systems	p71I7	(L7) In the case of a feature with a classifier reference, the classifier of the refined feature declara	0	(0
Abstract Features	p81I1	(L1) The feature direction in a refined feature declaration must be identical to the feature direction	0	(0
Abstract Features	p81l2	(L2) If the direction of an abstract feature is specified, then the direction must be satisfied by the re	0	(0
Abstract Features	p81I3	(L3) An abstract feature with a feature prototype identifier and the prototype being referenced mus	0	(0
Abstract Features	p81I4	(L4) An abstract feature refinement declaration of a feature with a feature prototype reference must	0	(0
	1				
Feature Groups and Feature Group T	n82n1	(N1) The defining identifier of a feature group type must be unique within the package namespace	0	(0
Feature Groups and Feature Group T	1	(N2) Each feature group type provides a local namespace. The defining identifiers of prototype, fe			
Feature Groups and Feature Group T	1	(N3) The local namespace of a feature group type extension includes the defining identifiers in the	_		0 0
Feature Groups and Feature Group T	1'	(N4) The defining feature identifiers of feature group declarations must be unique in the local name			7
Feature Groups and Feature Group T	-	(N5) The defining feature group identifier of feature refinement declarations in component types r			0
Feature Groups and Feature Group T	1	(N6) The package name of the unique feature group type reference must refer to a package name			0
Feature Groups and Feature Group T	1'	(N7) The prototype reference in a feature group declaration must refer to a prototype of the compo			-
· · · · · · · · · · · · · · · · · · ·	1		0		0 0
Feature Groups and Feature Group T	1	(L1) A feature group type may contain zero or more elements, i.e., feature or feature groups. If it c	_		
Feature Groups and Feature Group T	1	(L2) A feature group type can be declared to be the inverse of another feature group type, as indice			
Feature Groups and Feature Group T	1	(L3) Only feature group types without inverse of or feature group types with features and inverse of	_		0
Feature Groups and Feature Group T	1	(L4) A feature group type that is an extension of another feature group type without an inverse of	0		0
Feature Groups and Feature Group T	1	(L5) The feature group type that is an extension of another feature group type with features and in			0
Feature Groups and Feature Group T	1	(L6) A feature group declaration with an inverse of statement must only reference feature group ty	0		0
Feature Groups and Feature Group T	1'	(L7) A feature group refinement may be refined to only add property associations. In this case incl			·
Feature Groups and Feature Group T	1'	(L8) The number of feature or feature groups contained in the feature group and its complement in			0
Feature Groups and Feature Group T	1'	(L9) Each of the declared features or feature groups in a feature group must be a pair-wise compl	_		0
Feature Groups and Feature Group T	1	(L10) If both feature group types have zero features, then they are considered to complement each			0
Feature Groups and Feature Group T	p82l11	(L11) Ports are pair-wise complementary if they satisfy the port connection rules specified in Section	_		0
Feature Groups and Feature Group T	p82l12	(L12) Access features are pair-wise complementary if they satisfy the access connection rules in S			·
Feature Groups and Feature Group T	p82l13	(L13) If an in or out direction is specified as part of a feature group declaration, then all features in	0	(0
Ports	p83n1	(N1) A defining port identifier must adhere to the naming rules specified for all features (see Section	_		0
Ports	p83n2	(N2) The defining identifier of a port refinement declaration must also appear in a feature declaration	0	(0
Ports	p83n3	(N3) The unique component type identifier of the data classifier reference must be the name of a	0	(0
Ports	p83n4	(N4) The prototype identifier of a prototype reference, if specified, must exist in the namespace of	0	(0
Ports	p83l1	(L1) Ports can be declared in subprogram, thread, thread group, process, system, processor, virtu	0	(0
Ports	p83l2	(L2) Data and event data ports may be incompletely defined by not specifying the data componen	0	(0
Ports	p83l3	(L3) Data, event, and event data ports may be refined by adding a property association. The data	0	(0
Ports	p83l4	(L4) The port category of a port refinement must be the same as the category of the port being ref	0	(0
Ports	p83l5	(L5) The port direction of a port refinement must be the same as the direction of the feature being	0	(0
Subprogram and Subprogram Group	p84n1	(N1) The defining identifier of a provides or requires subprogram or subprogram group access dec	0	(0
Subprogram and Subprogram Group	p84n2	(N2) The defining identifier of a provides or requires subprogram or subprogram group refinement		(0
Subprogram and Subprogram Group	+	(N3) The component type identifier or component implementation name of a subprogram or subpr			0
Subprogram and Subprogram Group	+	(N4) The prototype identifier of a subprogram or subprogram group access classifier reference, if	0		0
Subprogram and Subprogram Group		(L1) If a subprogram access refers to a component classifier or a component prototype, then the component prototype, then the component prototype.	_		0
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Subprogram and Subprogram Group		(L2) If a subprogram group access refers to a component classifier or a component prototype, the 0 0	0
Subprogram and Subprogram Group		(L3) An abstract feature can be refined into a subprogram access or a subprogram group access. 0 0	0
Subprogram and Subprogram Group /		(L4) A subprogram or subprogram group access declaration that does not specify a component cl 0 0	0
Subprogram and Subprogram Group		(L5) A subprogram or subprogram group access declaration may be refined by adding a property 0 0	0
Subprogram and Subprogram Group		(L6) A provides subprogram access cannot be refined to a requires subprogram access and a req 0 0	0
Subprogram and Subprogram Group	p84c1	(C1) A provides subprogram access feature indicates that a subprogram is made available to be re 0 0	0
Subprogram Parameters	p85n1	(N1) The defining identifier of a parameter must be unique within the namespace of the subprogra 0 0	0
· · ·	p85n2	(N2) The defining parameter identifier of a parameter refinement declaration must also appear in a 0 0	0
	p85n3	(N3) The data classifier reference must refer to a data component type or a data component imple 0 0	0
Subprogram Parameters	p85n4	(N4) The prototype identifier, if present, must exist in the namespace of the subprogram classifier 0 0	0
Subprogram Parameters	p85l1	(L1) Parameters can be declared for subprogram component types. 0 0	0
Subprogram Parameters	p85l2	(L2) A parameter declaration that does not specify a data classifier reference is incomplete. Such 0 0	0
Subprogram Parameters	p85l3	(L3) A parameter declaration may be refined by adding a property association. Inclusion of the dat 0 0	0
Subprogram Parameters	p85l4	(L4) The parameter direction of a parameter refinement must be the same as the direction of the f 0 0	0
Data Component Access	p86n1	(N1) The defining identifier of a provides or requires data access declaration must be unique withi 0 0	0
Data Component Access	p86n2	(N2) The defining identifier of a provides or requires data access refinement must exist as a defini 0 0	0
Data Component Access	p86n3	(N3) The component type identifier or component implementation name of a data access classifie 0 0	0
Data Component Access	p86n4	(N4) The prototype identifier, if present, must exist in the namespace of the classifier that contains 0 0	0
Data Component Access	p86l1	(L1) If a data access refers to a component classifier or a component prototype, then the category 0 0	0
Data Component Access	p86l2	(L2) A data access declaration may be refined by refining the data classifier, by adding a property 0 0	0
Data Component Access	p86l3	(L3) A provides data access cannot be refined to a requires data access and a requires data acce 0 0	0
Data Component Access	p86l4	(L4) An abstract feature can be refined into a data access. In this case, the abstract feature must 0 0	0
Data Component Access	p86c1	(C1) A data access declaration that does not specify a data classifier reference is incomplete. Suci 0 0	0
Data Component Access	p86c2	(C2) If the source code of a component does access shared data, then the component type declar 0 0	0
Data Component Access	p86c3	(C3) A data access refinement may refine an abstract feature declaration. If the abstract feature de 0 0	0
Bus Component Access	p87n1	(N1) The defining identifier of a provides or requires bus access declaration must be unique within 0 0	0
Bus Component Access	p87n2	(N2) The defining identifier of a provides or requires bus refinement must exist as a defining identi 0 0	0
Bus Component Access	p87n3	(N3) The component type identifier or component implementation name of a bus access classifier 0 0	0
Bus Component Access	p87n4	(N4) The prototype identifier, if present, must exist in the namespace of the classifier that contains 0 0	0
Bus Component Access	p87l1	(L1) If a bus access refers to a component classifier or a component prototype, then the category 0 0	0
Bus Component Access	p87l2	(L2) A bus access declaration may be refined by refining the bus classifier, by adding a property a 0 0	0
Bus Component Access	p87l3	(L3) A provides bus access cannot be refined to a requires bus access and a requires bus access 0 0	C
Bus Component Access	p87l4	(L4) An abstract feature can be refined into a bus access. In this case, the abstract feature must n 0 0	
Bus Component Access	p87c1	(C1) A bus access declaration that does not specify a bus classifier reference is incomplete. Such 0 0	
Bus Component Access	p87c2	(C2) If a bus access feature is a refinement of an abstract feature, then the direction of the abstract 0 0	0
Bus Component Access	p87n1	(N1) The defining identifier of a defined connection declaration must be unique in the local names 0 0	0
Bus Component Access	p87n2	(N2) The connection identifier in a connection refinement declaration must refer to a named conne 0 0	0
Bus Component Access	p87l1	(L1) A connection refinement must contain at least one of the following: a connection source and 0 0	0
	p87l2	(L2) If a semantic connection may be active in a particular mode, then the ultimate source and ulti 0 0	0
Bus Component Access	p87l3	(L3) If a semantic connection may be active in a particular mode transition, then the ultimate source 0 0	0
	p91n1	(N1) A source or destination reference in a feature connection or feature connection refinement de 0 0	0
	p91n2	(N2) The subcomponent reference may refer to a subcomponent or a subcomponent array.	0
	p91l1	(L1) If the feature connection declaration represents a connection between features of sibling com 0 0	
Feature Connections	p91l2	(L2) If the feature connection declaration represents a connection between features up the contair 0 0	
Feature Connections	p91l3	(L3) If the feature connection declaration represents a connection between features down the con 0 0	
	p91l4	(L4) If the feature connection declaration specifies a directional connection, then the direction of the 0 0	0
Feature Connections	p91l5	(L5) The individual connections of a semantic connection must be bidirectional or have the same 0 0	0

Port Connections	p92n1	(N1) The connection identifier in a port connection refinement declaration must refer to a named p 0 0	0
Port Connections	p92n2	(N2) A source or destination reference in a port connection or port connection refinement declarat 0 0	0
Port Connections	p92n3	(N3) The subcomponent reference may also consist of a reference to a subcomponent array.	0
Port Connections	p92n4	(N4) The event_or_event_data identifier of event source specifications (self.event_or_event_data 0 0	0
Port Connections	p92l1	(L1) In the case of a directional port connection the connection end representing the source of the 0 0	0
Port Connections	p92l2	(L2) In the case of a bidirectional port connection either connection end can be the source. If the t 0 0	0
Port Connections	p92l3	(L3) If the source connection end is a data access feature it must have read access rights; if the d 0 0	0
Port Connections	p92l4	(L4) The feature identifier of a subcomponent reference may refer to a feature array, if the subcon 0 0	0
Port Connections	p92l5	(L5) The following are acceptable sources and destinations of port connections. The left column s 0 0	0
Port Connections	p92l6	(L6) If the port connection declaration represents a connection between ports of sibling componer 0 0	0
Port Connections	p92l7	(L7) If the port connection declaration represents a connection between ports up the containment 0 0	0
Port Connections	p92l8	(L8) If the port connection declaration represents a connection between ports down the containme 0 0	0
Port Connections	p92l9	(L9) The individual connections of a semantic port connection must be bidirectional or have the sa 0 0	0
Port Connections	p92l10	(L10) Selfidentifier> must only be referenced as the source of a connection.	0
Port Connections	p92l11	(L11) A data port cannot be the destination of more than one semantic port connection unless eac 0 0	0
Port Connections	p92l12	(L12) A semantic connection cannot contain connection declarations with both immediate and dela 0 0	0
Port Connections	p92l13	(L13) For connections between data ports, event data ports and data access, the data classifier of 0 0	0
Port Connections	p92l14	(L14) The following rules are supported: • • • • • Сlassifier_Match: The source data tyļ 0 0	0
Port Connections	p92l15	(L15) If more than one port connection declaration in a semantic port connection has a property as 0 0	0
Port Connections	p92l16	(L16) A processor port specification must only be used in event connections within threads and su 0 0	0
Port Connections	p92c1	(C1) There cannot be cycles of immediate connections between threads, devices, and processors 0 0	0
Port Connections	p92c2	(C2) The processor port identifier of a processor port specification (processor.processor_port_iden 0 0	0
Port Connections	p92c3	(C3) The Supports_Classifier_Subset_Matches property may be associated with a bus or virtual by 0 0	0
Port Connections	p92c4	(C4) The Supports_Type_Conversions property may be associated with a bus or virtual bus. This 0 0	0
Parameter Connections	p93n1	(N1) The connection identifier in a parameter connection refinement declaration must refer to a na 0 0	0
Parameter Connections	p93n2	(N2) A source (destination) reference in a parameter connection declaration must reference a par 0 0	0
Parameter Connections	p93l1	(L1) The source of a parameter connection must be an incoming data or event data port of the cor 0 0	0
Parameter Connections	p93l2	(L2) The following source/destination pairs are acceptable for parameter connection declarations: 0 0	0
Parameter Connections	p93l3	(L3) A parameter cannot be the destination feature reference of more than one parameter connec 0 0	0
Parameter Connections	p93l4	(L4) The data classifier of the source and destination must match. The matching rules as specified 0 0	0
Access Connections	p94n1	(N1) The connection identifier in an access connection refinement declaration must refer to a nam 0 0	0
Access Connections	p94n2	(N2) An access reference in an access connection declaration must reference an access feature 0 0	0
Access Connections	p94l1	(L1) The category of the source and the destination of a access connection declaration must be th 0 0	0
Access Connections	p94l2	(L2) In the case of a bidirectional semantic access connection either connection end can be the sq 0 0	0
Access Connections	p94l3	(L3) In the case of a directional data or bus access connection the connection end representing th 0 0	0
Access Connections	p94I4	(L4) In a partial AADL model the ultimate source or destination may be a provides access feature 0 0	0
Access Connections	p94I5	(L5) If the access connection declaration represents an access connection between access featur 0 0	0
Access Connections	p94l6	(L6) If the access connection declaration represents a feature mapping up the containment hierare 0 0	0
Access Connections	p94I7	(L7) If the access connection declaration represents a feature mapping down the containment hie 0 0	0
Access Connections	p94l8	(L8) A requires access cannot be the source or destination feature reference of more than one ac 0 0	0
Access Connections	p94I9	(L9) For access connections the classifier of the provider access must match to the classifier of th 0 0	0
Access Connections	p94l10	(L10) If more than one access feature in a semantic access connection has an Access_Right prop 0 0	0
Access Connections	p94l11	(L11) The category of the access connection source and destination must be identical. If the comp 0 0	0
			٦
Feature Group Connections	p95n1	(N1) The connection identifier in a feature group connection refinement declaration must refer to a 0 0	0
Feature Group Connections	p95n2	(N2) A source or destination reference in a feature group connection declaration must reference a 0 0	0
Feature Group Connections	p95l1	(L1) If the feature group connection declaration represents a component connection between sibli 0 0	0
Feature Group Connections	p95l2	(L2) The Classifier_Matching_Rule property specifies the rule to be applied to match the feature g 0 0	0
Feature Group Connections	p95l3	(L3) The following rules are supported for feature group connection declarations that represent a 0 0	0
Feature Group Connections	p95l4	(L4) The following rules are supported for feature group connection declarations that represent a 0 0	0
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Feature Group Connections	p95l5	(L5) If the feature group connection declaration represents a connection between feature group of 0 0	0
Feature Group Connections	p95l6	(L6) If the feature group connection declaration represents a connection between feature groups \ 0 0 0	0
Feature Group Connections	p95l7	(L7) If the feature group connection declaration represents a connection between feature groups 0 0 0	0
Feature Group Connections	p95l8	(L8) A feature group connection must be bidirectional or be consistent with the direction of the sou 0 0	0

SECTION NAME	ID	RULE TEXT	POS	NEG	COMMENTARY
AADL Specifications	p41n1	(N1) An AADL specification has one global namespace. The package and property set identifiers reside in this space an	0	0	Checked by DFS during processing package nodes
AADL Specifications	p41n2	(N2) These package and property set identifiers qualify the names of individual elements contained in them when they a	0	0	0
AADL Specifications	p41n3	(N3) Package declarations represent labeled namespaces for component type, component implementation, feature grounds.	0	0	0
AADL Specifications	p41n4	(N4) Property set declarations represent labeled namespaces for property type and property definition declarations.	0	0	0
AADL Specifications	p41n5	(N5) Packages and property sets may be separately stored. Those packages and property sets are considered to be pa	0	0	Provided by parser
AADL Specifications	p41n6	(N6) Defining identifiers in AADL must not be one of the reserved words of the language (see Section 15.7).	0	0	Provided by parser
AADL Specifications	p41n7	(N7) The AADL identifiers and reserved words can be in upper or lower case (or a mixture of the two) (see Section 15).	0	0	Provided by realization of AADLIdentifer class
AADL Specifications	p41n8	(N8) The AADL does not require that an identifier be declared before it is referenced.	0	0	0
Packages	p42n1	(N1) A defining package name consists of a sequence of one or more package identifiers separated by a double colon (0	0	Checked by counting private and public package declarations in DFS during processing package nodes
Packages	p42n2	(N2) The public and private section of a package may be declared in separate package declarations; these two declarat	0	0	Provided by parser
Packages	p42n3	(N3) Associated with every package is a package namespace that contains the names for all the elements defined within	0	0	0
Packages	p42n4	(N4) The package namespace is divided into a public section and a private section. Items declared in the public section	0	0	0
Packages	p42n5	(N5) The reference to an item declared in another package must be an item name qualified with a package name separa	0	0	Can be checked after all possible references are known, impossible in current realization
Packages	p42n6	(N6) The reference to a property other than predeclared properties must be an property name qualified with a property s	0	0	Can be checked after all possible references are known, impossible in current realization
Packages	p42n7	(N7) The package name in a import_declaration must exist in the global name space.	0	0	Checked by DFS during processing package nodes - imports
Packages	p42n8	(N8) The property set identifier in a import_declaration must exist in the global name space.	0	0	Checked by DFS during processing package nodes - imports
Packages	p42n9	(N9) Items declared in the private section of the package can only be referenced from within the private section of the package.	0	0	Can be checked after all possible references are known, impossible in current realization
Packages	p42n10	(N10) If the qualifying package identifier of a qualified reference is missing, the referenced component classifier, feature	0	0	0
Packages	p42n11	(N11) The package name referenced in an alias_declaration must exist in the global namespace and must be listed in the	0	0	Checked by DFS during processing package nodes - package aliases
Packages	p42n12	(N12) The classifier referenced by the alias_declaration must exist in the name space of the public section of the packag	0	0	Checked by DFS during processing package nodes - classifier aliases
Packages	p42n13	(N13) The classifier referenced by the alias declaration must refer to a component type or a feature group type.	0	0	Provided by parser
Packages	p42n14	(N14) The defining identifier of an alias_declaration must be unique in the namespace of the package containing the alia	0	0	Checked by DFS during processing package nodes - package and classifier aliases
Packages	p42n15	(N15) The alias_declaration makes the publicly visible identifier of classifiers declared in another package accessible in	: 0	0	0
Packages	p42n16	(N16) If the alias_declaration renames all publicly visible identifiers of component types and feature group types by nam	0	0	0
Packages	p42n17	(N17) The identifiers introduced by the alias_declaration are only accessible within the package. When declared in the p	0	0	Not compatible with current realization of N14 check (alias ids == component ids), not all possible references are known
Packages	p42n18	(N18) The alias declared for a component type can be used instead of a qualified component type in a reference to a co	0	0	0
Packages	p42l1	(L1) The defining package name following the reserved word end must be identical to the defining package name follow	0	0	Provided by parser
Packages	p42l2	(L2) For each package there may be at most one public section declaration and one private section declaration. These t	0	0	Checked with p42n1
Packages	p42l3	(L3) A component implementation may be declared in both the public and private part of a package. In that case the dec	0	0	Should be checked in component implementations
Packages	p42l4	(L4) The component category in an alias declaration must match the category of the referenced component type.	0	0	0
Component Types	p43n1	(N1) The defining identifier for a component type must be unique in the namespace of the package within which it is dec	0	0	Checked when creating local namespaces of the packages
Component Types	p43n2	(N2) Each component type has a local namespace for defining identifiers of prototypes, features, modes, mode transitio	0	0	O O
Component Types	p43n3	(N3) The component type identifier of the ancestor in a component type extension, i.e., that appears after the reserved v	0	0	Checked by DFS for each component type node
Component Types	p43n4	(N4) When a component type extends another component type, a component type namespace includes all the identifier	0	0	0
Component Types	p43n5	(N5) A component type that extends another component type does not include the identifiers of the implementations of i	: 0	0	0
Component Types	p43n6	(N6) The defining identifier of a feature, flow specification, mode, mode transition, or prototype must be unique in the na	0	0	0
Component Types	p43n7	(N7) The refinement identifier of a feature, flow specification, or prototype refinement refers to the closest refinement or	0	0	0
Component Types	p43n8	(N8) The prototypes referenced by prototype binding declarations must exist in the local namespace of the component to	0	0	0
Component Types	p43n9	(N9) Mode transitions declared in the component type may not refer to event or event data ports of subcomponents.	0	0	0
Component Types	p43l1	(L1) The defining identifier following the reserved word end must be identical to the defining identifier that appears after	0	0	Provided by parser
Component Types	p43l2	(L2) The prototypes, features, flows, modes, and properties subclauses are optional. If a subclause is present but empty	, 0	0	Provided by parser(kinda, error is - "No viable alternative")
Component Types	p43l3	(L3) The category of the component type being extended must match the category of the extending component type, i.e	, 0	0	Checked by DFS for each component type node
Component Types	p43l4	(L4) The classifier being extended in a component type extension may include prototype bindings. There must be at mo-	0	0	0
Component Types	p43l5	(L5) A component type must not contain both a requires_modes_subclause and a modes_subclause.	0	0	Provided by parser(kinda, error is - extraneous input 'requires' expecting ('ANNEX', 'END', 'PROPERTIES', IDENTIFIER})
Component Types	p43l6	(L6) If the extended component type and an ancestor component type in the extends hierarchy contain modes subclause	0	0	Checked by DFS for each component type node
Component Implementation	r p44n1	(N1) A component implementation name consists of a component type identifier and a component implementation ident	0	0	0
Component Implementation	r p44n2	(N2) The defining identifier of the component implementation must be unique within the local namespace of the component	0	0	0
Component Implementation	r p44n3	(N3) Every component implementation defines a local namespace for all defining identifiers of prototypes, subcomponer	0	0	0

Component Implementation p44n-	(N4) This local namespace inherits the namespace of the associated component type, i.e., defining identifiers must be ur	0	0
Component Implementation p44ns		0	0
Component Implementation p44nd		0	
Component Implementation p44n		0	
Component Implementation p44nl		0	
		0	
Component Implementation p44ns	(N9) The prototype referenced by the prototype binding declaration must exist in the local namespace of the component	- 0	
Component Implementation p44l1	(L1) The pair of identifiers separated by a dot (въњ.въќ) following the reserved word end must be identical to the pair of	0	
Component Implementation p44l2	(L2) The prototypes, subcomponents, connections, calls, flows, modes, and properties subclauses are optional. If they are	0	0
Component Implementation p44l3	(L3) The category of the component implementation must be identical to the category of the component type for which the	0	0
Component Implementation p44l4	(L4) If the component implementation extends another component implementation, the category of both must match, i.e.,	0	0
Component Implementation p44I5	(L5) The classifier being extended in a component implementation extension may include prototype bindings. There mus	0	0
Component Implementation p44l6	(L6) If the component type of the component implementation contains a requires_modes_subclause then the component	0	0
Component Implementation p44I7	(L7) If modes are declared in the component type, then modes cannot be declared in component implementations.	0	0
Component Implementation p44l8	(L8) If modes or mode transitions are declared in the component type, then mode transitions can be added in the compo	0	0
Component Implementation p44I9	(L9) The category of a subcomponent being refined must match the category of the refining subcomponent declaration, i	0	0
Component Implementation p44I1		0	0
Component Implementation p44l1		0	
	, and the second		
Subcomponents p45n	(N1) The defining identifier of a subcomponent declaration placed in a component implementation must be unique within	0	0
Subcomponents p45n2		-	
Subcomponents p45n3			
	C 12 - 1 - 1 - 21 - 1 - 1 - 1 - 1 - 1 - 1	0	
Subcomponents p45n4		. 0	
Subcomponents p45ns		0	
Subcomponents p45n0		0	
Subcomponents p45l1	(L1) The category of the subcomponent declaration must match the category of its corresponding component classifier re	0	
Subcomponents p45l2	(L2) The component classifier reference of a subcomponent declaration may include prototype bindings for a subset or a	0	
Subcomponents p45l3	(L3) In a subcomponent refinement declaration the component category may be refined from abstract to one of the concr	0	0
Subcomponents p45l4	(L4) The Classifier_Substitution_Rule property specifies the rule to be applied when a refinement supplies a classifier an	0	0
Subcomponents p45l5	(L5) In the case of a signature match, the component type of the subcomponent being refined must have a subset of the	0	0
Subcomponents p45l6	(L6) The component category and optional component classifier or prototype reference can be followed by a set of array	0	0
Subcomponents p45l7	(L7) The array size specification for the dimensions is optional. In this case the array declaration is considered incomplet	0	0
Subcomponents p45l8	(L8) When refining a subcomponent array the number of dimensions of the array cannot be changed, but the array size q	0	0
Subcomponents p45l9	(L9) When the subcomponent is declared as an array with array dimension sizes then a list of component implementation	0	0
Subcomponents p45l1	0 (L10) Selecting index ranges in one or more dimensions of an array is only possible if the size of the array for these dime	0	0
Subcomponents p45l1	(L11) An array element implementation list is valid only if (a) the subcomponent classifier is a component type and (b) all	0	0
Subcomponents p45c		0	0
Abstract Components p46l1	(L1) An abstract component type declaration can contain feature declarations (including abstract feature declarations), flo	0	0
Abstract Components p46l2	(L2) An abstract component implementation can contain subcomponent declarations of any category. Certain combination	0	0
Abstract Components p46l3	(L3) An abstract component implementation can contain a modes subclause, a connections subclause, a flows subclause	-	
Abstract Components p46l4	(L4) An abstract subcomponent can be contained in the implementation of any component category.	0	
Abstract Components p4615	(L5) If an abstract subcomponent can be contained in the implementation of any component category. (L5) If an abstract subcomponent is refined to a concrete category, the concrete category must be acceptable to the com	0	
Abstract Components p4616	(L6) An abstract subcomponent can be declared as an array of subcomponents.	-	
·		-	
Abstract Components p4617	(L7) If an abstract component type is refined to a concrete category, the features, modes, and flow specifications of the a	-0	
Abstract Components p46l8	(L8) If an abstract component implementation is refined to a concrete category, the subcomponents, call sequences, mod	0	
Prototypes p47n		0	
Prototypes p47n:		0	0
Prototypes p47n:	(N3) Unique component classifier references must exist in the public section of the package being identified in the references.	0	0
Prototypes p47n-	(N4) Unique feature group type references must exist in the public section of the package being identified in the reference.	0	0
Prototypes p47l1	(L1) The component category declared in the component prototype binding must match the component category of the p	0	0
Prototypes p47l2	(L2) The component category of the optional component classifier reference in the prototype declaration must match the	0	0

Prototypes	p47l3	(L3) If the component prototype only specifies a component category, then any component type and component impleme	0	0
Prototypes	p47l4	(L4) If the component prototype declaration includes a component classifier reference, then the classifier supplied in the	0	0
Prototypes	p47l5	(L5) The category of the component implementation that contains the prototype declaration places restrictions on the set	0	0
Prototypes	p47l6	(L6) If the direction is declared for feature prototypes, then the prototype actual satisfies the direction according to the sale	0	0
Prototypes	p47I7	(L7) In the case of feature group prototypes, the supplied feature group types must match the declared feature group type	0	0
Prototypes	p47l8	(L8) A classifier supplied in a feature prototype binding must match the classifier of the prototype declaration, if present,	0	-
Prototypes	p47l9	(L9) Component prototypes declared with square brackets specify that they expect a list of component classifiers. These	0	0
Prototypes	p47l10	(L10) The component category of the classifier reference or prototype reference in a prototype binding declaration must r	0	0
Prototypes	p47l11	(L11) If a direction is specified for an abstract feature in a prototype declaration, then the direction of the prototype actual	0	0
Prototypes	p47l12	(L12) Component prototype bindings must only bind component prototypes, feature group prototype bindings must only t	0	0
Prototypes	p47l13	(L13) Component prototype refinements must only refine component prototypes, feature group prototype refinements mu	0	0
Annex Subclauses and A	nr p48n1	(N1) The annex identifier must be the name of an approved annex or a project-specific identifier different from the approv	0	0
Annex Subclauses and A		(N2) The mode identifiers in the in modes statement must refer to modes in the component type or component implement	0	0
Annex Subclauses and A	-	(L1) Annex subclauses can only be declared in component types, component implementations, and feature group types.	0	0
Annex Subclauses and A	_	(L2) A component type, component implementation, or feature group type declaration may contain at most one annex su	0	
Annex Subclauses and A	-	(L3) Annex libraries must be declared in packages.	0	_
			_	_
Annex Subclauses and A	пгр4814	(L4) A package declaration may contain at most one annex library declaration for each annex.	0	0
Data	p51l1	(L1) A data type declaration can contain provides subprogram access declarations as well as property associations.	0	_
Data	p51l2	(L2) A data type declaration must not contain a flow specification or modes subclause.	0	0
Data	p51l3	(L3) A data implementation can contain abstract, data and subprogram subcomponents, access connections, and data p	0	0
ata	p51l4	(L4) A data implementation must not contain a flow implementation, an end-to-end flow specification, or a modes subclau	0	0
ubprograms and Subpro	og p52n1	(N1) The defining identifier of a subprogram call sequence declaration must be unique within the local namespace of the	0	0
ubprograms and Subpro	•	(N2) The defining identifier of a subprogram call declaration must be unique within the local namespace of the componer	0	0
ubprograms and Subpro		(N3) If the called subprogram name is a subprogram classifier reference, its component type identifier or component impl	0	0
ubprograms and Subpro	_	(N4) The subprogram classifier reference of a subprogram call may be a subprogram type reference.	0	0
	*		-	0
Subprograms and Subpro	-	(N5) If the called subprogram name is a subprogram subcomponent reference, the subprogram subcomponent must exist	0	
Subprograms and Subpro	-	(N6) If the called subprogram name is a requires subprogram access reference, the requires subprogram access must el	U	U
Subprograms and Subpro	<u>.</u>	(L1) A subprogram type declaration can contain parameter, out event port, out event data port, and feature group declaration	0	0
Subprograms and Subpro	p52l2	(L2) A subprogram implementation can contain abstract, subprogram, and data subcomponents, a subprogram calls sub	0	0
Subprograms and Subpro	p52l3	(L3) Only one subprogram call sequence can apply to a given mode.	0	0
Subprograms and Subpro	p52c1	(C1) The reference to a provides subprogram access of a processor in a subprogram call (processor . provides_subprogram	0	0
Subprograms and Subpro	p52c2	(C2) A subprogram call may reference a subprogram classifier. A project may enforce a consistency rule that this referen	0	0
Subprogram Groups and	Si p53n1	(N1) The defining identifier of a subprogram group type must be unique within the package namespace of the package w	0	0
Subprogram Groups and		(N2) Each subprogram group provides a local namespace. The defining subprogram identifiers of subprogram declaration	0	_
Subprogram Groups and		(N3) The local namespace of a subprogram group type extension includes the defining identifiers in the local namespace	0	_
	+			-
Subprogram Groups and		(N4) The defining subprogram identifiers of subprogram access feature declarations in feature group refinements must n	0	0
Subprogram Groups and		(N5) The package name of the unique subprogram group type reference must refer to a package name in the global nam	0	0
Subprogram Groups and	_	(L1) A subprogram group type can contain provides and requires subprogram access, and provides and requires subprogram	0	0
Subprogram Groups and	S p53l2	(L2) A subprogram group implementation can contain abstract, data, subprogram group, and subprogram subcomponent	0	0
Subprogram Groups and	S p53l3	(L3) A subprogram group type or implementation may contain zero or more subcomponent declarations. If it contains zer	0	0
Threads	p54l1	(L1) A thread type declaration can contain port, feature group, requires data access declarations, as well as requires and	0	0
Threads	p54l2	(L2) A thread component implementation can contain abstract, data, subprogram, and subprogram group subcomponent	0	0
Threads	p54l3	(L3) The Complete out event port, and Error out event data port are predeclared, i.e., are implicitly identifiers in the name	0	
	p54c3	(C3) Either the Compute Entrypoint, and Entrypoint Source Text Compute Entrypoint Call Sequence property in	0	
Threads	poaco		0	0
Thereside			0	
Threads	p54c4	(C4) The Period property must have a value if the Dispatch_Protocol property value is periodic, sporadic, timed, or hybrid.		<u> </u>
Threads	p54c4	(C4) The Period property must have a value if the Dispatch_Protocol property value is periodic, sporadic, timed, or hybrid. (L1) A thread group component type can contain provides and requires data access, as well as port, feature group, provides.		

Thread Groups	p55l2	(L2) A thread group component implementation can contain abstract, data, subprogram, subprogram group, thread, and	0	0
Thread Groups	p55l3	(L3) A thread group implementation can contain a connections subclause, a flows subclause, a modes subclause, and pr	0	0
Thread Groups	p55l4	(L4) A thread group must not contain a subprogram calls subclause.	0	0
rocesses	p56I1	(L1) A process component type can contain port, feature group, provides and requires data access, provides and require	0	0
Processes	p56l2	(L2) A process component implementation can contain abstract, data, subprogram, subprogram group, thread, and threa	0	0
Processes	p56l3	(L3) A process implementation can contain a connections subclause, a flows subclause, a modes subclause, and a prop	0	0
Processes	p56l4	(L4) A thread group must not contain a subprogram calls subclause.	0	0
Processes	p56c1	(C1) The complete source text associated with a process component must form a complete and legal program as defined	0	0
Processors	p61I1	(L1) A processor component type can contain port, feature group, provides subprogram access, provides subprogram gr	0	0
Processors	p61I2	(L2) A processor component implementation can contain declarations of memory, bus, virtual bus, virtual processor, and	0	0
Processors	p61I3	(L3) A processor implementation can contain a modes subclause, flows subclause, and a properties subclause.	0	0
Processors	p61I4	(L4) A processor implementation can contain bus access, subprogram access, subprogram group access, port, feature, a	0	0
Processors	p61I5	(L5) A processor implementation must not contain a subprogram calls subclause.	0	0
100633013	porio	(LS) A processor implementation must not contain a subprogram cans subclause.	٠	
Educal Drassassass	~COI4	(I 1) A vide of exposure components are considered forther provides or have component and or have com-	_	
Virtual Processors	p62l1	(L1) A virtual processor component type can contain port, feature group, provides subprogram access, and subprogram	0	-
Virtual Processors	p62l2	(L2) A virtual processor component implementation can contain declarations of virtual bus, virtual processor, and abstract	0	0
Virtual Processors	p62l3	(L3) A virtual processor implementation can contain a modes subclause, flows subclause, and a properties subclause.	0	0
Virtual Processors	p62I4	(L4) A virtual processor implementation must not contain a subprogram calls subclause.	0	0
Virtual Processors	p62I5	(L5) A virtual processor implementation can contain subprogram access, subprogram group access, port, feature, and fe	0	_
Virtual Processors	p62c1	(C1) In a fully bound system every virtual processor must be directly or indirectly bound to, or directly or indirectly contain	0	0
/irtual Processors	p62c2	(C2) In a fully deployed system a requires virtual bus binding of a virtual processor specified by the Required_Virtual_Bus	0	0
lemory	p63I1	(L1) A memory type can contain bus access declarations, feature groups, a modes subclause, and property associations	0	0
lemory	p63l2	(L2) A memory implementation can contain abstract, memory, and bus subcomponent declarations.	0	0
lemory	p63I3	(L3) A memory implementation can contain a modes subclause and property associations.	0	0
emory	p63I4	(L4) A memory implementation can contain bus access connection declarations. Bus access connections can connect a	0	0
lemory	p63I5	(L5) A memory implementation must not contain flows subclause, or subprogram calls subclause.	0	0
	poolo	(co)		
uses	p64l1	(L1) A bus type can have requires bus access declarations, a modes subclause, and property associations.	0	0
	p6412		0	-
Suses	_	(L2) A bus type must not contain any flow specifications.	0	0
Buses	p64l3	(L3) A bus implementation can contain virtual bus and abstract subcomponent declarations.	_	-
uses	p64I4	(L4) A bus implementation can contain a modes subclause and property associations.	0	0
Buses	p64I5	(L5) A bus implementation must not contain flows subclause, or subprogram calls subclause.	0	0
/irtual Buses	p65l1	(L1) A virtual bus type can have property associations.	0	-
/irtual Buses	p65l2	(L2) A virtual bus type must not contain flow specifications.	0	0
Virtual Buses	p65I3	(L3) A virtual bus implementation can contain virtual bus subcomponent declarations.	0	0
/irtual Buses	p65l4	(L4) A virtual bus implementation can contain a modes subclause and property associations.	0	0
/irtual Buses	p65l5	(L5) A virtual bus implementation must not contain a connections subclause, flows subclause, or subprogram calls subclause	0	0
Virtual Buses	p65c1	(C1) In a fully deployed system virtual buses must be directly or indirectly bound to processors or buses that support thes	0	0
				\blacksquare
Devices	p66l1	(L1) A device type can contain port, feature group, provides subprogram access, provides subprogram group access, but	0	0
Devices	p66l2	(L2) A device component implementation must not contain a subprogram calls subclause.	0	0
Devices	p66l3	(L3) A device implementation can contain abstract, data, virtual bus, and bus subcomponents, bus access connections, a	0	-
	poolo	A device implementation can contain abstract, data, virtual bus, and bus subcomponents, bus access connections, a	- 0	
Printe man	-7414	(1.4) A system consequent type consequent system of the consequence of	_	_
Systems	p71l1	(L1) A system component type can contain subprogram, subprogram group, data and bus access declarations, port, feat	0	0
Systems	p71l2	(L2) A system component implementation can contain abstract, data, subprogram, subprogram group, process, and syst	0	0
Systems	p71I3	(L3) A system implementation can contain a modes subclause, a connections subclause, a flows subclause, and propert	0	0
Systems	p71l4	(L4) A thread group must not contain a subprogram calls subclause.	0	0
Systems	p71n1	(N1) The defining identifier of a feature must be unique within the namespace of the associated component type.	0	0
Systems	p71n2	(N2) Thread features may not be declared using the predeclared ports names Complete or Error.	0	0
	F	, , , , , , , , , , , , , , , , , , ,	<u> </u>	

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· '	p71n3	(N3) Each refining feature identifier that appears in a feature refinement declaration must also appear in a feature declaration	_	0
- ,	p71n4	(N4) A feature is referenced in one of two ways. Within the component implementations for a component type, a feature 0		0
, ,	p71n5	(N5) The path of a contained property association for a feature must refer to an element of a feature group.	_	0
	p71l1	(L1) Each feature can be refined at most once in the same type extension.	_	0
Systems	p71l2	(L2) A feature refinement declaration of a feature and the original feature must both be declared as port, parameter, acce 0	_	0
Systems	p71I3	(L3) Feature arrays must only be declared for threads, devices, and processors. 0	(0
Systems	p71I4	(L4) If the feature refinement specifies an array dimension, then the feature being refined must have an array dimension.	(0
Systems	p71I5	(L5) If the refinement specifies an array dimension size, then the feature being refined must not have an array dimension 0	(0
Systems	p71l6	(L6) A contained property association must only be used when the feature is a feature group. 0	(0
Systems	p71I7	(L7) In the case of a feature with a classifier reference, the classifier of the refined feature declaration in a component typ 0		0
Abstract Features	p81l1	(L1) The feature direction in a refined feature declaration must be identical to the feature direction in the feature declaration	(0
Abstract Features	p81l2	(L2) If the direction of an abstract feature is specified, then the direction must be satisfied by the refinement (see also the	(0
Abstract Features	p81l3	(L3) An abstract feature with a feature prototype identifier and the prototype being referenced must both specify the same 0	-	0
Abstract Features	p81I4	(L4) An abstract feature refinement declaration of a feature with a feature prototype reference must only add property as 0	(0
Feature Groups and Featur	p82n1	(N1) The defining identifier of a feature group type must be unique within the package namespace of the package where 0		0
Feature Groups and Featur	•	(N2) Each feature group type provides a local namespace. The defining identifiers of prototype, feature, and feature group 0		0
Feature Groups and Featur		(N3) The local namespace of a feature group type extension includes the defining identifiers in the local namespace of the	_	0
Feature Groups and Featur	•	(N4) The defining feature identifiers of feature group declarations must be unique in the local name space of the compon 0	_	0
Feature Groups and Feature	•	(N5) The defining feature group identifier of feature refinement declarations in component types must exist in the local na 0	_	0
Feature Groups and Feature		(N6) The package name of the unique feature group type reference must refer to a package name in the global namespa 0	_	0
Feature Groups and Feature		(N7) The prototype reference in a feature group declaration must refer to a prototype of the component type or feature gr		0
Feature Groups and Featur		(L1) A feature group type may contain zero or more elements, i.e., feature or feature groups. If it contains zero elements, 0	_	0
Feature Groups and Feature		(L2) A feature group type can be declared to be the inverse of another feature group type, as indicated by the reserved w		0
Feature Groups and Feature	•	(L3) Only feature group types without inverse of or feature group types with features and inverse of can be extended.	_	0
Feature Groups and Featuri		(L4) A feature group type that is an extension of another feature group type without an inverse of cannot contain an inverse contain		0
Feature Groups and Feature		(L5) The feature group type that is an extension of another feature group type with features and inverse of that adds feat 0	_	0
' '				0
Feature Groups and Featur	•	(L6) A feature group declaration with an inverse of statement must only reference feature group types without an inverse 0	_	0
Feature Groups and Featur		(L7) A feature group refinement may be refined to only add property associations. In this case inclusion of the feature gro	_	
Feature Groups and Featur		(L8) The number of feature or feature groups contained in the feature group and its complement must be identical;	_	0
Feature Groups and Featur	<u> </u>	(L9) Each of the declared features or feature groups in a feature group must be a pair-wise complement with that in the f	_	0
Feature Groups and Featur	<u> </u>	(L10) If both feature group types have zero features, then they are considered to complement each other; 0	_	0
Feature Groups and Featur	•	(L11) Ports are pair-wise complementary if they satisfy the port connection rules specified in Section 9.2.1. This includes 0		0
Feature Groups and Featur		(L12) Access features are pair-wise complementary if they satisfy the access connection rules in Section 9.4.	_	0
Feature Groups and Featur	p82l13	(L13) If an in or out direction is specified as part of a feature group declaration, then all features inside the feature group 0	-	0
	p83n1	(N1) A defining port identifier must adhere to the naming rules specified for all features (see Section 8).	_	0
	p83n2	(N2) The defining identifier of a port refinement declaration must also appear in a feature declaration of a component type 0		0
	p83n3	(N3) The unique component type identifier of the data classifier reference must be the name of a data component type. T	_	0
	p83n4	(N4) The prototype identifier of a prototype reference, if specified, must exist in the namespace of the component type or 0	_	0
	p83l1	(L1) Ports can be declared in subprogram, thread, thread group, process, system, processor, virtual processor, and devided the control of the	_	0
Ports	p83l2	(L2) Data and event data ports may be incompletely defined by not specifying the data component classifier reference or		0
	p83l3	(L3) Data, event, and event data ports may be refined by adding a property association. The data component classifier d	_	0
Ports	p83l4	(L4) The port category of a port refinement must be the same as the category of the port being refined, or the port being 0	_	0
Ports	p83l5	(L5) The port direction of a port refinement must be the same as the direction of the feature being refined. If the feature b	(0
Subprogram and Subprogra	p84n1	(N1) The defining identifier of a provides or requires subprogram or subprogram group access declaration must be uniqu 0		0
Subprogram and Subprogra	p84n2	(N2) The defining identifier of a provides or requires subprogram or subprogram group refinement must exist as a definin 0		0
Subprogram and Subprogra	p84n3	(N3) The component type identifier or component implementation name of a subprogram or subprogram group access cl 0		0
Subprogram and Subprogra	p84n4	(N4) The prototype identifier of a subprogram or subprogram group access classifier reference, if present, must exist in the	-	0
Subprogram and Subprogra	p84l1	(L1) If a subprogram access refers to a component classifier or a component prototype, then the category of the classifie 0	(0
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Subgroups and Findings (Selfation Company) (Se		·	(L2) If a subprogram group access refers to a component classifier or a component prototype, then the category of the cl	0	· ·	0
Subgragement of Subgrage (See 1) Subgragement	Subprogram and Subprogr	p84I3	(L3) An abstract feature can be refined into a subprogram access or a subprogram group access. In this case, the abstra	0		0
Subprogram and Subprograps(self-coll) of proceeds absorbed management of the self-collection of the subprogram and some subprogram and some subprogram and some subprogram and subprogram	Subprogram and Subprogr	p84l4	(L4) A subprogram or subprogram group access declaration that does not specify a component classifier reference is inc	0		0
Supporgram Parameters	Subprogram and Subprogr	p84I5	(L5) A subprogram or subprogram group access declaration may be refined by adding a property association. Inclusion d	0		0
Suprogram Parameters Suprogram	Subprogram and Subprogr	p84l6	(L6) A provides subprogram access cannot be refined to a requires subprogram access and a requires subprogram acce	0		0
Suprogram Parameters \$50,000 NO.1 the defending parameter feature for a parameter reference to coloration to must also appose in a Statuto declaration of 0 0 \$50,000 NO.1 the defending parameter feature for all parameters and select to all composed in parameters and select to all composed in parameters and select to all composed in parameters and select to all colorations in parameters and selections of the description of the selection of the se	Subprogram and Subprogr	p84c1	(C1) A provides subprogram access feature indicates that a subprogram is made available to be referenced. A project made available to be referenced.	0		0
Suprogram Parameters \$550, Description Parameters \$550, Descript						T
Subprogram Parameters SSED_C (VC). The defining parameter feedling of a susmetter reforment declaration must also appear in a feature exclusion of 0 0 0 0 0 0 0 0 0 0	Subprogram Parameters	p85n1	(N1) The defining identifier of a parameter must be unique within the namespace of the subprogram type containing the	0		0
Subgrougner Parsonnes Signification	_ · ·	+		0		0
Subprogram Parameters Selection Selec		p85n3		0		0
Septogran Parameters Septogran		<u> </u>		0	0 0	0
Subprogram Parameters		<u> </u>		0		n
Substragram Prameration Septing and Septin		<u> </u>		0		-
Subjoognam Prisonnelles 9554 Lo. The parameter direction of a parameter referement must be the same as the direction of the feature being refined. If 0 0 0 0 0 0 0 0 0		-		0		n
Data Component Access 88611 (1) 17 the defining identifier of a provides or requires data access declaration must be unique within the namespace of the O 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0		+		0		7
Data Component Access Date Component Access	oubprogram r arameters	posit	(14) The parameter direction of a parameter remiement must be the same as the direction of the reactive being remied. If	-	, ,	_
Data Component Access Date Component Access	Data Component Access	n86n1	(N1) The defining identifier of a provides or requires data access declaration must be unique within the personness of the			_
Data Component Access 56943 (1.3) The component type identifier or component implementation name of a data access classifier reference makes exist. 0 0 0 0 0 0 0 0 0	· ·	<u> </u>		0		0
Data Component Access	· · · · · · · · · · · · · · · · · · ·	 		0		9
Date Component Access	· · · · · · · · · · · · · · · · · · ·	<u> </u>		- 0	· · ·	0
Data Component Access		<u> </u>		0		0
Data Component Access 88813 (1.3) A provided data access cannot be refined to a requires data access. In this case, the abstract feature must not have a direction sp 0 Data Component Access 88614 (1.4) An abstract deature can be refined into a data access. In this case, the abstract feature must not have a direction sp 0 Data Component Access 88612 (C2) If the source code of a component does access access declaration that does not specify a data classifier reference is incomplete. Such a reference can be a direction sp 0 Data Component Access 88612 (C2) If the source code of a component does access shared data, then the component value of the provided of	· · · · · · · · · · · · · · · · · · ·	<u> </u>		0	· · ·	괵
Data Component Access p864 (4.1) An abstract feature can be refined into a data access. In this case, the abstract feature must not have a direction sp 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0		+		0		0
Data Component Access p86c1 C1) A data access declaration that does not specify a data classifier reference is incomplete. Such a reference can be a 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	· · · · · · · · · · · · · · · · · · ·	<u> </u>		0		0
Data Component Access 88c2 (C2) If the source code of a component does access shared data, then the component type declaration must specify a re 0 0 0 0 0 0 0 0 0		i -		0		0
Data Component Access 98C3 (C3) A data access refinement may refine an abstract feature declaration. If the abstract feature declaration specifies a 0 0 0 0 0 0 0 0 0	·	+	(C1) A data access declaration that does not specify a data classifier reference is incomplete. Such a reference can be a	0		0
Bus Component Access p8711 (N1) The defining identifier of a provides or requires bus access declaration must be unique within the namespace of the D 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	Data Component Access	p86c2	(C2) If the source code of a component does access shared data, then the component type declaration must specify a re	0		0
Bus Component Access p87n2 (N2) The defining identifier of a provides or requires bus refinement must exist as a defining identifier of a requires or pro 0 D D D D D D D D D D D D D D D D D D	Data Component Access	p86c3	(C3) A data access refinement may refine an abstract feature declaration. If the abstract feature declaration specifies a d	0		0
Bus Component Access p87n2 (N2) The defining identifier of a provides or requires bus refinement must exist as a defining identifier of a requires or pro 0 D D D D D D D D D D D D D D D D D D						
Bus Component Access p87n3 (N3) The component type identifier or component implementation name of a bus access classifier reference must exist in 0 0 0 Bus Component Access p87n4 (N4) The prototype identifier, if present, must exist in the namespace of the classifier or a component prototype identifier, if present, must exist in the namespace of the classifier or a component prototype identifier, if present, must exist in the namespace of the classifier or a component prototype identifier or 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	Bus Component Access	p87n1	(N1) The defining identifier of a provides or requires bus access declaration must be unique within the namespace of the	0		0
Bus Component Access p8711 (L1) If a bus access refers to a component classifier or a component prototype, then the category of the classifier or proto p8712 (L2) A bus access refers to a component classifier or a component prototype, then the category of the classifier or proto p8713 (L3) A provides bus access cannot be refined by refining the bus classifier, by adding a property association, or by doing p8714 (L4) An abstract feature can be refined to a requires bus access and a requires bus access cannot be refined to a Bus Component Access p8714 (L4) An abstract feature can be refined into a bus access in this case, the abstract setule must not have a direction spe Bus Component Access p8714 (L4) An abstract feature can be refined into a bus access in this case, the abstract feature must not have a direction spe Bus Component Access p8716 (C1) A bus access declaration may be refined into a bus access and a requires bus access cannot be refined to a Bus Component Access p8716 (C1) A bus access declaration that does not specify a bus access in this case, the abstract feature must not have a direction spe Bus Component Access p8710 (C2) If a bus access feature is a refinement of an abstract feature, if specified, in the defining identifier of a defined connection declaration must refer to a named connection declaration and an	Bus Component Access	p87n2	(N2) The defining identifier of a provides or requires bus refinement must exist as a defining identifier of a requires or pro	0		0
Bus Component Access 8712 (L1) If a bus access refers to a component classifier or a component prototype, then the category of the classifier or prox 0 0 0 0 0 0 0 0 0	Bus Component Access	p87n3	(N3) The component type identifier or component implementation name of a bus access classifier reference must exist in	0		0
Bus Component Access p8712 (L2) A bus access declaration may be refined by refining the bus classifier, by adding a property association, or by doing 0 0 0 0 0 0 0 0 0	Bus Component Access	p87n4	(N4) The prototype identifier, if present, must exist in the namespace of the classifier that contains the bus access declar	0		0
Bus Component Access p8713 (L.3) A provides bus access cannot be refined to a requires bus access and a requires bus access annot be refined to a 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	Bus Component Access	p87I1	(L1) If a bus access refers to a component classifier or a component prototype, then the category of the classifier or prototype	0		0
Bus Component Access p87i4 (L4) An abstract feature can be refined into a bus access. In this case, the abstract feature must not have a direction spe 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	Bus Component Access	p87l2	(L2) A bus access declaration may be refined by refining the bus classifier, by adding a property association, or by doing	0		0
Bus Component Access p87c1 (C1) A bus access declaration that does not specify a bus classifier reference is incomplete. Such a reference can be ad 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	Bus Component Access	p87I3	(L3) A provides bus access cannot be refined to a requires bus access and a requires bus access cannot be refined to a	0		0
Bus Component Access p87c2 (C2) If a bus access feature is a refinement of an abstract feature, then the direction of the abstract feature, if specified, i 0 0 0 Bus Component Access p87n1 (N1) The defining identifier of a defined connection declaration must be unique in the local namespace of the component of 0 0 Bus Component Access p87n2 (N2) The connection identifier in a connection refinement declaration must refer to a named connection declared in an ar 0 0 0 Bus Component Access p87n2 (L1) A connection identifier in a connection refinement must contain at least one of the following: a connection source and destination subclause, 0 0 Bus Component Access p87n2 (L2) If a semantic connection may be active in a particular mode, then the ultimate source and ultimate destination comp 0 0 Bus Component Access p87n3 (L3) If a semantic connection may be active in a particular mode transition, then the ultimate source component must be 0 0 0 Centre of the following: a connection of feature connection must referent of the particular mode transition, then the ultimate source component must be 0 0 0 Centre of the particular mode transition, then the ultimate source component must be 0 0 0 Centre of the particular mode transition must referent of the particular mus	Bus Component Access	p87I4	(L4) An abstract feature can be refined into a bus access. In this case, the abstract feature must not have a direction spe	0		0
Bus Component Access p87n1 (N1) The defining identifier of a defined connection declaration must be unique in the local namespace of the component of the component of the component of the component Access p87n2 (N2) The connection identifier in a connection refinement declaration must refer to a named connection declared in an arm of the component Access p87n2 (L1) A connection refinement must contain at least one of the following: a connection subclause, of the component Access p87n2 (L2) If a semantic connection may be active in a particular mode, then the ultimate source and destination subclause, of the component Access p87n3 (L3) If a semantic connection may be active in a particular mode transition, then the ultimate source component must be of the component Access p87n3 (L3) If a semantic connection may be active in a particular mode transition, then the ultimate source component must be of the component Access p87n3 (N2) The subcomponent of a feature connection refinement declaration must reference of the component of the connection and the connection of the connection of the connection feature connection of the connection feature connection of the connection of the connection of the connection must be of the connection features of the connection must be of the co	Bus Component Access	p87c1	(C1) A bus access declaration that does not specify a bus classifier reference is incomplete. Such a reference can be add	0	0	0
Bus Component Access 887n2 (N2) The connection identifier in a connection refinement declaration must refer to a named connection declared in an ar 0 0 0 0 0 0 0 0 0	Bus Component Access	p87c2	(C2) If a bus access feature is a refinement of an abstract feature, then the direction of the abstract feature, if specified, if	0	0	0
Bus Component Access p87n2 (N2) The connection identifier in a connection refinement declaration must refer to a named connection declared in an ar 0 0 0 Bus Component Access p87l1 (L1) A connection refinement must contain at least one of the following: a connection source and destination subclause, 0 0 0 Bus Component Access p87l2 (L2) If a semantic connection may be active in a particular mode, then the ultimate source and ultimate destination comp 0 0 0 Bus Component Access p87l3 (L3) If a semantic connection may be active in a particular mode transition, then the ultimate source component must be 0 0 0 Feature Connections p91l1 (N1) A source or destination reference in a feature connection or feature connection refinement declaration must reference p91l1 (N2) The subcomponent reference may refer to a subcomponent array. Feature Connections p91l2 (N2) The subcomponent reference may refer to a subcomponent array. Feature Connections p91l2 (L2) If the feature connection declaration represents a connection between features of sibling components, then the sour 0 0 Feature Connections p91l3 (L3) If the feature connection declaration represents a connection between features up the containment hierarchy, then to 0 0 Feature Connections p91l3 (L3) If the feature connection declaration represents a connection between features down the containment hierarchy, the 0 0 0 Feature Connections p91l4 (L4) If the feature connection declaration specifies a directional connection, then the direction of the connection must be 0 0	<u> </u>	<u> </u>		0		0
Bus Component Access p8711 (L1) A connection refinement must contain at least one of the following: a connection source and destination subclause, 0 0 0 Bus Component Access p8712 (L2) If a semantic connection may be active in a particular mode, then the ultimate source and ultimate destination comp 0 0 0 Bus Component Access p8713 (L3) If a semantic connection may be active in a particular mode transition, then the ultimate source component must be 0 0 0 Feature Connections p91n1 (N1) A source or destination reference in a feature connection or feature connection refinement declaration must referen 0 0 0 Feature Connections p91n2 (N2) The subcomponent reference may refer to a subcomponent array. 0 0 0 Feature Connections p91l1 (L1) If the feature connection declaration represents a connection between features of sibling components, then the sour 0 0 Feature Connections p91l2 (L2) If the feature connection declaration represents a connection between features up the containment hierarchy, then to 0 0 Feature Connections p91l3 (L3) If the feature connection declaration represents a connection between features down the containment hierarchy, then to 0 0 Feature Connections p91l4 (L4) If the feature connection declaration specifies a directional connection, then the direction of the connection must be 0 0 0		· -		0		0
Bus Component Access p8712 (L2) If a semantic connection may be active in a particular mode, then the ultimate source and ultimate destination comp 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	<u> </u>	+-		0		0
Bus Component Access p8713 (L3) If a semantic connection may be active in a particular mode transition, then the ultimate source component must be 0 0 0 Feature Connections p91n1 (N1) A source or destination reference in a feature connection refinement declaration must referen 0 0 0 Feature Connections p91n2 (N2) The subcomponent reference may refer to a subcomponent array. 0 0 0 Feature Connections p91l1 (L1) If the feature connection declaration represents a connection between features of sibling components, then the sour 0 0 Feature Connections p91l2 (L2) If the feature connection declaration represents a connection between features up the containment hierarchy, then to 0 0 Feature Connections p91l3 (L3) If the feature connection declaration represents a connection between features down the containment hierarchy, then to 0 0 Feature Connections p91l4 (L4) If the feature connection declaration specifies a directional connection, then the direction of the connection must be 0 0	· · · · · · · · · · · · · · · · · · ·	<u> </u>		0		0
Feature Connections p91n1 (N1) A source or destination reference in a feature connection or feature connection refinement declaration must referen 0 0 0 Feature Connections p91n2 (N2) The subcomponent reference may refer to a subcomponent array. 0 0 0 Feature Connections p91n1 (L1) If the feature connection declaration represents a connection between features of sibling components, then the sour 0 0 Feature Connections p91n2 (L2) If the feature connection declaration represents a connection between features up the containment hierarchy, then the sour 0 0 Feature Connections p91n3 (L3) If the feature connection declaration represents a connection between features down the containment hierarchy, then the sour 0 0 Feature Connections p91n3 (L3) If the feature connection declaration represents a connection between features down the containment hierarchy, then the sour 0 0 0 CFeature Connections p91n4 (L4) If the feature connection declaration specifies a directional connection, then the direction of the connection must be consistent of the connection declaration connectio	<u> </u>	+		0		0
Feature Connections p91n2 (N2) The subcomponent reference may refer to a subcomponent or a subcomponent array. 0 0 0 Feature Connections p91l1 (L1) If the feature connection declaration represents a connection between features of sibling components, then the sour 0 0 Feature Connections p91l2 (L2) If the feature connection declaration represents a connection between features up the containment hierarchy, then to 0 0 Feature Connections p91l3 (L3) If the feature connection declaration represents a connection between features down the containment hierarchy, then to 0 0 Feature Connections p91l4 (L4) If the feature connection declaration specifies a directional connection, then the direction of the connection must be 0 0 0			(22) In a comment of the second of the particular mode distribution, then the distribute outlier of the second of			-
Feature Connections p91n2 (N2) The subcomponent reference may refer to a subcomponent or a subcomponent array. 0 0 0 Feature Connections p91l1 (L1) If the feature connection declaration represents a connection between features of sibling components, then the sour 0 0 Feature Connections p91l2 (L2) If the feature connection declaration represents a connection between features up the containment hierarchy, then to 0 0 Feature Connections p91l3 (L3) If the feature connection declaration represents a connection between features down the containment hierarchy, the 0 0 Feature Connections p91l4 (L4) If the feature connection declaration specifies a directional connection, then the direction of the connection must be 0 0	Feature Connections	n91n1	(N1) A source or destination reference in a feature connection or feature connection refinement declaration must reference	0		0
Feature Connections p911 (L1) If the feature connection declaration represents a connection between features of sibling components, then the sour 0 0 0 Feature Connections p912 (L2) If the feature connection declaration represents a connection between features up the containment hierarchy, then t 0 0 0 Feature Connections p913 (L3) If the feature connection declaration represents a connection between features down the containment hierarchy, the 0 0 0 Feature Connections p914 (L4) If the feature connection declaration specifies a directional connection, then the direction of the connection must be 0 0 0		l'		n	· · ·	٥
Feature Connections p9112 (L2) If the feature connection declaration represents a connection between features up the containment hierarchy, then t 0 0 0 Feature Connections p913 (L3) If the feature connection declaration represents a connection between features down the containment hierarchy, the 0 0 0 Feature Connections p914 (L4) If the feature connection declaration specifies a directional connection, then the direction of the connection must be 0 0 0		+		n		0
Feature Connections p9113 (L3) If the feature connection declaration represents a connection between features down the containment hierarchy, the 0 0 Feature Connections p9114 (L4) If the feature connection declaration specifies a directional connection, then the direction of the connection must be 0 0 0		l'		0		٥
Feature Connections p9114 (L4) If the feature connection declaration specifies a directional connection, then the direction of the connection must be 0 0				0		0
				- 0		v
Peature Connections P9115 (L5) The Individual connections of a semantic connection must be bidirectional or have the same direction. The direction U U U U U U U U U		· -		U		Ů
	reature Connections	p9115	(LS) THE INDIVIDUAL CONNECTIONS OF A SEMANTIC CONNECTION MUST be Didirectional or have the same direction. The direction	U	J V	U
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Port Connections	p92n1	(N1) The connection identifier in a port connection refinement declaration must refer to a named port or feature connectid 0	
Port Connections	p92n2	(N2) A source or destination reference in a port connection or port connection refinement declaration must reference a p	0
Port Connections	p92n3	(N3) The subcomponent reference may also consist of a reference to a subcomponent array.	0
Port Connections	p92n4	(N4) The event_or_event_data identifier of event source specifications (self.event_or_event_data_identifier) must not cor 0	
Port Connections	p92l1	(L1) In the case of a directional port connection the connection end representing the source of the flow must be the source of	
Port Connections	p92l2	(L2) In the case of a bidirectional port connection either connection end can be the source. If the bidirectional connection 0	
Port Connections	p92l3	(L3) If the source connection end is a data access feature it must have read access rights; if the destination connection e	0
Port Connections	p92l4	(L4) The feature identifier of a subcomponent reference may refer to a feature array, if the subcomponent is a thread, de 0	0
Port Connections	p92l5	(L5) The following are acceptable sources and destinations of port connections. The left column shows connections betw 0	
Port Connections	p92l6	(L6) If the port connection declaration represents a connection between ports of sibling components, then the source mu 0	
Port Connections	p92I7	(L7) If the port connection declaration represents a connection between ports up the containment hierarchy, then the sou 0	
Port Connections	p92l8	(L8) If the port connection declaration represents a connection between ports down the containment hierarchy, then the \$ 0	0
Port Connections	p92l9	(L9) The individual connections of a semantic port connection must be bidirectional or have the same direction. The direction of the direction	
Port Connections	p92l10	(L10) Self. <identifier> must only be referenced as the source of a connection.</identifier>	0
Port Connections	p92l11	(L11) A data port cannot be the destination of more than one semantic port connection unless each semantic port connection	
Port Connections	p92l12	(L12) A semantic connection cannot contain connection declarations with both immediate and delayed Timing property v 0	
Port Connections	p92l13	(L13) For connections between data ports, event data ports and data access, the data classifier of the source port must r	
Port Connections	p92l14	(L14) The following rules are supported: • • • • Сlassifier_Match: The source data type and data implemente 0	
Port Connections	p92l15	(L15) If more than one port connection declaration in a semantic port connection has a property association for a given c 0	
Port Connections	p92l16	(L16) A processor port specification must only be used in event connections within threads and subprograms.	
Port Connections	p92c1	(C1) There cannot be cycles of immediate connections between threads, devices, and processors.	0
Port Connections	p92c2	(C2) The processor port identifier of a processor port specification (processor processor port_identifier) must name a po 0	
Port Connections	p92c3	(C3) The Supports Classifier Subset Matches property may be associated with a bus or virtual bus. This specifies the s	0
Port Connections	p92c4	(C4) The Supports_Type_Conversions property may be associated with a bus or virtual bus. This specifies the subset may 0	0
Parameter Connections	p93n1	(N1) The connection identifier in a parameter connection refinement declaration must refer to a named parameter or feat 0	
Parameter Connections	p93n2	(N2) A source (destination) reference in a parameter connection declaration must reference a parameter of a preceding (0	0
Parameter Connections	p93l1	(L1) The source of a parameter connection must be an incoming data or event data port of the containing thread, an inco	0
Parameter Connections	p93l2	(L2) The following source/destination pairs are acceptable for parameter connection declarations: threadport -> call.para 0	0
Parameter Connections	p93l3	(L3) A parameter cannot be the destination feature reference of more than one parameter connection declaration unless 0	0
Parameter Connections	p93l4	(L4) The data classifier of the source and destination must match. The matching rules as specified by the Classifier_Matc 0	0
Access Connections	p94n1	(N1) The connection identifier in an access connection refinement declaration must refer to a named access or feature of	0
Access Connections	p94n2	(N2) An access reference in an access connection declaration must reference an access feature of a subcomponent, sut 0	0
Access Connections	p94l1	(L1) The category of the source and the destination of a access connection declaration must be the same, i.e., they must	0
Access Connections	p94I2	(L2) In the case of a bidirectional semantic access connection either connection end can be the source.	0
Access Connections	p94I3	(L3) In the case of a directional data or bus access connection the connection end representing the component being acc	0
Access Connections	p94I4	(L4) In a partial AADL model the ultimate source or destination may be a provides access feature of a component instead 0	0
Access Connections	p94I5	(L5) If the access connection declaration represents an access connection between access features of sibling componen 0	0
Access Connections	p94l6	(L6) If the access connection declaration represents a feature mapping up the containment hierarchy, then one connectio	0
Access Connections	p94I7	(L7) If the access connection declaration represents a feature mapping down the containment hierarchy, then one conne	0
Access Connections	p94I8	(L8) A requires access cannot be the source or destination feature reference of more than one access connection declared the control of the c	0
Access Connections	p94I9	(L9) For access connections the classifier of the provider access must match to the classifier of the requires access acco 0	0
Access Connections	p94I10	(L10) If more than one access feature in a semantic access connection has an Access Right property association, then to	0
Access Connections	p94l11	(L11) The category of the access connection source and destination must be identical. If the component category is sped 0	
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Feature Group Connections	p95n1	(N1) The connection identifier in a feature group connection refinement declaration must refer to a feature group named (0	0
Feature Group Connections		(N2) A source or destination reference in a feature group connection declaration must reference a feature group declared	0
Feature Group Connections		(L1) If the feature group connection declaration represents a component connection between sibling components, the fea	0
Feature Group Connections		(L2) The Classifier_Matching_Rule property specifies the rule to be applied to match the feature group classifier of a con 0	0
Feature Group Connections		(L3) The following rules are supported for feature group connection declarations that represent a connection up or down 0	0
Feature Group Connections	i -	(L4) The following rules are supported for feature group connection declarations that represent a connection between two 0	0
. Sature Group Connections	12001-7	12-17 The following ratio and supported for realizing group connection decisions that represent a connection between two	~

Feature Group Connectionsp95l5	(L5) If the feature group connection declaration represents a connection between feature group of sibling components, th	0	0	0
Feature Group Connectionsp95l6	(L6) If the feature group connection declaration represents a connection between feature groups up the containment hier	0	0	0
Feature Group Connectionsp95I7	(L7) If the feature group connection declaration represents a connection between feature groups down the containment h	0	0	0
Feature Group Connectionsp95l8	(L8) A feature group connection must be bidirectional or be consistent with the direction of the source and destination feature	0	0	0