CRYPTOLINE

January 15, 2022

1 Introduction

CRYPTOLINE is a tool and a language for the verification of low-level implementations of mathematical constructs. In CRYPTOLINE, users can specify two kinds of properties, namely algebraic properties and range properties. Algebraic properties involve equalities and modular equalities in the integer domain while range properties involve bit-accurate variable ranges. CRYPTOLINE verifies algebraic properties and range properties separately. Verification of algebraic properties is reduced to ideal membership queries which are solved by external computer algebra systems. Verification of range properties is reduced to Satisfiability Modulo Theories (SMT) queries which are solved by external SMT solvers.

2 CryptoLine Language

A Syntax of CryptoLine

An *identifier* is a regular string started by a letter or an underscore, followed by letters, digits, or underscores.

```
id ::= (letter \mid underscore)[letter \mid digit \mid underscore]
```

All constants and variables in CRYPTOLINE are typed. Let w be a positive integer. $\mathsf{uint} w$ and $\mathsf{sint} w$ in CRYPTOLINE denote the types of bit-vectors with width w in the unsigned and two's complement signed representations respectively. The type $\mathsf{uint}1$ is also written as bit .

```
typ ::= uint1 \mid sint2 \mid uint2 \mid sint3 \mid \cdots \mid uintw \mid sint(w+1)
```

A constant is an integer, a hexadecimal number, a named constant, or arith-

metic expressions over constants.

The value of a named integer c is read by c. Cryptoline supports the following arithmetic operators over constants: unary minus (-), addition (+), subtraction (-), multiplication (*), and exponent (**). A typed constant is a constant with its type explicitly specified.

A *variable* is an identity. A *typed variable* is a variable with its type explicitly specified. An *lval* is either a variable or a typed variable.

```
\begin{array}{cccc} var & ::= & id \\ typed\_var & ::= & var@typ \mid typ \ var \\ lval & ::= & var \mid typed\_var \end{array}
```

The notation t_{\circ}^* and t_{\circ}^+ respectively represents a possibly empty and a non-empty sequence of \circ -separated t.

An *atom* is either a typed constant, a variable, or a typed variable. It is not necessary to specify the variable type explicitly in an atom because CRYPTOLINE can infer the type automatically.

```
atom ::= typed\_const \mid var \mid typed\_var
```

An algebraic expression is evaluated over \mathbb{Z} .

limbs n [e_1, \ldots, e_m] represents $e_1 + e_2 2^n + e_3 2^{2n} + \cdots + e_m 2^{mn}$. A range expression is evaluated over bit vectors. const w n is a bit-vector of width w and value n. $\sim (neg)$ is logical negation. ! (not), & (and), | (or), $\hat{}$ (xor) are respectively bitwise negation, bit-wise AND, bit-wise OR, and bit-wise XOR. umod is unsigned remainder. srem is 2's complement signed remainder (sign follows dividend).

smod is 2's complement signed remainder (sign follows divisor). *uext* and *sext* are respectively unsigned and signed extension operations.

```
rexp ::= ( rexp ) | const const const | − rexp | rexp + rexp | rexp + rexp | rexp + rexp | rexp | rexp | not rexp | not rexp | rexp & rexp | and rexp rexp | rexp | rexp | rexp | cor rexp rexp | rexp | rexp | xor rexp rexp | xor rexp rexp | smod rexp rexp | simbs const [ rexp<sup>+</sup> ] | uext rexp const | sext rexp const |
```

A predicate is represented by an algebraic predicate and a range predicate.

```
pred ::= true | epred && rpred
```

An algebraic predicate is evaluated over the integer domain. $e_1 = e_2$ (eq e_1 e_2) is an equality over algebraic expressions. $e_1 = e_2$ (mod e_3) (eqmod e_1 e_2 e_3) is a modular equality. $p_1 \ / \ p_2$ (and p_1 p_2) is a logical conjunction of p_1 and p_2 . The conjunction of a sequence of algebraic predicates e_1, \ldots, e_n is written as $/ \ [e_1, \ldots, e_n]$ (and $[e_1, \ldots, e_n]$).

A range predicate specifies the ranges of variables. CRYPTOLINE offers comparisons such as equality (=), modular equalities (equmod, eqsmod, eqsrem), unsigned less than (<), unsigned less than or equal to (<=), unsigned greater than (>), unsigned greater than or equal to (>=), signed less than (< s), signed less than or equal to (<= s), signed greater than (> s), and signed greater than

or equal to (>=s).

```
rpred ::=
            (rpred)
                                              true
            rexp = rexp
                                              eq rexp rexp
            rexp = rexp (umod rexp)
                                              equmod rexp rexp rexp
             rexp = rexp (smod rexp)
                                              eqsmod rexp rexp rexp
            rexp = rexp (srem rexp)
                                              eqsrem rexp rexp rexp
            rexp < rexp
                                              ult rexp rexp
                                              ule rexp rexp
            rexp \le rexp
                                              ugt rexp rexp
            rexp > rexp
            rexp >= rexp
                                              uge rexp rexp
            rexp < s rexp
                                              slt rexp rexp
            rexp \le s rexp
                                              sle rexp rexp
                                              sgt rexp rexp
            rexp > s rexp
            rexp >= s rexp
                                              sge rexp rexp
            \sim rpred
                                              neg rpred
            rpred \ / \ rpred
                                              and rpred rpred
             rpred \/ rpred
                                              or rpred rpred
             / [rpred^+]
                                              and [ rpred<sup>+</sup> ]
             or [ rpred<sup>+</sup>]
```

There are numerous instructions supported by Cryptoline. $mov \ x \ a$ assigns destination variable x by the value of the source atom a. $cmov \ x \ c \ a_1 \ a_2$ assigns destination variable x by the value of the source atom a_1 if the condition bit c is 1, and otherwise by the value of the source atom a_2 . add $x a_1 a_2$ assigns x by the addition of the source atoms a_1 and a_2 . Note that add may overflow. adds $c \times a_1 \times a_2$ assigns x by the addition of the source atoms a_1 and a_2 with carry bit c set. addr c x a_1 a_2 assigns x by the addition of the source atoms a_1 and a_2 with carry bit c reset to 0. adc x a_1 a_2y assigns x by the addition of the carry bit y and the source atoms a_1 and a_2 . adcs and adcr are the same as adc except the carry bit is respectively set and reset. There are also instructions sub for subtraction; subc, sbc and isbcs for subtraction with carry; subb, sbb, and sbbs for subtraction with borrow. mul, muls, and mulr are half multiplication operations. The differenace is that muls sets the carry bit if the multiplication under- or over-flow while multiplication under- over- o is full multiplication with results split into high part and low part. muli is also full multiplication without splitting the results. set x assigns the bit variable x by 1 while clear x assigns the bit variable x by 0. and, or, not, and xor are bit-wise operations. assert tells CRYPTOLINE to verify the specified predicate. assume tells Cryptoline to assume the specified predicate. $cut \ e \ \&\& \ r$ is an alias of one ecut e followed by a rcut r. For ecut, CRYPTOLINE verifies the specified algebraic predicate and starts afresh with the predicate assumed when verifying algebraic properties. Similarly for rcut, CRYPTOLINE verifies the specified range predicate and starts afresh with the predicate assumed when verifying range properties. call $p(a_1, a_2, \ldots, a_n)$ executes a defined procedure

p with arguments a_1, a_2, \ldots, a_n .

```
instr ::=
            mov lval atom
                                               cmov lval lval atom atom
            add lval atom atom
                                               adds lval lval atom atom
            addr lval lval atom atom
                                               adc lval atom atom var
            adcs lval lval atom atom var
                                               adcr lval lval atom atom var
            sub lval atom atom
                                               subc lval lval atom atom
            subb lval lval atom atom
                                               subr lval lval atom atom
            sbc lval atom atom var
                                               sbcs lval lval atom atom var
            sbcr lval lval atom atom var
                                               sbb lval atom atom var
            sbbs lval lval atom atom var
                                               sbbr lval lval atom atom var
            {\it mul} {\it lval} {\it atom} {\it atom}
                                               muls lval lval atom atom
            mulr lval lval atom atom
                                               mull lval lval atom atom
                                               nondet lval
            mulj lval atom atom
            set lval
                                               clear lval
            shl lval atom const
                                               cshl lval lval atom atom const
            split lval lval atom const
                                               join lval lval atom const
            and lval atom atom
                                               or lval atom atom
            xor lval atomatom
                                               not lval atom
            assert pred
                                               assume pred
            cut pred_clause
                                               ecut\ epred\_clause
                                               ghost typed\_var^+: pred
            \it rcut \ rpred\_clause
            call id ( atom^* )
```

Instructions add, adds, addr, adc, adcs, adcr, sub, subc, subb, subr, sbc, sbcs, sbcr, sbb, sbbs, sbbr, mul, muls, mulr, mull, mulj, and split also have specific unsigned and signed versions with prefix "u" or "s". For example, uadd and sadd are respectively unsigned and signed versions of add.

Sometimes a predicate has to be proved with facts that have been cut off. CRYPTOLINE offers the specification of hints required to prove a predicate.

```
pred\_clause ::=
                                         epred_clause && rpred_clause
                     true
epred\_clause
                     epred
                                         epred prove with [prove_with_+]
                     epred\_clause^+
                                        rpred prove with [prove_with, +]
rpred\_clause
                    rpred
                    rpred\_clause^+
                    precondition
                                         all cuts
 prove\_with ::=
                    all\ assumes
                                         all ghosts
                     cuts [\mathbb{N}^+]
```

Note that the indices of ecut and rcut are numbered separately (starting from 0). When verifying algebraic properties, rcut instructions are ignored. When verifying range properties, ecut instructions are ignored. For example, consider the following program.

```
mov x 15@uint16;
ecut x = 15;
```

```
mov y 3@uint16;

cut y = 3 && and [x = 15@16, y = 3@16];

add z x y;

rcut z = 18@16;
```

If we want to prove e prove_with [cuts[1]] && r prove_with [cuts[1]], then y=3 will be assumed when proving the algebraic property e while z=18@16 will be assumed when proving the range property r.

A procedure is a parameterized program together with its specification (precondition and postcondition).

```
proc ::= proc id (formals) = \{ pre \} prog \{ post \}
```

The $formal\ parameters$ of a procedure may be separated by a semicolon into inout and out variables.

```
formals ::= typed\_var^* \mid typed\_var^*; typed\_var^*
```

Variables before the semicolon are inout variables while variables after the semicolon are out variables. Formal parameters without a semicolon are all inout variables. The difference between inout and out variables is that when calling a procedure, actual parameters of the inout formal variables must be defined but this is not required for the actual parameters of the out formal variables. However, this does not mean that an out variable can be read before initialized. Every variable must be initialized before reading its value. A precondition is a predicate.

$$pre ::= pred$$

A postcondition is a predicate clause.

```
post ::= pred\_clause
```

A *statement* is a declaration of a procedure or a named integer.

```
stmt ::= proc \mid const id = const
```

A *program* is a sequence of semicolon separated statements. The entry point of the program is the *main* procedure. Other procedures called in main are inlined.

$$prog ::= stmt_{:}^{+}$$