**Reza Amani** Mobile: +64-223688977

Email: reza.amani@gmail.com

https://www.linkedin.com/in/rezaamani

Summary

Electronics engineer with 15 years’ experience in a variety of positions and fields, including embedded hardware/software implementation, RF engineering, system architecture, DSP engineering, test and debug with a great track record of accomplishing projects within limited budget and time.

Technical skills

* Microcontroller hardware and software
* Analogue, digital and mixed systems
* system architecture
* Designing, simulating and evaluating RF modules; RF amplifier, mixer, PLL, up/down converter, filter, synthesizer, etc.
* Simulating/optimising RF blocks by AWR
* Electronic measurements; Spectrum-analyzer, network analyzer
* Modulations; AM, FM, FSK, OOK
* Analog Devices solutions for sub-GHz short-range wireless communication
* SDR platform; HW and SW design
* Signal processing
* Developing communication protocols
* Superheterodyne, Single chip transceivers, IQ modulator/demodulator
* Embedded programming within limitations
* Test and debug
* C, C++ and assembly
* Python, Visual C# and Visual C++
* Developing peripheral drivers, ADC, DMA, I2C, UART, SPI, etc.
* Driving external data-acquisition parts, measuring technical parameters, ENOB, THD
* Agile methodology, Scrum TDD, unit testing
* Version control software; SmartGit, Gitlab
* Safety-critical programming, fault trees, IEC and AS/NZ safety standards
* Analog circuits; design and debug
* EMC and EMI
* PCB layout considerations for reducing interference and noise
* Power supplies, filters, signal shaping
* Noise challenges in mixed systems
* DC/DC converters and battery management
* Analogue circuits simulation with LTSpice
* Designing and implementing test jigs
* Ability to translate user requirements into technical solutions
* Predict future problems and providing best solutions during design stage

Soft Skills

**Problem solver:** Actively seeking and resolving technical and operational problems.

**Teamwork:** Extensive experience in team projects at different levels including small team leadership.

**Independent:** Able to work without being supervised; with preference for handle barriers in person.

**Supporter:** Eager to mentor colleagues and help them to debug their works.

**Time management:** committed to do the job in time with prioritising tasks and a fast pace

**Hands-on Engineering:** Involving closely and directly in all tasks of projects.

Attributes

* Action-oriented
* Co-operative
* Decisive
* Detail-oriented
* Can-do attitude
* Creative
* Calm
* Avid traveller
* Seeking out new responsibilities
* Logical thinker

Experience

**R&D engineer** 2015-now

**Tru-test group, Auckland office**

Responsibilities:

* Embedded software programmer for a state-of-the-art energiser
* Signal processing; real-time pulse processing
* Ensuring compliance with safety standards; IEC, AS/NZ, EN
* Gathering data from field tests, simulating real data in lab
* Design architecture, HW prototyping and SW implementation for independent safety module as a team member
* Working on legacy codes, debugging and improving
* Code reviewing

Achievements:

* Suggested a new mixed SW/HW idea for safety module to reduce TMC and TTM
* Designed new signal processing method that enabled us to perform in-pulse calculations
* Provided ideas to increase software development pace
* Improved the cooperation between HW and SW team by a common understanding

**Embedded/DSP engineer, RF/analog engineer, system architecture** 2005-2015

**Pardazesh Basamad Ltd.**

An agile, small size, high-tech Company performing high-level R&D projects

Responsibilities:

* HW and SW designing for DSP systems and SDR platforms
* Designing RF modules using MMIC’s, simulating with AWR
* Performing some mechanical calculations and interfacing electronic concepts with mechanical requirements
* Choosing and setting up platforms; ARM, PIC and AVR microcontrollers, Piccolo and floating point DSP’s
* Designing and implementing serial protocols for wireless projects
* Technical negotiating with the costumer, providing solution and choosing platform
* Proof-of-Principle, Form Study and/or functional prototyping
* Designing, implementing and testing to meet military environment requirements
* Professional testing and evaluating radio systems
* Supervising junior engineers, helping other teams to overcome their barriers

Achievements:

* Designed and implemented a high-tech AD-HOC FH-SS handheld wireless radio, using TI DSP’s and Analog Devices ISM-band transceiver modules
* Proposed and developed an innovative wide-band spread-spectrum wireless link for remote-controlling a UAV, robust against interference, interception and jamming
* Successfully managed the project of prototyping 200W 200MHz-1050MHz RF Amplifier
* Designed and implemented a secure FH-SS video down-link for UAV’s
* Reverse-engineering for an old under-water communication system, in order to improve it for new requirements
* Proposed an under-ground communication system based on seismic signal processing
* Introduced a new idea of combining MAC and PHY layers in a frequency-hopping Ad-Hoc radio with a state-of-the-art robust routing algorithm
* Developed a low-cost reliable embedded system to control chemical and pharmaceutical manufacturing systems with complicated processes
* Successfully managed to develop a military product following some of “Mil-std-810A” and “Mil-std-810G part two” rules and methods
* Suggesting a new communication system in a parking management/guidance system, led to wiring costs being halved; using WSN, Ad-Hoc and wireless technologies
* Developed an automatic system to test digital cards of up to 80 I/O’s
* Integrated power supplies of an aircraft, re-designed protection modules and halved occupied space
* Built a Hall-effect current sensor with full range of 50A for testing our products in steady-state and evaluating them in transients

**Digital electronics engineer, Driver/Firmware developer** 2002-2005

**Basamad Negar Ltd.**

Developer of laboratory and broadcast products

Responsibilities:

* Digital hardware designing; schematic and PCB
* Developing peripheral drivers for DSP and microcontroller in C and assembly
* Implementing simple GUI’s with visual C++ 6.0
* Designing and testing analogue interfaces and high precision circuits
* Proof-of-Principle, Form Study and/or functional prototyping
* Planning and teaching costumers training courses

Achievements:

* Reached the record of 100MS/s sampling rate and 13.5 ENOB with an acquisition board
* Cut the hardware cost of future projects by 80% suggesting and developing a general purpose SDR platform
* Successfully implemented telephony algorithms on DSP; FSK, ADPCM, CALLER-ID, etc.

**Signal processing group member** 2000-2002

**Professor Hesabi organization (NGO)**

A scientific and research non-governmental organization

Responsibilities:

* PCB design, montage, primary tests
* Developing DSP drivers with assembly
* System test and evaluating

Achievement:

* As a team member, managed to develop a portable battery-powered DSP-based ANC (Active noise controller)

Education

**M.Sc. in Digital Electronics Engineering 2000**

**Sharif University of Technology**

Thesis: Debugging method for parallel-processing DSP systems

Implemented on a platform consisting 4 floating-point digital-signal-processors

**B.Sc. in Electronics Engineering 1998**

**Sharif University of Technology**

Final project: Implementing a narrowband FSK transceiver

Controlled and used by a MCS-51 microcontroller

Computer Skills

Altium/ Protel 99SE Jira, Gitlab, Git, version control systems

Xilinx ISE, VERILOG and VHDL Microsoft office, Microsoft Visual Studio

TI code composer, Visual DSP Analog Devices simulation programs

Code Vision, Keil uvision, Eclipse, AVR studio AWR (RF simulation)

Awards

3 silver medals from physics, mathematics and computer students Olympiads, 1993

2nd place in university scientific competitions among 150 students, 1998

8th place in national electrical/electronic engineering Olympiad, 2000

Outside interests

Chess (Fide Rating: 1624, Howick club team member) Team sports, volleyball

Physics Classical music

Psychology Travelling

Referees

Available on request\*