**Reza Amani** Pakuranga heights, Auckland 2010

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Summary

An embedded software engineer with experience in a variety of positions and fields, including electronics engineering, software implementation, DSP, test and debug with a great track record of accomplishing projects within limited budget and time.

Technical skills

* Microcontroller hardware and software
* Analogue, digital and mixed systems
* system architecture
* Ability to translate user requirements into technical solutions
* Predict future problems and providing best solutions during design stage

Software

* C, C++ and assembly
* Embedded programming within limitations
* Test and debug
* Python
* Visual C#, Visual C++
* RTOS, design patterns
* OOP
* Software architecture
* Developing peripheral drivers
* Agile methodology, Scrum
* TDD, unit testing
* Version control software; SmartGit, Gitlab
* Familiar with Jira, ProjectPlace, …
* Common IDE’s, Eclipse, Keil, IAR, …
* GCC, MakeFile
* Bluetooth programming, SiLab and Nordic
* Safety-critical programming, fault trees, IEC and AS/NZ safety standards

Analog

* Analog circuits; design and debug
* Schematic and PCB
* Analogue circuits simulation with LTSpice

DSP

* SDR, implementing communication algorithms by DSP’s
* Modulations; AM, FM, FSK, OOK
* Implementing signal processing algorithms on DSP’s & MCU’s with maximum performance

Soft Skills

**Problem solver:** Actively seeking and resolving technical and operational problems.

**Teamwork:** Extensive experience in team projects at different levels including small team leadership.

**Independent:** Able to work without being supervised; with preference for handle barriers in person.

**Supporter:** Eager to mentor colleagues and help them to debug their works.

**Hands-on Engineer:** Involving closely and directly in all tasks of projects.

**Time management:** committed to do the job in time with prioritising tasks and a fast pac

Attributes

* Action-oriented
* Co-operative
* Detail-oriented
* Can-do attitude
* Creative
* Calm
* Avid traveller
* Logical thinker

Experience

**R&D engineer** 2015-now

**Tru-test group, Auckland office**

Responsibilities:

* Embedded software programmer for a state-of-the-art energiser
* Signal processing; real-time pulse processing
* Ensuring compliance with safety standards; IEC,AS/NZ, EN
* Gathering data from field tests, simulating real data in lab
* Design architecture, HW design and SW implementation for independent safety module
* Working on legacy codes, debugging and improving
* Code reviewing
* Investigating development tools, to help selection of platform for future projects
* Technical supervisor of SW team for a project

Achievements:

* Configuring Bluetooth Low Energy in advanced modes, e.g. master/slave simultaneously
* Suggested a new mixed SW/HW idea for safety module to reduce TMC and TTM
* Designed new signal processing method that enabled us to perform in-pulse calculations
* Provided ideas to increase software development pace
* Improved the cooperation between HW and SW team by a common understanding
* 3 times titled as “Engineer of the month”

**Embedded designer, system architecture** 2005-2015

**Pardazesh Basamad Ltd.**

An agile, small size, high-tech Company performing high-level R&D projects

Responsibilities:

* HW and SW designing for DSP systems, SDR platforms
* Simulating
* Programming embedded systems with C/C++
* Choosing and setting up platforms; ARM, PIC and AVR microcontrollers, Piccolo and floating point DSP’s
* Technical negotiating with the costumer, providing solution and choosing platform
* Proof-of-Principle, Form Study and/or functional prototyping
* Working on legacy codes, debugging and improving

Achievements:

* Designed and implemented a high-tech AD-HOC FH-SS handheld wireless radio, using TI DSP’s and Analog Devices ISM-band transceiver modules
* Proposed and developed an innovative FH wireless link for remote-controlling a UAV, robust against interference, interception and jamming

**Digital electronics engineer, Driver/Firmware developer** 2002-2005

**Basamad Negar Ltd.**

Developer of laboratory and broadcast products

Responsibilities:

* Developing peripheral drivers for DSP and microcontroller in C and assembly
* Implementing simple GUI’s with visual C++ 6.0
* Designing signal processing algorithms
* Proof-of-Principle, Form Study and/or functional prototyping
* Planning and teaching costumers training courses

Education

**M.Sc. in Digital Electronics Engineering**

**Sharif University of Technology**

Thesis: Debugging method for parallel-processing DSP systems

Implemented on a platform consisting 4 floating-point digital-signal-processors

**B.Sc. in Electronics Engineering**

**Sharif University of Technology**

Final project: Implementing a narrowband FSK transceiver

Controlled and used by a MCS-51 microcontroller

Computer Skills

Altium/ Protel 99SE Microsoft office

Keil uvision Microsoft Visual C# and C++

TI code composer, Visual DSP Analog Devices simulation programs

Code Vision, Keil uvision, Eclipse, AVR studio AWR (RF simulation)

Jira, Gitlab, Git, version control systems Simplicity Studio, SEGGER embedded IDE

Awards

3 silver medals from physics, mathematics and computer students Olympiads, 1993

2nd place in university scientific competitions among 150 students, 1998

8th place in national electrical/electronic engineering Olympiad, 2000

Outside interests

Chess (Fide Rating: 1624, Howick club team member) Table Tennis, volleyball

Physics Classical music

Psychology Travelling

Referees

Available on request\*