

Reza Eyvazpour

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Research Interest

- Hardware Implementation on FPGA
- Biomedical Signal Processing
- Machine and Deep Learning
- Image Processing and machine Vision

Education

Academic Qualifications.....

- **Master of Science, Electronic Engineering, Digital Integrated Circuit Design** **Tabriz, Iran**
Faculty of Electrical and Computer Engineering, University of Tabriz *2017-in Progress*
GPA:17.61/20
Thesis field: Hardware Implementation of pose estimation algorithm for Augmented Reality Application
- **Bachelor of Science, Electronic Engineering** **Bonab-Iran**
Faculty of Engineering, University of Bonab *2013-2017*
GPA:18.51/20
Thesis field: Evaluation of spasticity in knee joint using pendulum test

Publications

- **Objective assessment of spasticity by pendulum test: a systematic review on methods of implementation and outcome measures**
F.Rahimi, **R.Eyvazpour**, N.Salahshour, and M.R.Azghani
Journal of NeuroEngineering and Rehabilitation, 2020, (Submitted)
- **Crossing obstacles in a walkway: on the capability of wavelet-based detection strategies using wearable sensor data**
F.Rahimi, **R.Eyvazpour**, B.Nobahar, M.Jog, and C.Duval
Journal of Advanced Signal Processing, 2020, (Under Review, In Persian Language)

Research Experience

Accelerating Pose Estimation Algorithm on Hardware Platform

- Implementation of visual odometry estimation algorithm using RGB-D cameras on FPGA

Objective evaluation and Quantitative (sensor-based) analysis of movement

- Objective assessment of spasticity by pendulum test: a systematic review on methods of implementation and outcome measures, F.Rahimi, **R.Eyvazpour**, N.Salahshour, and M.R.Azghani, Journal of NeuroEngineering and Rehabilitation, 2020.
- Crossing obstacles in a walkway: on the capability of wavelet-based detection strategies using wearable sensor data, F.Rahimi, **R.Eyvazpour**, B.Nobahar, M.Jog, and C.Duval, Journal of Advanced Signal Processing, 2020.

Teaching Experiences

Teaching Assistant

- Introduction to Electrical Engineering, University of Bonab, Fall 2019,2018.
- Computer aided design of digital systems, University of Tabriz, Fall 2018.

Lab Assistant

- Instrumentation Laboratory, University of Bonab, Spring 2017.

Honors and Awards

- Ranked 3rd among M.Sc Electronic(Integrated Circuit Design) Engineering students, 2020.
- Ranked 1rd among B.Sc Electrical Engineering students, 2017.

Technical Skills

- **Programming Languages:** C/C++, Python, Octave.
- **Hardware Description Languages:** VHDL, Verilog.
- **Technical Tools:** Xilinx Vivado, Xilinx ISE, Altera Quartus, ModelSim, Matlab, Altium Designer.
- **Typesetting:** T_EX, Microsoft Office(Word, Power Point, Excel).

Languages

Azeri: Mother-tongue

Persian: Professional Working Proficiency

English: Professional Working Proficiency

References

Ghader Karimian

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