Use **Data Flow Modeling** and write the codes of half subtractor and full subtractor (3-bits input and 2-bits output).

- 1. In the module file use assign statement
- **2.** Write the testbench
- 3. Use RTL viewer and show the drawing of half subtractor and full subtractor
- 4. Simulate the testbench and make the analyze of timing diagram
- 5. Run the codes on the board