

Q → 4(b) Explain in detail about the Threads and their management?

Ans → 4(b) A Thread is a light weight sub process. It is the basic unit of CPU utilization.

It includes Thread ID, Program Counter, Register set & Stack. It shares with other threads belonging to the same process its code section, data section and the other OS resources such as files. A traditional or heavy weight process has a single thread of control. If a process has multiple threads of control it can do more than one task at the same time.

An application is typically implemented as separate process with several threads of control.

A word processor may have a thread for displaying graphics, another thread for reading key strokes from the user and a third thread for performing spell checking in background and this is called Multithreading.

* Benefits of Multithreadings.

(a) Responsiveness ⇒ Multithreading and interactive application may allow the program to continue running even if part of it blocks or performing a lengthy operation. thereby increasing responsiveness to the user.

(b) Resource sharing ⇒ By default, threads share the memory and the resources of the process to which

they belong.

The benefits of code sharing is that it allow an application to have several different threads of activity are within the same address space.

(1) Economy \Rightarrow Allocating the memory and resources for the process creation is costly. Alternatively because thread share the resources of the process to which they belong, it is more economical to create and the context switch threads.

(2) Utilisation of Multiprocessor Architecture.

The benefits of multithreading can be increase in the multiprocessor architecture, where each thread may be running in parallel on a different processor.

* Types of Thread:

There are two types of thread.

(a) User Level Thread \Rightarrow User threads are supported above the kernel and are implemented by thread library at user kernel.

The library provide support for thread creation, scheduling and management with no support from kernel. Because the kernel is unaware of user level thread, all the thread creation and scheduling done in the user space without the need for kernel intervention. Therefore user level thread are generally fast to create and manage they have drawbacks.

② Kernel Level Thread \Rightarrow These threads are supported strictly by OS. The kernel perform thread creation, scheduling and management in the kernel space area.

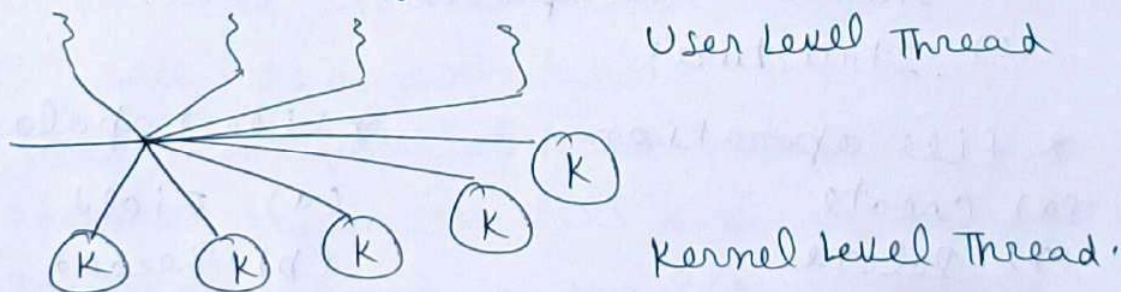
Because thread management is done by the OS. Kernel thread are generally slower to create and manage than the User level thread.

In multi processor environment, the kernel can schedule threads on different processors.

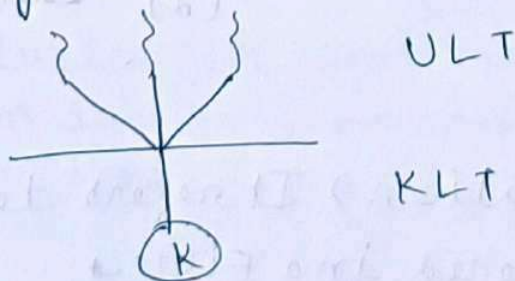
Most OS includes, support kernel threads windows 2000, windows NT.

$\rightarrow \nwarrow \nearrow$ Multi-threading Model \Rightarrow some OS provide a combined approach User level threads and the kernel level threads.

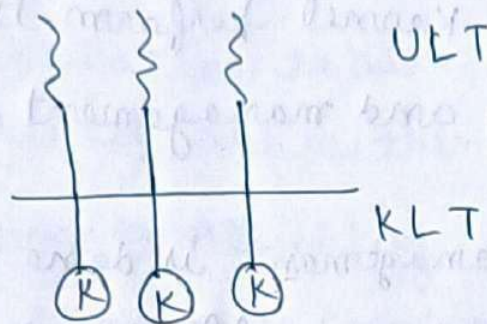
① Many to Many model



② Many to one model



③ One to one Model

question 5(a)

Explain about the Concept of File Concept.

Define in detail about the File organization & access mechanism?

Ans → File Concept ⇒ A file is a named collection of data such as image, video, music, word file, presentation & document etc.

It has following properties such as:

- (a) Long term existence.
- (b) Shareable b/w process.
- (c) Structure.

* file operation

- (a) create
- (b) Delete
- (c) open
- (d) Read
- (e) write
- (f) truncate.

* file topologies.

- (a) Field
- (b) Record
- (c) File
- (d) Database.

* File organization ⇒ It refers to the way the data is stored in a file.

FO is very imp, because it determine the method of access, efficiency, flexibility and storage

device to use.

in DBMS user.

(23)

* Types of File Organization: There are following

types of File organization are:

(a) Sequential File Organization \Rightarrow This is the most straightforward technique of file arrangement. Files are saved in this method in sequential order.

(b) Heap File Organization \Rightarrow It is most fundamental and basic type of organizational structure. It's based on data chunks. The records are inserted at the end of the file in the heap file organization.

(c) Hash File organization \Rightarrow The computation of the hash function on some of the fields of the records is used by Hash File organization.

(d) B+ File organization \Rightarrow The advanced way of an indexed sequential access mechanism is the B+ tree File organization.

(e) Indexed Sequential Access method \Rightarrow It is an advanced sequential file organization method. Records are stored in the file using the primary key in this way.

(f) Cluster File organization \Rightarrow Clusters are created when two or more records are saved in the same file.

* File access mechanism \Rightarrow There are three ways to access a file in computer system

(a) Sequential access \Rightarrow It is the simplest access method. Information in the file is processed in order, one record after the other.

(b) Direct access \Rightarrow It is also known as relative access method. A file of length logical records that allows the program to read & write record rapidly.

(c) Index sequential method \Rightarrow It is the other method of accessing a file that is built on the top of the sequential access method.

These methods construct an index for the file.

* It is built on top of sequential access.

* It controls the pointer by using index.

Q \rightarrow (b) A Hard disk having 2000 cylinders, numbered from 0 to 1999. The drive is currently serving the request at cylinder 143 and the previous request was at cylinder 125. The status of the queue is as follows 86, 1470, 913, 1774, 948, 1509, 1022, 1750, 130.

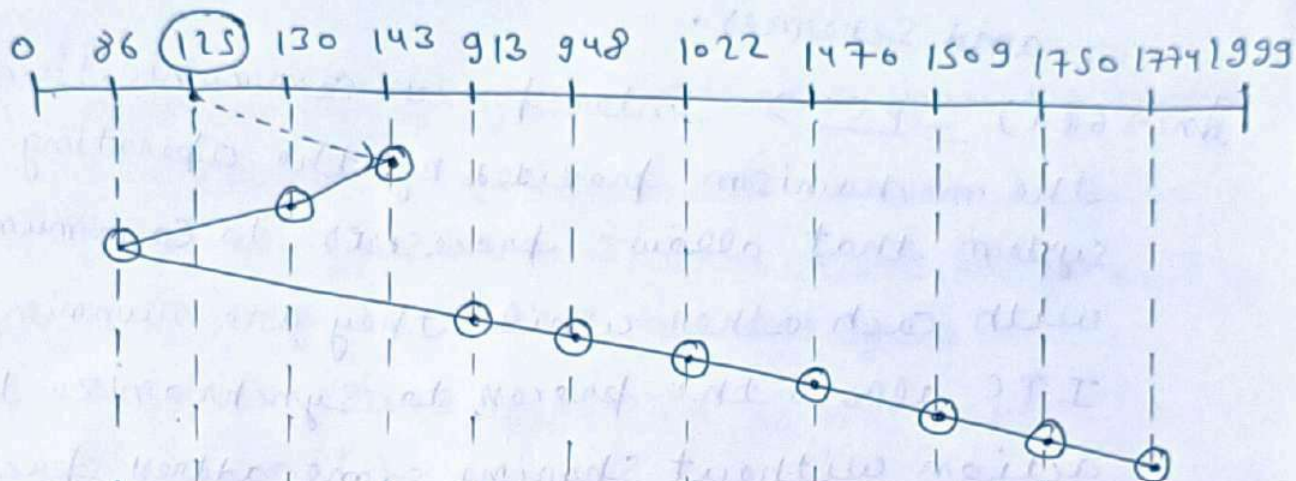
What is the total distance (in cylinders) that the disk arm moves to satisfy the entire pending request for each of the following disk scheduling algorithms?

(i) SSTF

(ii) FCFS

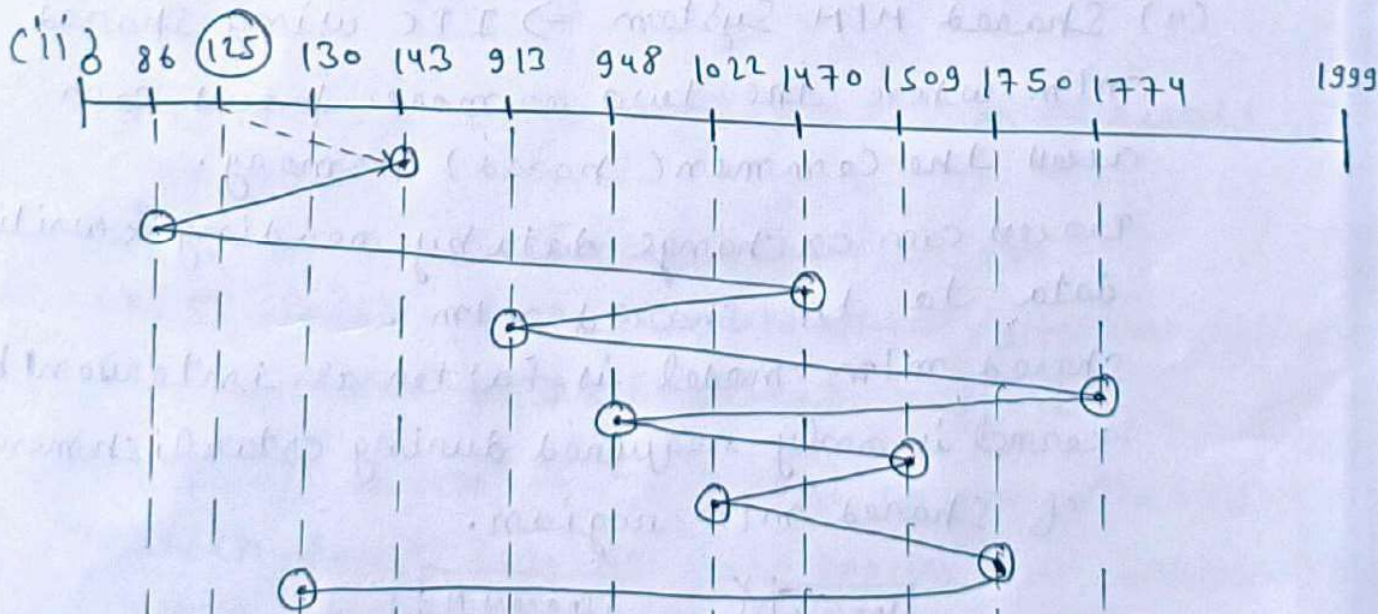
Am $\rightarrow S(b) \Rightarrow$

(i) SSTF



$$\begin{aligned} \text{Total Distance} &= (143 - 86) + (1774 - 86) \\ &= 57 + 1688 \\ &= \boxed{1745} \end{aligned}$$

FCFS



$$\begin{aligned} \text{Total distance} &= (143 - 86) + (1470 - 86) + (1470 - 913) + \\ &+ (1774 - 913) + (1774 - 948) + (1509 - 948) + (1509 - 1022) + \\ &+ (1750 - 1022) + (1750 - 130) = \boxed{7081} \end{aligned}$$

Q → 6(a) Explain in detail about the Inter Process communication model and Schemes.

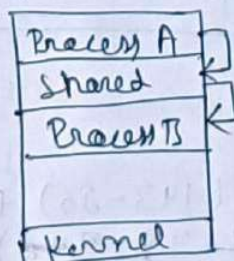
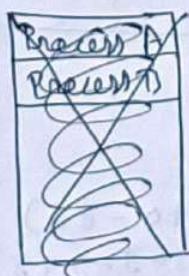
Ans → 6(a) IPC ⇒ Inter process communication is the mechanism provided by the operating system that allows processes to communicate with each other while they are running. IPC allow the process to synchronize their action without sharing same address space. IPC is very ~~useful~~ useful in distributed system where the actual process reside on different computer & connected on a NW.

* Model of IPC ⇒ There are mainly two fundamental model of IPC are :-

(a) Shared M/M System ⇒ IPC using shared m/m where the two or more process can access the common (shared) memory.

Process can exchange data by reading & writing data to the shared m/m.

shared m/m model is faster as intervention by kernel is only required during establishment of shared m/m region.



Ex: (a) Producer Consumer Problem.

(b) Reader writer Problem.

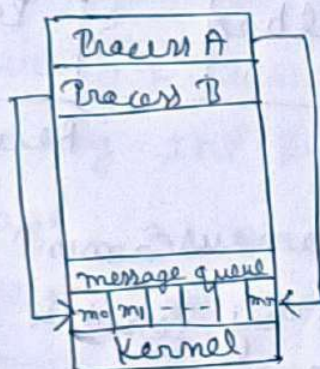
(27)

~~Def~~
① Message Passing System \Rightarrow Message passing provides a mechanism to allow the process to communicate & synchronize their actions without sharing the same address space.

There are two operation of MPS are:

(a) Send()

(b) Receive().



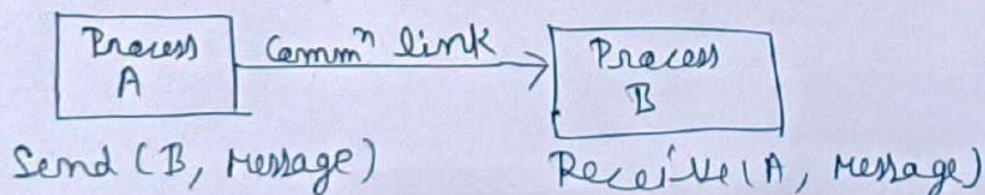
* Method to implement Send() or receive() operation

(a) Direct or Indirect Communication

(b) Synchronous or asynchronous communication.

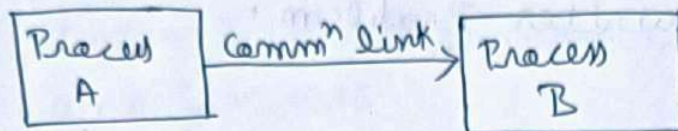
(c) Automatic or Explicit buffering.

(a) Direct Commⁿ \Rightarrow It is the communication which occurs between two processes the processes only need a identity of each other process.



Symmetric Addressing

(28)

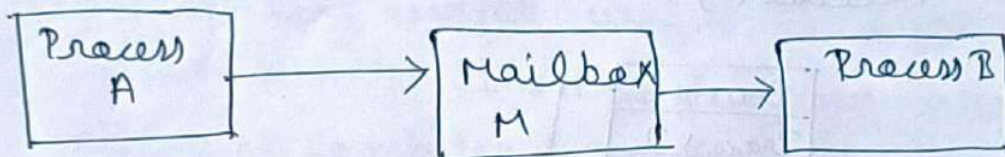


Send (B, Message)

Receive (id, Message)

(Asymmetric Addressing)

* Indirect communication \Rightarrow It is the commⁿ b/w two process through the mailbox, the process only need a unique id of each mailbox.



Send (M, Message)

Receive (M, Message)

(b) Synchronous & Asynchronous commⁿ \Rightarrow commⁿ b/w the process take place by send & receive function.

(i) Blocking Send & Blocking Receive.

(ii) Non-Blocking Send & Blocking Receive.

(iii) Non-Blocking send & Non-Blocking Receive.

(c) Buffering \Rightarrow Buffer is used in direct & indirect communication.

(i) Zero capacity buffer.

(ii) Bounded Capacity buffer.

(iii) Unbounded capacity buffer.

Q → 6(b) Explain in detail about the Monolithic and Microkernel Systems?

Ans → 6(b) Monolithic Kernel ⇒ The monolithic kernel manages the system's resource b/w the system application and the system HW.

Unlike the microkernel User and Kernel services are run in the same address space. It increases the kernel size and also increases the size of OS.

The monolithic kernel offer CPU scheduling, device management, File management, memory management, process management and other OS service by the system calls.

* Advantage → (a) The monolithic kernel runs quickly because of memory management, file management, process scheduling etc.

(b) All the components may interact directly with other and also with the kernel.

* Disadvantage → (a) If the user needs to add a new service the user requires to modify the complete OS.

(b) If any of the service fails, the entire system fails.

(30)

* Microkernel system \Rightarrow Microkernel is a type of kernel that permits the customization of the OS. It is privileged and provides low-level address space management as well as I/O. Furthermore, OS funcⁿ like the VM manager, file system and CPU scheduler are built on top of the microkernel.

* Advantage \Rightarrow (a) These are modular, and several modules may be modified, reloaded, replaced without modifying the kernel.

(b) The architecture of microkernel is small and isolated but it may work better.

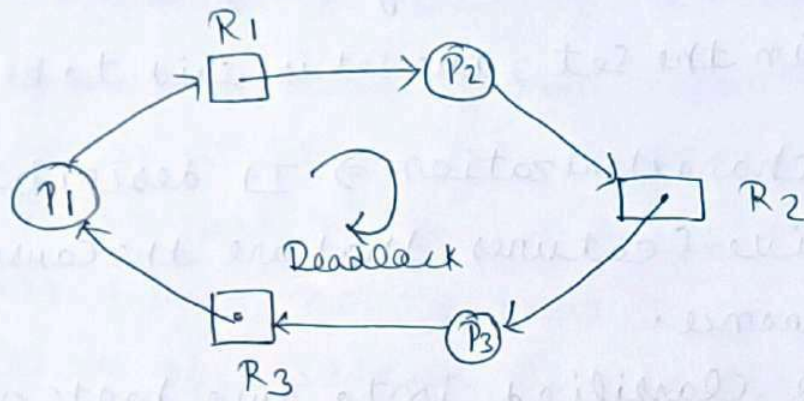
* Disadvantage \rightarrow (a) A context switch is required in the microkernel when the drivers are running processes.

(b) The microkernel system performance might be variable & cause issues.

Q \rightarrow 7(a) Explain in detail about Deadlock system model and Deadlock characterization.

Ans \rightarrow 7(a) Deadlock \Rightarrow It is a situation where a group of processes are permanently blocked as a result each process having acquired a subset of the resources needed for its completion and waiting for release of remaining resource held by other process, so that it can make

Impossible any of the process to be proceed completely. This situation is called Deadlock Condition.



* System Model →

* For the purposes of deadlock discussion, a system can be modeled as a collection of limited resources that can be divided into different categories and allocated to a variety of processes, each with different requirements.

* Memory, printers, CPUs, open files, tape files, CD-ROMs and other resource are examples of resource categories.

* By definition, all resources within a category are equivalent and any of the resources within that category can equally satisfy a request from that category.

* Some categories may only have one resource.

* The kernel keeps track of which resources are free and which are allocated, to which process they are allocated, and a queue of processes waiting for this resource to become available.

for all kernel managed resources. Mutex can be (32)
used to control application managed resources.

* when every process in a set is waiting for a resource that is currently assigned to another process in the set, the set is said to be deadlocked.

* Deadlock characterization \Rightarrow It describes the distinctive features that are the cause of Deadlock occurrence.

It can be classified into two parts are:

(a) Deadlock prerequisites \Rightarrow

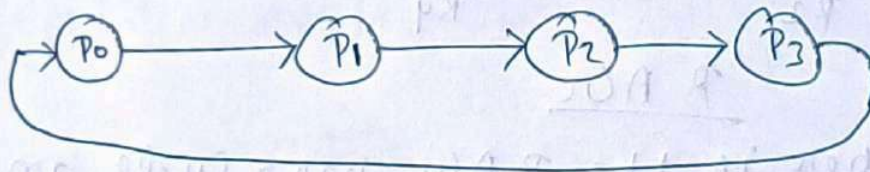
(i) Mutual exclusion \Rightarrow In a multiprogramming environment, there may be several processes requesting the same resource at a time. The mutual exclusion condition, allow only a single process to access the resource at a time. While the other processes requesting the same resource must wait and delay their execution until it has been released.

(ii) Hold & wait \Rightarrow The Hold and wait condition simply means that the process must be holding access to one resource and must be waiting to get hold of other resource that have been acquired by the other processes.

(iii) No preemption \Rightarrow A process acquiring a resource, can not be preempted in b/w, to release the acquired resource. Instead, the process must

Voluntarily release the resource it has acquired when the task of the process has been completed.

(iv) Circular wait \Rightarrow A condition is arise in which P_0 process is waiting for resource from P_1 , P_1 process is waiting for resource from P_2 , P_2 process is waiting for resource from P_3 and so on. the last P_n process is waiting for resource from P_0 . So that It can create a condition of deadlock due to Circular wait -



* System Resource Allocation \Rightarrow The system reallocation graph is a directed graph that briefs you about the deadlock more precisely. when a process requests for a resource it denoted by the request edge in the RAG. In the graph resources are denoted by the Rectangle and process are denoted by circle.

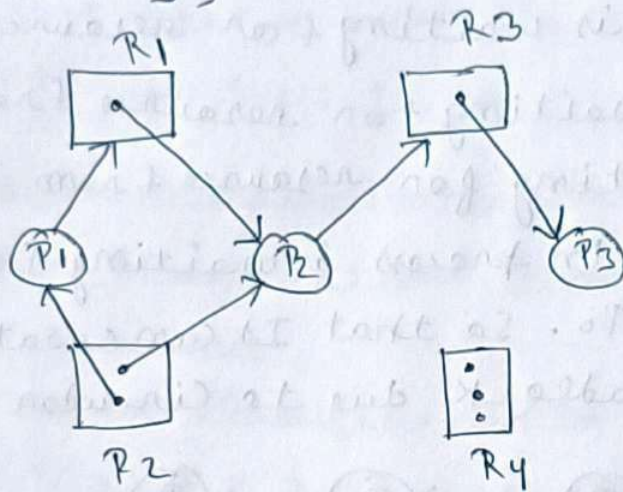
If a resource has multiple instances then it is denoted by the dots inside the rectangle.

To understand deadlock we let us take an example. Consider we have following set of nodes and edges.

1. There are three active processes $P = \{P_1, P_2, P_3\}$
2. There are four resources $R = \{R_1, R_2, R_3, R_4\}$

3. The set of request edge and assignment edges (34)
we have

$$E = \{ P_1 \rightarrow R_1, P_2 \rightarrow R_3, R_1 \rightarrow P_2, R_2 \rightarrow P_1, R_2 \rightarrow P_3, R_3 \rightarrow P_3 \}$$



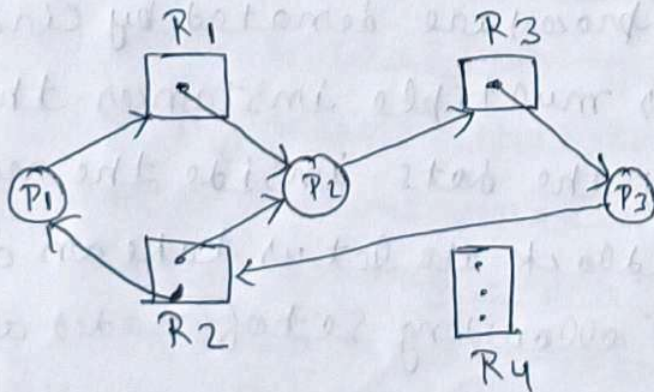
RAG

Remember if the RAG has a cycle and every resource has a single instance then it implies that a deadlock has occurred.

* In this case, you will observe that there are two cycles in the RAG.

$$1. P_1 \rightarrow R_1 \rightarrow P_2 \rightarrow R_3 \rightarrow P_3 \rightarrow R_2 \rightarrow P_1$$

$$2. P_2 \rightarrow R_3 \rightarrow P_3 \rightarrow R_2 \rightarrow P_2$$



RAG \Rightarrow Deadlock

Q → 7(b) Illustrate the following Page replacement algorithm

(i) FIFO

(ii) Optimal Page Replacement.

Use the reference string 7, 0, 1, 2, 0, 3, 0, 4, 2, 3, 0, 3, 2, 1, 2, 0, 1, 7, 0, 1, 0, 3, 2, 1, 2, 0, 1, 7, 0, 1 for a m/m with three frames.

Ans → 7(b) → (i) FIFO

7	0	1	2	0	3	0	4	2	3	0	3	2	1	2	0	1	7	0	1
7*	7	7	2*	2*	2	2	4*	4	4	0*	0*	0*	0	0	0	0	7*	7	7
0*	0	0	0	3*	3	3	2*	2	2	2	2	1*	1	1	1	1	0*	0	0
1*	1	1	1	1	0*	0	0	0	3*	3	3	3	3	2*	2*	2*	2	2	1*
			H							H	H			H	H				

No. of hits → 5 , Hit ratio = $5/20 = \underline{0.25}$

No. of miss → 15 , Miss ratio = $15/20 = \underline{0.75}$

(ii) optimal Page Replacement,

7	0	1	2	0	3	0	4	2	3	0	3	2	1	2	0	1	7	0	1
7	7	7	2	2	2	2	2	2	2	2	2	2	2	2	2	2	7	7	7
0	0	0	0	0	0	0	4	4	4	0	0	0	0	0	0	0	0	0	0
1	1	1	1	1	3	3	3	3	3	3	3	3	1	1	1	1	1	1	1
			H		H		H	H		H	H		H	H	H		H	H	

No. of hits = 11 , Hit ratio = $11/20 = \underline{0.55}$

No. of miss = 9 , Miss ratio = $9/20 = \underline{0.45}$