



PA0 (SPI0_MISO)/MCDB0	B10	PA0 (SPI0_MISO)/[MCDB0]	
PA1 (SPI0_MOSI)/MCDB1	B11	PA1 (SPI0_MOSI)/[MCDB1]	
PA2 (SPI0_SPCK)	P19	PA2 (SPI0_SPCK)	
PA3	T2	PA3 (SPI0_NPCS0)/[MCDB3]	
PA4	R15	PA4 (RTS2)/[MCDB2]	
PA5	R12	PA5 (CTS2)/[MCDB1]	
PA6	T10	PA6 (MCDA0)	
MDIVTR (PA7)	P13	PA7 (MCDA0)	
PA8	T11	PA8 (MCKC)	
PA9	P14	PA9 (MCKA)	
ETX2 (PA10)	R13	PA10 [MCDA2]/[ETX2]	
ETX3 (PA11)	T12	PA11 [MCDA3]/[ETX3]	
ETX0 (PA12)	U9	PA12 [ETX0]	
ETX1 (PA13)	U10	PA13 [ETX1]	
ERX0 (PA14)	U11	PA14 [ERX0]	
ERX1 (PA15)	U12	PA15 [ERX1]	
ETXEN (PA16)	U13	PA16 [ETXEN]	
ERXDV (PA17)	U14	PA17 [ERXDV]	
ERXER (PA18)	U16	PA18 [ERXER]	
ETXCK (PA19)	U13	PA19 [ETXCK]	
EMDC (PA20)	T14	PA20 [EMDC]	
EMDIO (PA21)	R14	PA21 [EMDIO]	
ETXER (PA22)	T16	PA22 [ADTRG]/[ETXER]	
PA23	R15	PA23 [TWD]/[ETX2]	
PA24	R16	PA24 [TCKB]/[ETX3]	
ERX2 (PA25)	P16	PA25 [TCLCK]/[ERX2]	
ERX3 (PA26)	P17	PA26 [TIOA0]/[ERX3]	
ERXCLK (PA27)	T17	PA27 [TIOA1]/[ERXCK]	
ECRS (PA28)	L14	PA28 [TIOA2]/[ECRS]	
ECOL (PA29)	R17	PA29 [SCK1]/[ECOL]	
PA30	N15	PA30 [SCK2]/[RXD4]	
PA31	N14	PA31 [SCK0]/[TXD4]	
PB0	N16	PB0 [SPI1_MISO]/[TIOA3]	
PB1	M14	PB1 [SPI1_MOSI]/[TIOB3]	
PB2	M15	PB2 [SPI1_SPCK]/[TIOA4]	
PB3	M16	PB3 [SPI1_NPCS0]/[TIOA5]	
PB4	K14	PB4 [TXD0]	
PB5	P17	PB5 [RXD0]	
PB6	N17	PB6 [TXD1]/[TCLCK1]	
PB7	R10	PB7 [RXD1]/[TCLCK2]	
PB8	L16	PB8 [TXD2]	
PB9	L15	PB9 [RXD2]	
PB10	T5	PB10 [TXD3]/[ISL_D8]	
PB11	P5	PB11 [RXD3]/[ISL_D9]	
PB12	K5	PB12 [TXD5]/[ISL_D10]	
PB13	P6	PB13 [RXD5]/[ISL_D11]	
PB14	L17	PB14 [DRXD]	
PB15	K17	PB15 [DTXD]	
PB16	J15	PB16 [TXD0]/[TCLCK3]	
PB17	T10	PB17 [TXD0]/[TCLCK4]	
PB18	H17	PB18 [TD0]/[TIOB4]	
PB19	J15	PB19 [TXD0]/[TIOB5]	
PB20	U5	PB20 [RK0]/[ISL_D0]	
PB21	U6	PB21 [RF0]/[ISL_D1]	
PB22	T6	PB22 [DSR0]/[ISL_D2]	
PB23	R6	PB23 [DCD0]/[ISL_D3]	
PB24	P7	PB24 [DTR0]/[ISL_D4]	
PB25	U7	PB25 [R0]/[ISL_D5]	
PB26	R8	PB26 [RTS0]/[ISL_D6]	
PB27	U8	PB27 [CTS0]/[ISL_D7]	
PB28	U9	PB28 [RTS1]/[ISL_PCK]	
PB29	T8	PB29 [CTS1]/[ISL_VSYNC]	
PB30	P9	PB30 [PCK0]/[ISL_HSYNC]	
PB31	P10	PB31 [PCK1]/[ISL_MCK]	
PC0	T3	PC0 [SCK3]/[AD0]	
PC1	T4	PC1 [PCK0]/[AD1]	
PC2	U1	PC2 [I/PCK1]	
PC3	U4	PC3 [I/SPH_NPCS3]	
PC4	A17	PC4 [A23]/[SPI_NPCS2]	
PC5	A15	PC5 [A24]/[SPI_NPCS1]	
PC6	A16	PC6 [TIOB2]/[CFCE1]	
PC7	B11	PC7 [TIOB1]/[CFCE2]	
PC8	B16	PC8 [NC54/CFCS0]/[RTS3]	
PC9	C11	PC9 [NC55/CFCS1]/[TIOB0]	
PC10	B12	PC10 [A25/CFRN]/[CTS3]	
PC11 (SPI0_NPCS1)	R14	PC11 [NC52]/[SPI_NPCS1]	
PC12	C12	PC12 [RQ0]/[NC57]	
NANDRB (PC13)	B13	PC13 [RQ]/[NC56]	
NANDCS (PC14)	B15	PC14 [NC53/ANDCS]/[I/RO2]	
PC15	G1	PC15 [NKATU/UR0]	

	J14 TDO	J16 TDI	J17 TDI	H15 TRK	H16 NRST	H6 RTCK	F16 JTACSEL		G15 NRST		F15 BMS		B17 WKUP	C17 SHDN
	TDO	TDI	TDI	TMS	TCK	NRST	RTCK	JTACSEL					WKUP	SHDN
OSCSEL	PA0 [SPI0_MISO]/[MCDB0]													D0
	PA1 [SPI0_MOSI]/[MCDB1]													D1
	PA2 [SPI0_SPCK]													D2
	PA3 [SPI0_NPCS0]/[MCDB3]													D3
	PA4 [RTS2]/[MCDB2]													D4
	PA5 [CTS2]/[MCDB1]													D5
	PA6 [MCDA0]													D6
	PA7 [MCDA1]													D7
	PA8 [MCKC]													D8
	PA9 [MCDA1]													D9
	PA10 [MCDA2]/[ETX2]													D10
	PA11 [MCDA3]/[ETX3]													D11
	PA12 [ETX0]													D12
	PA13 [ETX1]													D13
	PA14 [ERX0]													D14
	PA15 [ERX1]													D15
	PA16 [ETXEN]											PC16 [D16]/[SPI0_NPCS2]		D16
	PA17 [ERXDV]											PC17 [D17]/[SPI0_NPCS3]		D17
	PA18 [ERXER]											PC18 [D18]/[SPI1_NPCS1]		D18
	PA19 [ETXCK]											PC19 [D19]/[SPI1_NPCS2]		D19
	PA20 [EMDC]											PC20 [D20]/[SPI1_NPCS3]		D20
	PA21 [EMDIO]											PC21 [D21]/[EF100]		D21
	PA22 [ADTRG]/[ETXER]											PC22 [D22]/[TCLKF5]		D22
	PA23 [TWD]/[ETX2]											PC23 [D23]		D23
	PA24 [TWCK]/[ETX3]											PC24 [D24]		D24
	PA25 [TCLK0]/[ERX2]											PC25 [D25]		D25
	PA26 [TIOA0]/[ERX3]											PC26 [D26]		D26
	PA27 [TKO1]/[JERXCK]											PC27 [D27]		D27
	PA28 [TIOA2]/[ECRS]											PC28 [D28]		D28
	PA29 [SCK1]/[ECOL]											PC29 [D29]		D29
	PA30 [SCK2]/[RXD4]											PC30 [D30]		D30
	PA31 [SCK0]/[TXD4]											PC31 [D31]		D31
	XIN	PB0 [SPI1_MISO]/[TIOA3]												
PB1 [SPI1_MOSI]/[TIOB3]														NWR2/NBS2/A1
PB2 [SPI1_SPCK]/[TIOA4]														A2
PB3 [SPI1_NPCS0]/[TIOA5]														A3
PB4 [TXD0]														A4
PB5 [RXD0]														A5
PB6 [TXD1]/[TCLK1]														A6
PB7 [RXD1]/[TCLK2]														A7
PB8 [TXD2]														A8
PB9 [RXD2]														A9
PB10 [TXD3]/[ISL D8]														A10
PB11 [RXD3]/[ISL D9]														A11
PB12 [TXD5]/[ISL D10]														A12
PB13 [RXD5]/[ISL D11]														A13
PB14 [DRXD]														A14
PB15 [DTXD]														A15
PB16 [TK0]/[TCLK3]														SDA10
PB17 [TF0]/[TCLK4]														SDCK
PB18 [TXD]/[TIOB4]														SDWE
PB19 [RXD]/[TIOB5]														SDCKE
PB20 [RX0]/[ISL D0]														SDCS/NC51
PB21 [RF0]/[ISL D1]													CAS	
PB22 [DSR0]/[ISL D2]													RAS	
PB23 [DCD0]/[ISL D3]													CFIOW/NBS3/NWR3	
PB24 [DTR0]/[ISL D4]													CFIOR/NBS1/NWR1	
PB25 [RD0]/[ISL D5]													NANDOE	
PB26 [RTS0]/[ISL D6]													NANDWE	
PB27 [CTS0]/[ISL D7]													CFOR/NRD	
PB28 [RTS1]/[ISL D8]													CFWE/NFE/NWR5	
PB29 [CTS1]/[ISL VSYNCS]													NC50	
PB30 [PCK0]/[ISL HSYNCS]														
PB31 [PCK1]/[ISL MCK]														
XOUT	PC0 [SCK3]/[AD0]													
	PC1 [PCK0]/[AD1]													
	PC2 [/]/[PCK1]													
	PC3 [/]/[SPI1_NPCS3]													
	PC4 [A3]/[SPI1_NPCS2]													
	PC5 [A34]/[SPI1_NPCS1]													
	PC6 [TIOB2]/[CFCE1]													
	PC7 [TIOB1]/[CFCE2]													
	PC8 [NC54/CFCS0]/[RTS3]													HDMA
	PC9 [NC55/CFCS1]/[TIOB0]													HDPA
XOUT32	PC10 [A25/CFRNW]/[CTS3]													
	PC11 [NC52]/[SPI0_NPCS1]													HDMB
	PC12 [IRQ0]/[NC57]													HDPB
	PC13 [FIO]/[NC56]													
	PC14 [NC53/NANDCS]/[IRQ2]													DDM
	PC15 [NWAIF]/[IRQ1]													DDP
XIN32														
PLRCA														
TST														
NC														

U1A  
AT91SAM9260B-CU  
or AT91SAM9G20-CU

PC16 [D16]/[SPI0\_NPCS2]  
PC17 [D17]/[SPI0\_NPCS3]  
PC18 [D18]/[SPI1\_NPCS1]  
PC19 [D19]/[SPI1\_NPCS2]  
PC20 [D20]/[SPI1\_NPCS3]  
PC21 [D21]/[EF100]  
PC22 [D22]/[TCLK5]  
PC23 [D23]  
PC24 [D24]  
PC25 [D25]  
PC26 [D26]  
PC27 [D27]  
PC28 [D28]  
PC29 [D29]  
PC30 [D30]  
PC31 [D31]

NBS0/A0  
NWR2/NBS2/A1  
A2  
A3  
A4  
A5  
A6  
A7  
A8  
A9  
A10  
A11  
A12  
A13  
A14  
A15  
BA0/A16  
BA1/A17  
A18  
A19  
A20  
A21  
A22

SDA10  
SDCK  
SDWE  
SDCKE  
SDCS/NCS1  
CAS  
RAS  
CFIOW/NBS3/NWR3  
CFIOR/NBS1/NWR1  
NANDOE  
NANDWE  
CFOE/NRD  
CFWE/NWE/NWR0  
NCS0

CFIOW/NBS3/NWR3  
CFIOR/NBS1/NWR1  
NANDOE  
NANDWE  
CFOE/NRD  
CFWE/NWE/NWR0  
NCS0

HDMA	HDPA
HDMB	HDPB
DDM	DDP

A10	B4	SDA10
DCK	B1	SDCK
DWE	F2	SDWE
CKE	G3	SDCKE
CAS	B3	SDCS NCS1
CAS1	C3	CAS
CAS2	D3	RAS
NWR3	A1	CFHOW NBS3 NWR3
NWR1	B2	CFHOR NBS1 NWR1
DWE	N0	NADWE
NWR0	N10	NANDWE
NWR0	A13	CFOE NRD
CS0	A12	CFWE NWE NWR0
	A14	NC50
HDMA	E15	HDMA
HDPA	E14	HDPA
HDMB	C14	HDMB
HDPB	D14	HDPB
DDM	D13	DDM
DDP	C13	DDP







