Tutorial - 6

Floating point numbers & Operations

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Sol contd.....

So, it makes a GP series, with f bits maximum number possible is sum of GP series.

Maximum value with f bits possible

=
$$\frac{1}{2}$$
 + $\frac{1}{4}$ + $\frac{1}{8}$ + $\frac{1}{16}$ + $\frac{1}{32}$ += $\frac{1}{2}$ [(1-(1/2))] = 1-2-f

So, maximum fractional value possible

= max value with i bits + max value with f bits

=
$$2^{i}$$
-1 +1 -2-f = 2^{i} -2-f = so, required range will be 0 to 2^{i} - 2^{-f} .

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The floating point representation has three parts:

- 1. Sign of Mantissa represent the sign of number{0- negative,1- positive},
- 2. Exponent represent both positive and negative exponent bias is added to the actual exponent in order to get stored, exponent.
- 3. Normalised Mantissa- represent the digits with only one "1" to the left of the decimal. 4. The value of the Normalized number is (-1)s x 1.ME-127

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Sol. Contd...

Precision can be represented by 1.M, So M+1 is used to represent the precise number 23 bits + 1 = 24 bits

According to the formula:

Base Xno of digits = Base Yno of digits

 \Rightarrow 2²⁴=10^y \Rightarrow y=24 log₁₀2 = 7.22.

Hence, the precision in terms of the decimal digits is 7

Q

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Sol:

a)
$$S = 1$$

Fraction = $01000...00_2$ Exponent = $10000001_2 = 129$
 $x = (-1)^{1} \times (1_{10} + 01_2) \times 2^{(129 - 127)}$
 $= (-1) \times 1.25 \times 2^{2} = -5.0$

Q.

- a) What number is represented by the single-precision float 11000000101000...00
- b) Represent -0.75 in single and double precision format.

Sol Contd.....

$$-0.75 = (-1)^1 \times 0.11_2 = (-1)^1 \times 1.1_2 \times 2^{-1} \square \square$$
 in binary

S = 1

Mantissa(Fraction) = 1000...00₂ Exponent = -1 + Bias

Single: $-1 + 127 = 126 = 011111110_2$ (e=E-127 => E=e + 127)

Double: -1 + 1023 = 1022 = 011111111110₂ Single: 10111111101000...00

Double: 10111111111101000...00

Perform the following operations and check the result for errors due to rounding.

- a)Add 9.78 x 10²⁵ and 8.79 x 10²⁴ assuming 3 digit mantissa .
- b)Add 9.76 x 10²⁵ and 2.59 x 10²⁴ assuming 3 digit mantissa.

- Perform the following operations and check the result for errors due to rounding.
- a)Add 9.78 x 10²⁵ and 8.79 x 10²⁴ assuming 4 digit mantissa.

Sol. Add 9.78×10^{25} and 8.79×10^{24} (without rounding)

- Shift mantissa of the smaller number to the right: 0.879×10^{25}
- Add mantissas: 10.65 x 1025
 - Normalize mantissa if necessary: 1.065 x 10²⁶.
- 0 11010101 0001000011011110110100
- -Add 9.78 x 10²⁵ and 8.79 x 10²⁴ (with rounding)
- Shift mantissa of the smaller number to the right: 0.879×10^{25}
- Add mantissas (note extra digit on the left): 10.6590×10^{25}
- Check and normalize mantissa if necessary: 1.065×10^{26}
 - 0 11010101
 - Round the result: 1.07 x 10²⁶.
 - 0 11010101 000010000101010101010101

Difference due to rounding -1.07 - 1.065= 0.005.

Sol:

- Perform the following operations and check the result for errors due to rounding.
- b)Add 9.76 x 10²⁵ and 2.59 x 10²⁴ assuming 3 digit mantissa.

- Add 9.76 x $10^{25}\,and$ 2.59 x 10^{24} (without rounding)
- Shift mantissa of the smaller number to the right: 0.259 x 10²⁵
- Add mantissas: 10.01x 1025
- Check and normalize mantissa if necessary: 1.00x 10²⁶.
- Add 9.76 x 10^{25} and 2.59 x 10^{24} (with rounding)
- Internal registers have extra two digits: 9.7600 x 1025 and 2.5900 x 1024
- Shift mantissa of the smaller number to the right: 0.2590×10^{25}
 - Add mantissas: 10.0190 x 1025
 - Check and normalize mantissa if necessary: 1.00 x 10²⁶
 - Round the result: 1.00 x 10²⁶.
 - Difference due to rounding: 0

Perform the following operations and check the result for errors due to rounding.

b)Add 9.76 x 10²⁵ and 2.59 x 10²⁴ assuming 3 digit mantissa.

Sol:

- Add 9.76 x 10²⁵ and 2.59 x 10²⁴ (without rounding)
- Shift mantissa of the smaller number to the right: 0.259×10^{25}
- Add mantissas: 10.01x 10²⁵
- Check and normalize mantissa if necessary: 1.00x 10²⁶
- $Add 9.76 \times 10^{25}$ and 2.59×10^{24} (with rounding)
- Internal registers have extra two digits: 9.7600 x 1025 and 2.5900 x 1024
- Shift mantissa of the smaller number to the right: 0.2590 x 10²⁵
 - Add mantissas: 10,0190 x 1025
 - Check and normalize mantissa if necessary: 1.00 x 1026
 - Round the result: 1.00 x 10²⁶.

Difference due to rounding: 0

As per the IEEE 754 floating point representation,

the standard default rounding mode is rounding-to-nearest.

Here, values are rounded to the closest representable number and results that lie exactly halfway between two representable numbers are rounded such that the least significant digit of their result is even.

Given the binary numbers in the table, round these numbers to nearest ¼ and fill the rest of the entries as well.

| Binary | Rounded Binary | Action(round ed up or down) | Rounded Value (decimal) |
|----------|-------------------|-----------------------------------|----------------------------|
| 10.00011 | 7000 | | |
| 10.00110 | | | |
| 10.11100 | | | |
| 10.10100 | | | |

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Given the binary numbers in the table, round these numbers to nearest ¼ and fill the rest of the entries as well.

| Binary | Rounded Binary | Action (rounded up or down) | Rounded Value (Decimal) |
|----------|-------------------|-----------------------------------|-------------------------------|
| 10.00011 | 10.00 | Rounded down(1/2) | 2 |
| 10.00110 | 10.01 | Rounded up (1/2) | 2 1/4 |
| 10.11100 | 11.00 | Rounded up (1/2) | 3 |
| 10.10100 | 10.10 | Rounded down (1/2) | 2 1/2 |