Start Load HL pair with Address 2250 Move content from Memory to (Increment HL pair) Subtract memory content to A' content. Increment HL paix END Flow chart for subtraction of 2 Heradecimal Expt. No. 02

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	50-treminages			
	Sim: To perform subtraction of 2-resadecimal numbers			
	Apparatus Required: 8085 microprocessor trainer kit.			
	Program:			
	First press i and then 'A'			
	RAM ADR 2200			
	MOV A,M			
	INX H			
	SUB M			
1	INX H MOV M, A			
	RST S			
2209	END			
	Now again, Fixst press '1' and then 'A'			
	RAM ADR 2250			
2250	DB 3F 2C END			
22.52				
	For incution: G2200\$			
	612200\$			
	Teacher's Signature:			

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	Fox display:	Verification:	
	M 2250		
2250		C/P->(3F), -> 0011 1111	
2251	(24)16	(1P > (2C), -> 0010 1100	
2252	(13)16	$O/P \rightarrow (13)_{16}$ 0001 0011	
		3	
	Rough Des Store 1st	t (13), has been obtained	
	and have	e resultied.	
	WWW VOVII	a de la gran	
	Description of Intractions Used:		
_0	LXI H 0-2>50: 3 lute instruction which loads		
	The HL register pa	or with the 16-bit immediate	
	tall value 7250	Н	
(2)	MOV A.M: Mous	The data from the memory	
13	Incaled philade	ty the 16-bit HL pair ley 1.	
19	SUB M: sultant	to the 8-bit data stored in the	
	memore located -	by pointed by the ML pair from	
	the accumulator (A).		
(3)	RST S: restart		
6	END: telly the assembler that the program		
	has ended and me	further instructions exist.	
(7)	6 2200 \$: Jus	telle the nuiroprocessor to go	
	1.10/11	00 and procule the program	
8	Starting from 2 M2250: Memo	ry display at 2250, to display	
		Teacher's Signature :	

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The contents of m	emory lor 2250.
Conclusion:	
Hence the program of results were were	was written and the fied for subtraction of their by the 8085 microprocesso.
2 randortival van	tills try the 8085 milroprocesso.
	Teacher's Signature :

Date\_\_\_\_\_