

PC 3242 Part II

<u>Topic</u>	<u>Text Book (Zhen Cui '05)</u>	<u>Lectures</u>
<u>Optical lithography</u>	Chapter 2	1, 2 & 3
<u>Electron Beam Lithography</u>	Chapter 3	4 & 5
<u>Focused Ion Beam Technology</u>	Chapter 4	
Low Energetic Ions (keV)		6, 7 & 8
SIMS	Extra material provided	
FIB in Lithography	Chapter 4	
High Energetic Ions (MeV)	Extra material provided	8
RBS	Extra material provided	9
Light ions in lithography	Extra material provided	10
<u>Etching</u>	Chapter 7	10, 11
<u>Nano Imprint Lithography</u>	Chapter 6	12
<u>3DP Three Dimensional Printing</u>	Extra material provided	13

Degree of Anisotropy

r_{lat} : lateral etch rate

r_{ver} : vertical etch rate

A_f : degree of anisotropy

$$A_f \equiv 1 - \frac{r_{\text{lat}}}{r_{\text{ver}}}$$

$$0 \leq A_f \leq 1$$

Isotropic

Anisotropic

Isotropic Etching

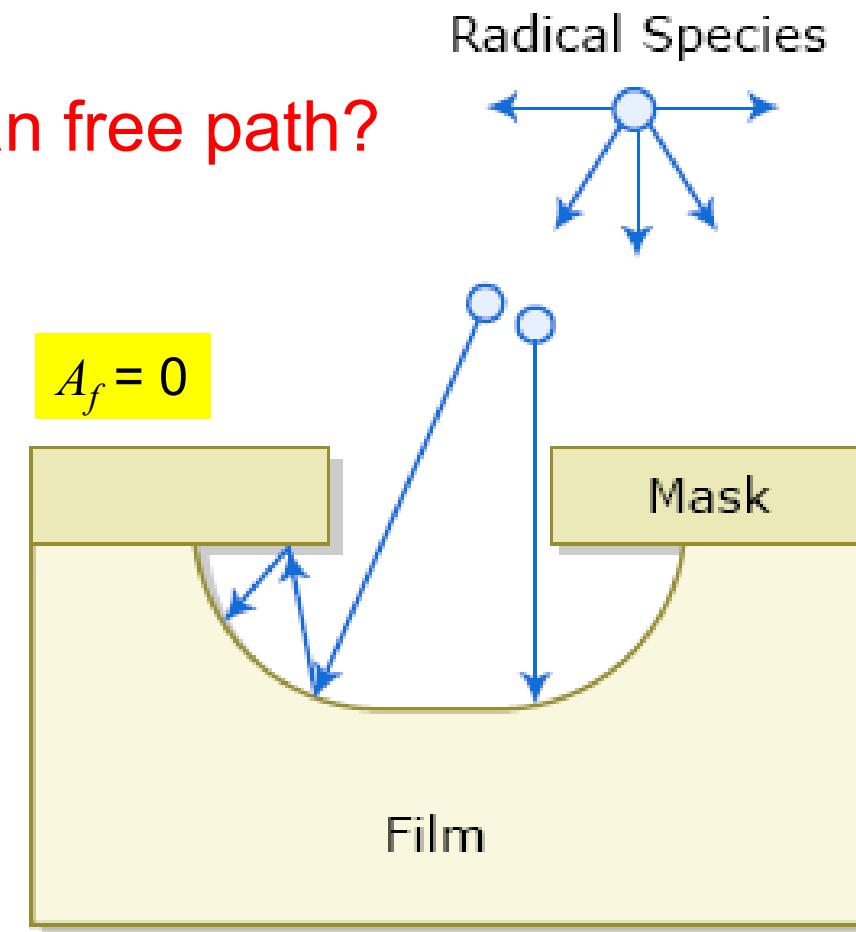
Isotropic etching was used first. Here the etch rate is the same in all directions: Depth and lateral

Wafer is in contact with liquid, reactive gas or plasma.

If you use gas what is the mean free path?

Etching is the result of a chemical reaction and is therefore:

- 1 Isotropic
- 2 Highly selective
- 3 Rate is thermally activated

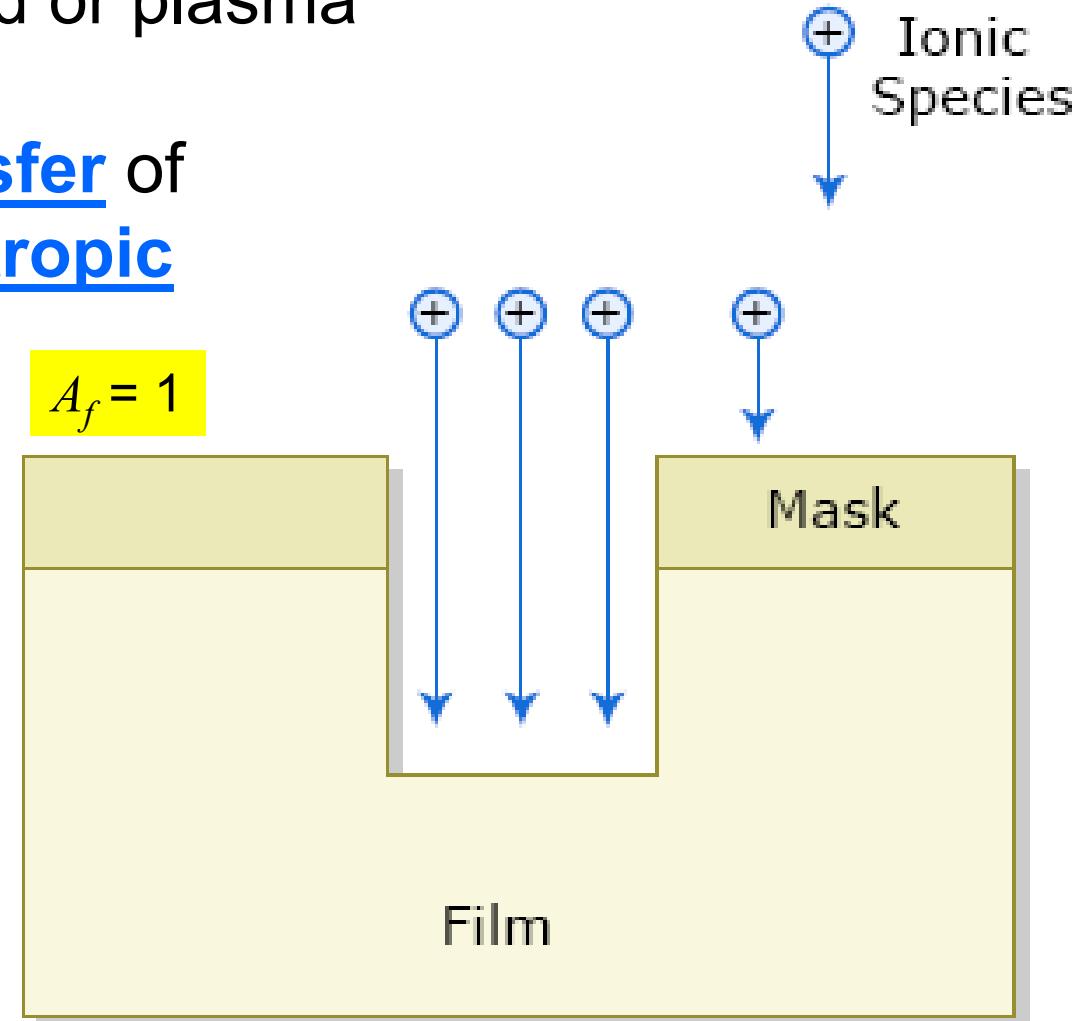


Physical Etching (anisotropic)

Wafer exposed to inert gas and or plasma

Etch is from momentum transfer of accelerated ions acting anisotropic

- 1 Good edge definition
- 2 Anisotropic
- 3 Low selectivity
- 4 Rate is mass transport dependent (by products)



Most preferred is dry anisotropic etching since the structures don't shrink
But wet etching is very commonly used in industry (Cheap).

Etching Selectivity S

$$S_{AB} = \frac{r_A \text{ (vertical etching velocity of intended material A)}}{r_B \text{ (vertical etching velocity of mask B)}}$$



Chemical reactions are highly selective (20-50)

S is controlled by:

Chemicals, concentration, temperature

Physical etch processes less so (1-5)

RIE (reactive ion etching)

S is controlled by:

Plasma parameters, plasma chemistry, gas pressure, flow rate & temperature.

Etching Selectivity S

Directionality: From Isotropic to Anisotropic



Wet etch (Chemical)	Dry etch (Physical)
------------------------	------------------------

Selectivity	25 - 50	1 - 5
--------------------	---------	-------

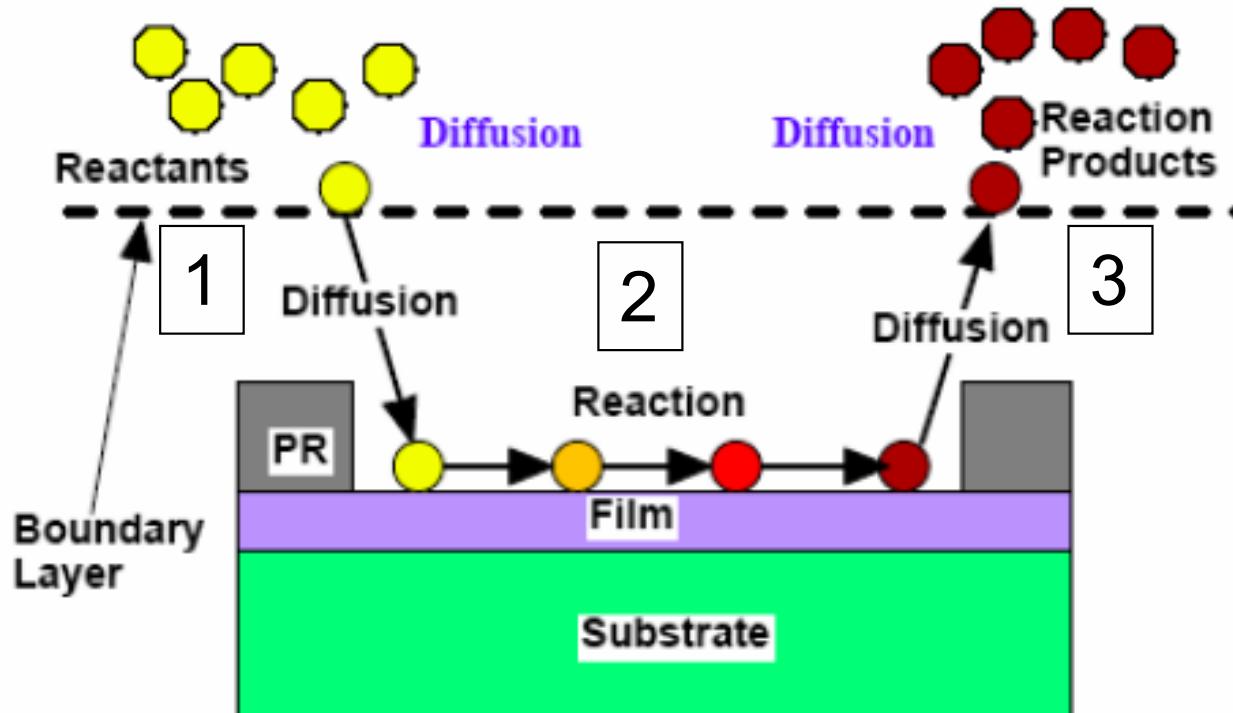
<i>Directionality</i>	low	high
------------------------------	-----	------

Etch Process - Figures of Merit

- Etch rate fast enough to be practical, slow enough to be controllable.
- Etch rate uniformity across wafer
- Selectivity rate of etching target material relative to mask-etch rate (should be large)
- Anisotropy directional dependence of etch rate
- By products volatile or otherwise easily removed,
Are they safe?

Wet Etching

Wafer in solution that attacks film to be etched, but not mask



Advantages:

High selectivity due to chemical reactions

Disadvantages:

- Isotropic (except for Si)
- Poor process control
- Strong T-dependence
- Excessive particulates

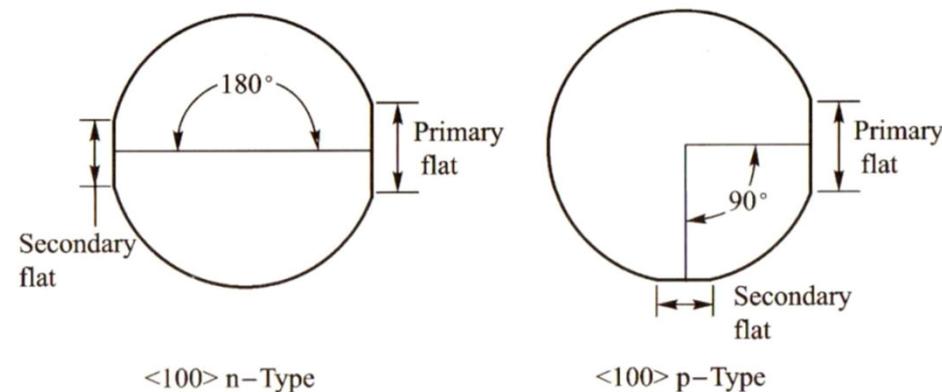
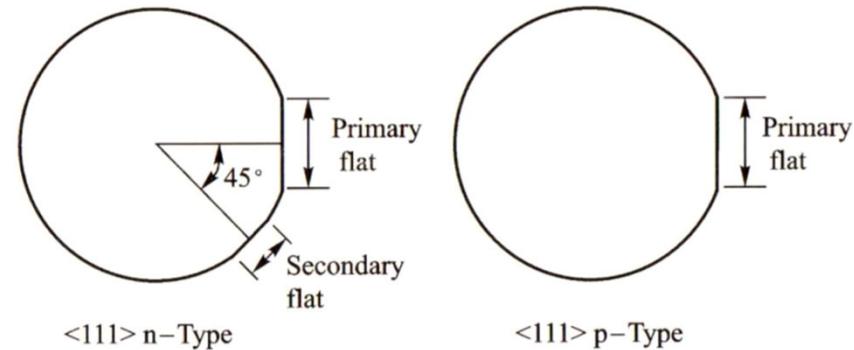
- 1 Reactant diffuse through boundary layer to surface
- 2 Thermally activated, selective reaction at the surface of etchant with the film to be etched gives soluble species
- 3 Diffusion of by-products through boundary layer away from surface

Wet Bulk Surface Micromachining

Wet etching is mainly isotropic. The anisotropic etching of Si by alkaline (eg KOH) is an exception.

In wet bulk micromachining features are sculptured in bulk materials like Si, SiO₂, sapphire, ceramics, SiC, GaAs, InP and Ge by **isotropic** or **anisotropic** wet etchants.

Good etch masks are Si₃N₄ (silicon nitride deposited by low pressure- or plasma enhanced Chemical vapour deposition) 40 nm is typically enough. Au and Cr are also acceptable.

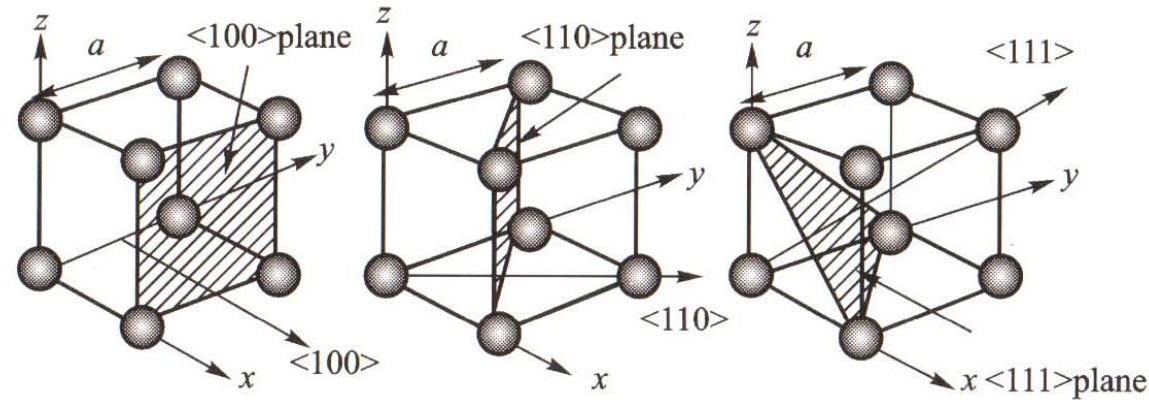


Standard markings can help to align within 0.01 degree!

Si₃N₄ has 0.1 nm/min etch rate in KOH →
Si has 1 um/min etch rate
Selectivity ?
→ 10,000

Importance of Orientation

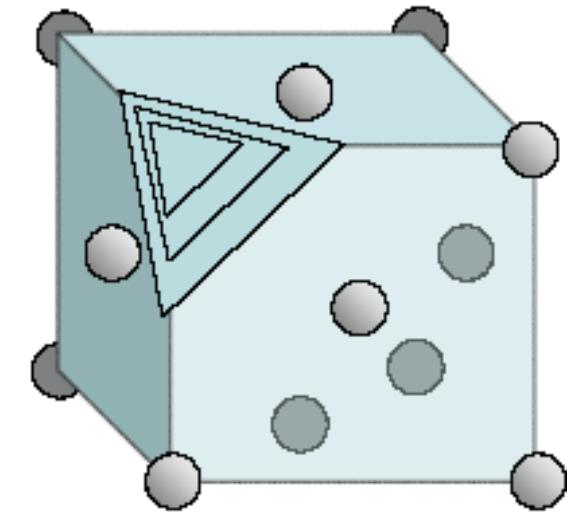
Orientation is important because of different characteristics and etching speeds in different planes and resulting etching angles.



In Si the etch rate depends on crystallography

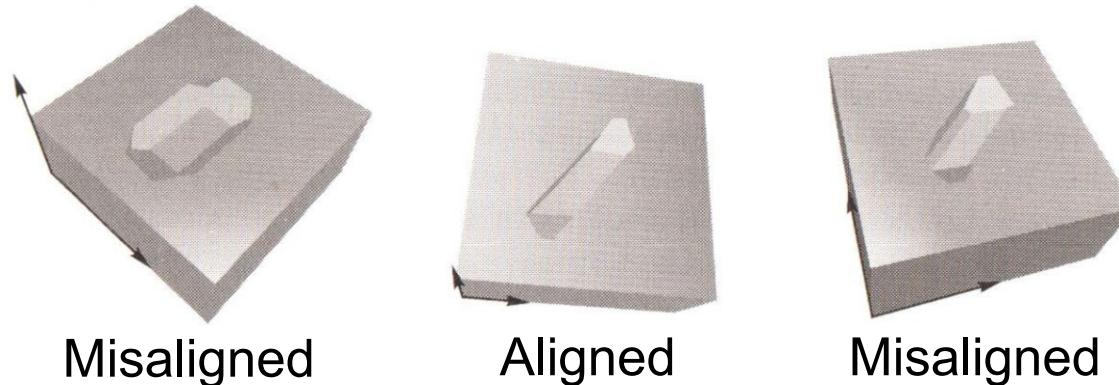
Fastest normal to low-in-plane-bond direction
 $<110>$ then $<100>$

Slowest normal to high-in-plane-bond direction
 $<111>$



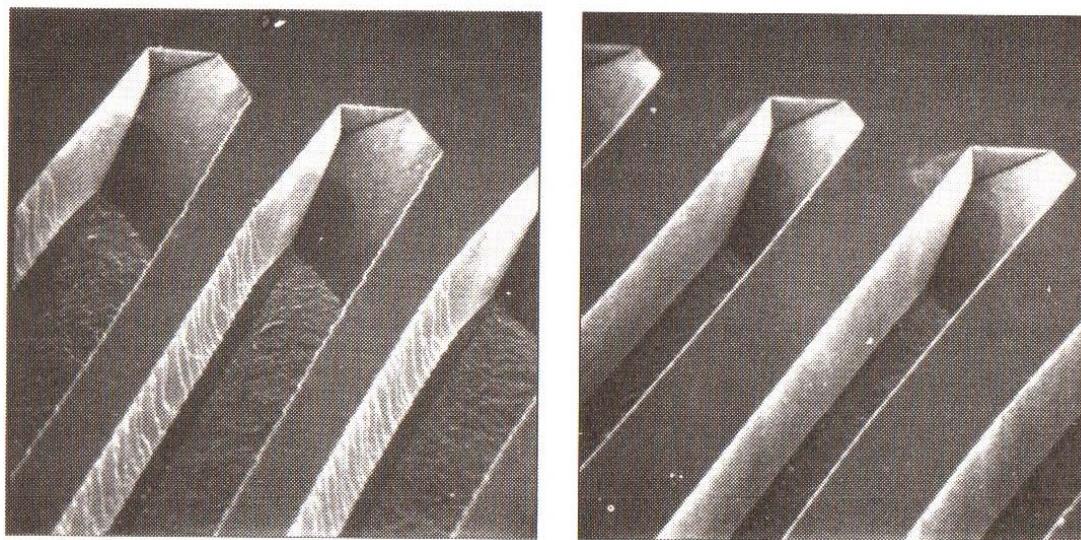
Si planes $<100>$ $<110>$ $<111>$
 High atomic planar density \equiv low etch rate

Importance of Orientation



Effect of mask misalignment
with the Si crystal axis

Computer simulations are good to get an idea how to etch and get desired final structures.



Misaligned Aligned
Experimental results showing
misaligned and aligned KOH etching

We need alignment of the etch mask with respect to the crystal planes.
Otherwise, we don't follow the etch mask but the crystal planes.

→ Rough edges

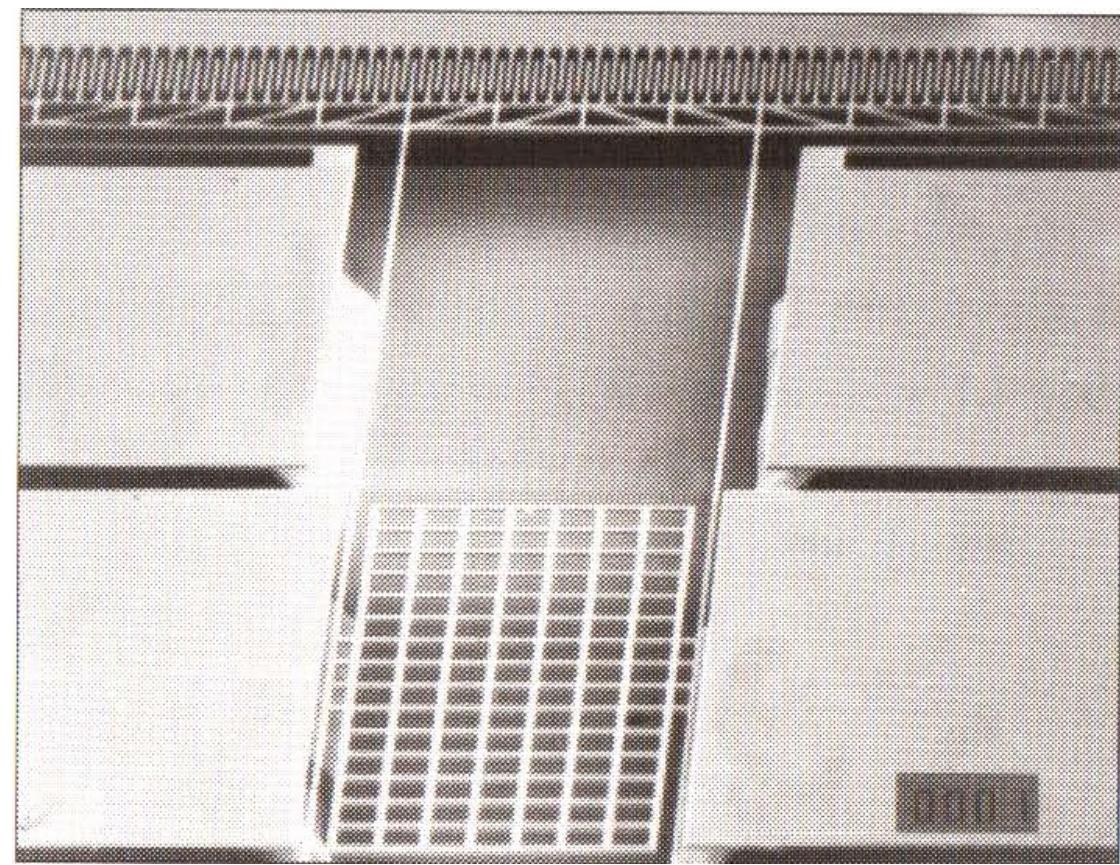
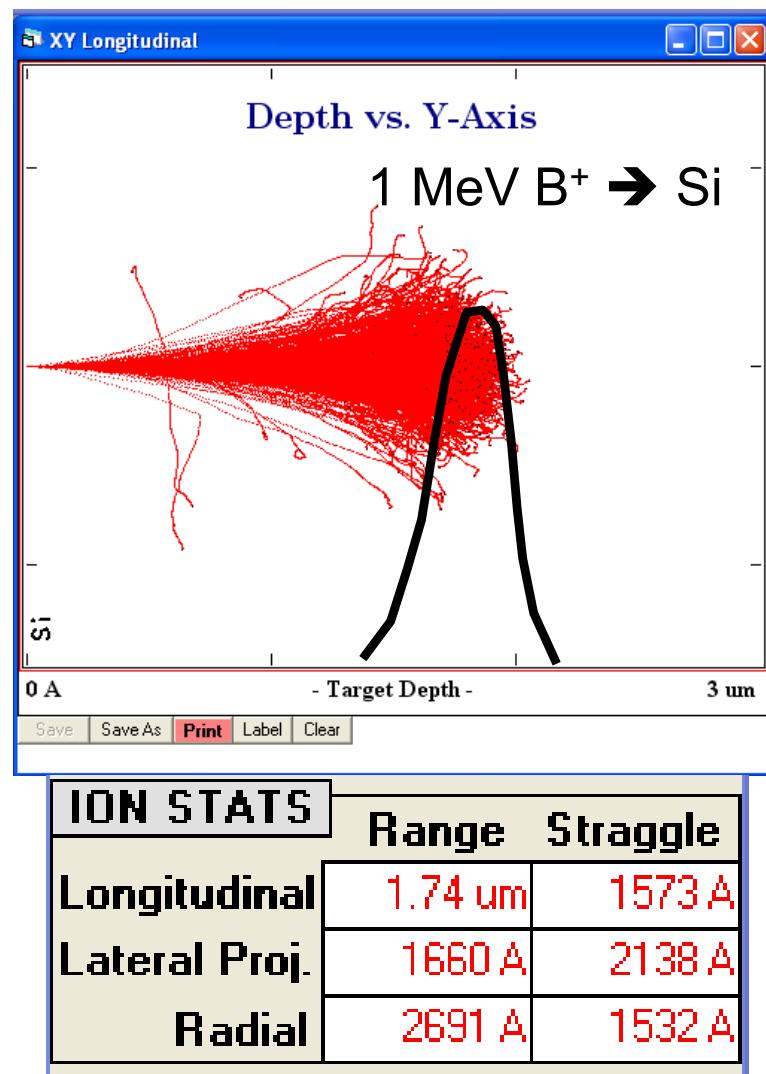
Controlled Etching

- **Si** etches preferentially along preferred crystallographic directions (commonly used in MEMS fabrication)
 - Si shows a reduction of etch rate in heavily doped p-type regions
- Etch control: Boron typically is incorporated using ion implantation or diffusion for this purpose

B as etch stop

Si etch rate in EDP (Ethylenediamine Pyrocatechol) depends strongly on B⁺ concentration in the Si

→ The Si etch rate will drop 140 times if we have a B concentration of ~ $10^{20}/\text{cm}^3$. This can be achieved either through implanting or diffusion of B⁺ in Si



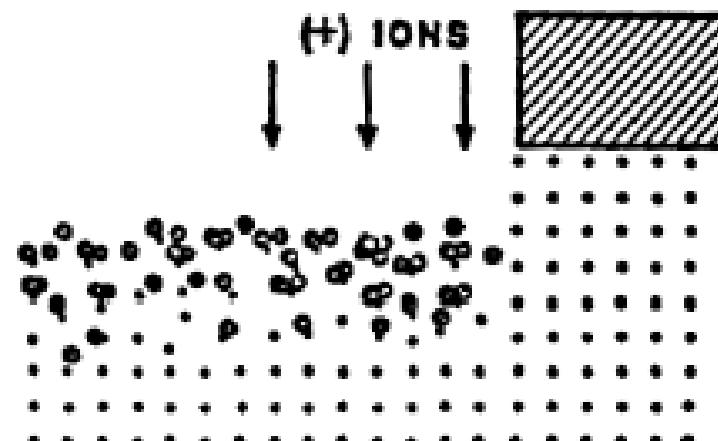
Electrostatic micro-actuator. Here the Si is under etched using EDP. The Si was implanted with B⁺ to form an etch stop layer

Bosch Process or DRIE

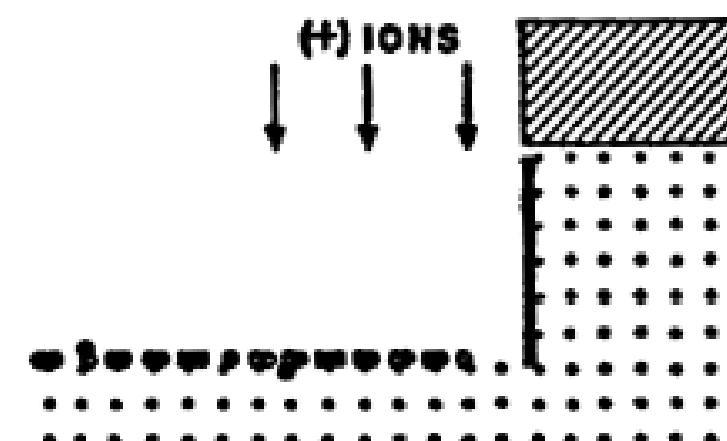
How to Control Anisotropy ?

- 1) ionic bombardment to damage expose surface.
- 2) sidewall coating by inhibitor prevents sidewall etching.

**SURFACE DAMAGE INDUCED
ANISOTROPY**



**SURFACE INHIBITOR
MECHANISM OF ANISOTROPY**



(○) ETCHANT

(●) SUBSTRATE ATOM

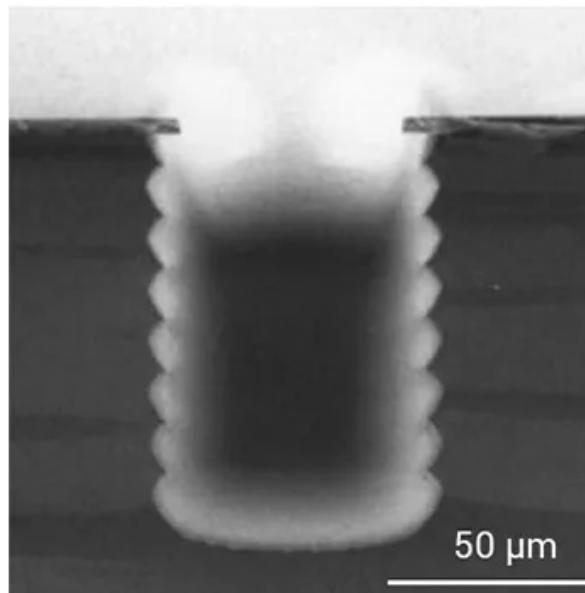
— INHIBITOR

Bosch Process or DRIE

Silicon Deep Reactive Ion Etching (DRIE) for Etching Si and SiO₂

The Bosch process is a deep silicon etch which was developed and patented by Robert Bosch GmbH.

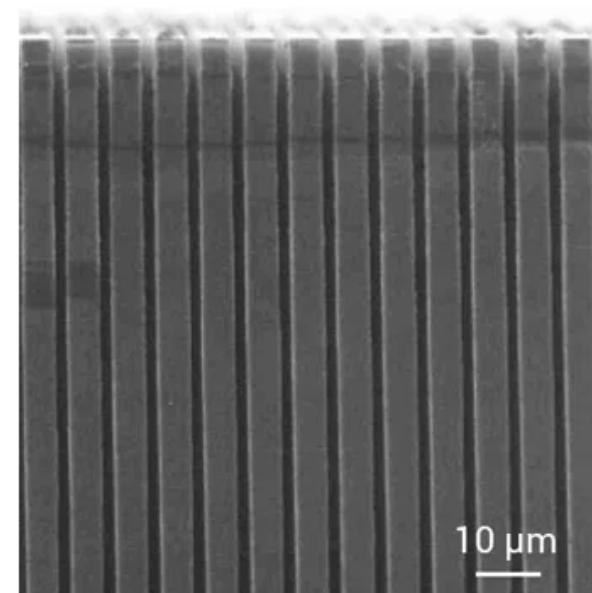
- Two step plasma process which **alternates between deposition** and etching
- The result is an anisotropic etch that can have a vertical sidewall regardless of the orientation of the silicon crystal.



High-rate Si deep etching

Etch Rate : 55 μm/min

Pattern Width : 50 μm



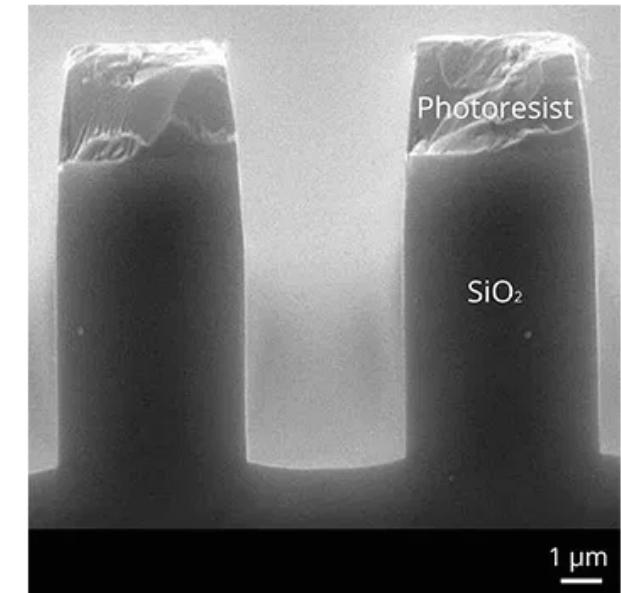
Si etching with high aspect ratio

Aspect Ratio : 40

Pattern Width : 2.5 μm

Etch Depth : 100 μm

Scallop Size : less than 100 nm



Anisotropic SiO₂ etching

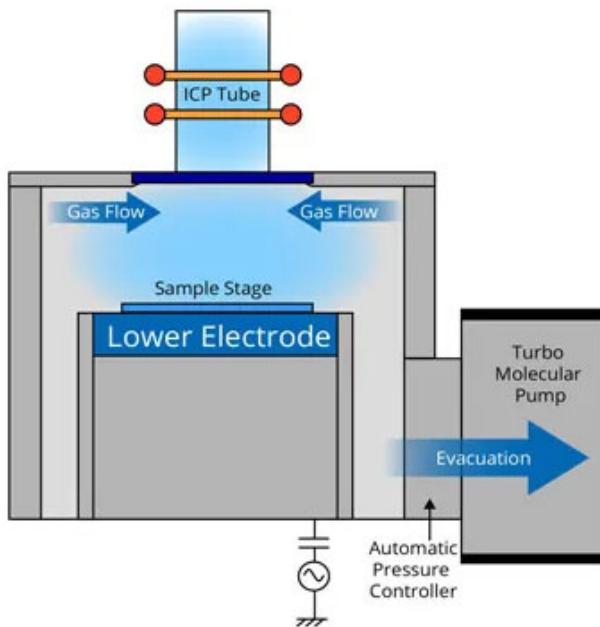
4 μm width L/S pattern

Etch Depth : 6.5 μm

Etch Rate : 325 nm/min

Bosch Process or DRIE

High-density ICP plasma source



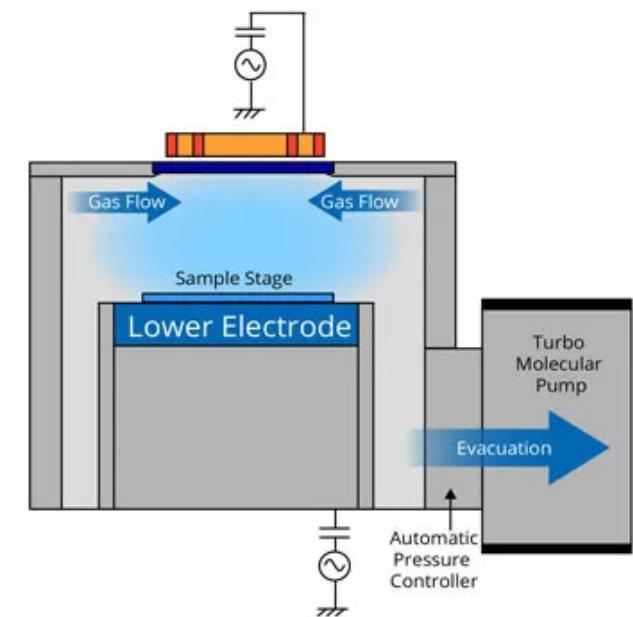
High power ICP source generates a repeatable high density plasma and enables high-rate Si etching of max. $55 \mu\text{m}/\text{min}$.

High-speed Gas Switching



Fast gas switching ($\sim 0.1 \text{ sec}$) allows smooth sidewalls with less scallops.

Optional SiO₂ Etch Kit



Optional ICP coil enables high-speed SiO₂ etching.

Process consists of the cyclic isotropic etching; Inductively Coupled Plasma (ICP) and fluorocarbon-based protection film deposition by quick gas switching

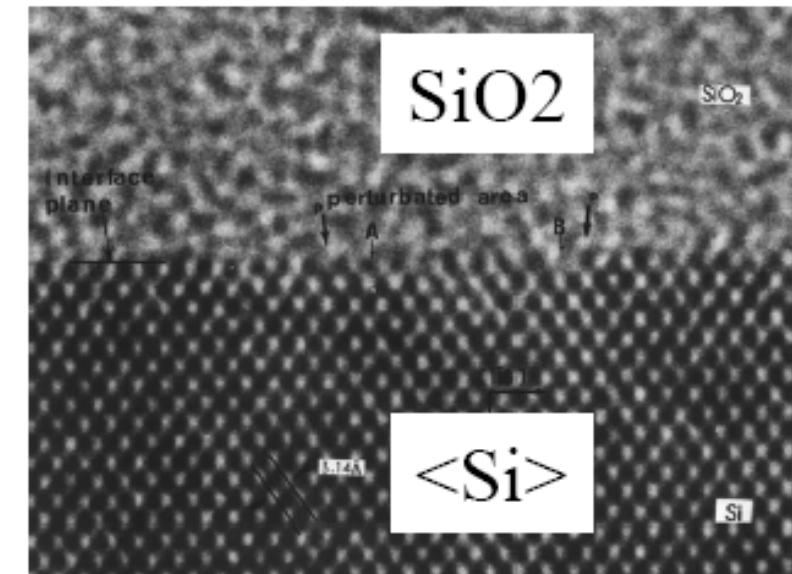
Properties of SiO₂

Thermal SiO₂ is **amorphous**.

Weight Density = 2.20 gm/cm³

Molecular Density = 2.3E22 molecules/cm³

Crystalline SiO₂ [Quartz] = 2.65 gm/cm³



(1) Excellent Electrical Insulator

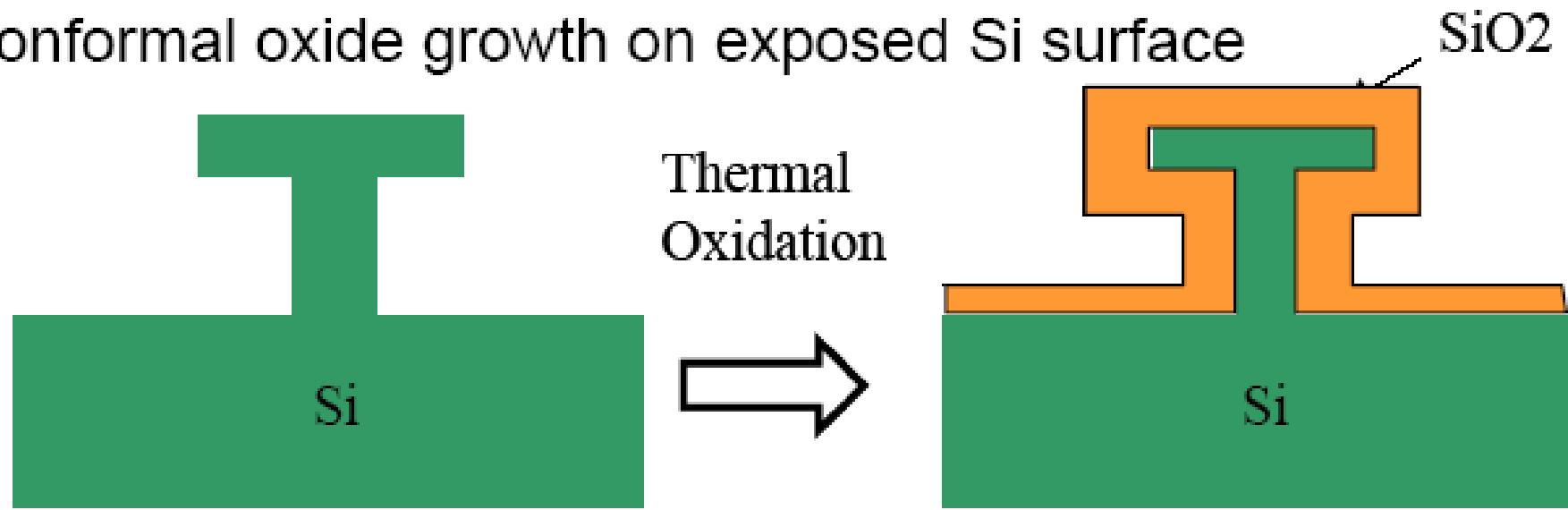
Resistivity > 1E20 ohm-cm Energy Gap ~ 9 eV

(2) High Breakdown Electric Field
> 10MV/cm

(3) Stable and Reproducible Si/SiO₂ Interface

Properties of SiO_2 (cont'd)

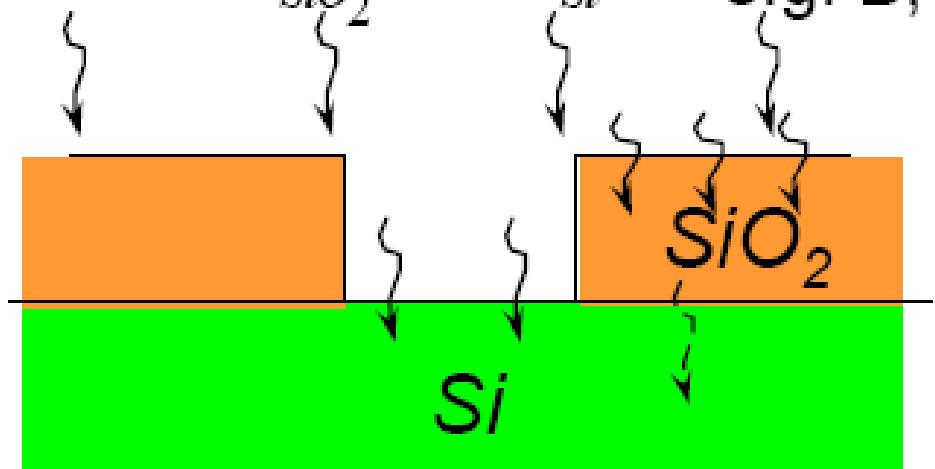
(4) Conformal oxide growth on exposed Si surface



(5) SiO_2 is a good diffusion mask for common dopants

$$D_{SiO_2} \ll D_{Si}$$

e.g. B, P, As, Sb.



*exceptions are Ga
(a p-type dopant) and some
metals, e.g. Cu, Au

Thermal Oxidation Equipment



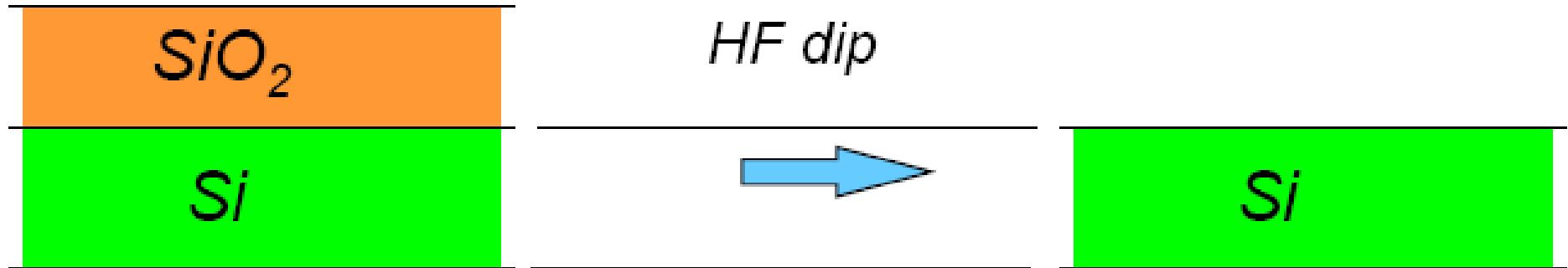
Horizontal Furnace



Vertical Furnace

Properties of SiO₂

SiO₂ has very good etching selectivity compared to Si



SiO₂ also called **oxide** is a popular material in IC industry

Controlled Etching

Anisotropic etchants

- Potassium hydroxide (**KOH**) Etches Si,
- Ethylene diaminepyrochatechol (**EDP**)
- Tetramethyl ammonium hydroxide (**TMAH**)
- Etch silicon preferentially along preferred crystallographic directions (commonly used in MEMS fabrication)
- Shows a reduction of etch rate in heavily doped p-type regions
- Etch control: Boron typically is incorporated using ion implantation or diffusion for this purpose

Applications of SiO₂

Sacrificial layer

- Movable structures
- Fluidics lab on a chip

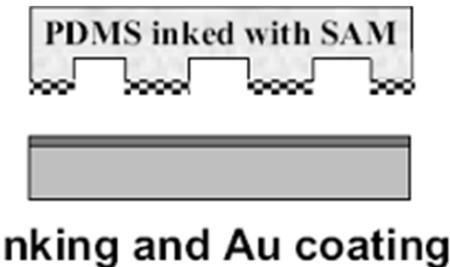
Pyrex glass has expansion coefficient close to Si and can be bonded nicely to form closed fluidic channels.

Structure with photo resist (thin layers) Au or Cr for deeper etched structures, etchant buffered HF (9% of HCl) can improve smoothness.

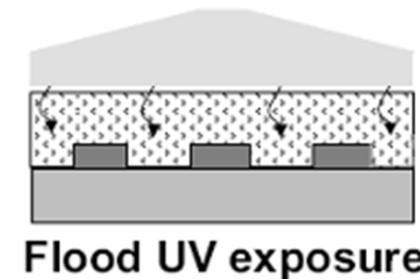
SiO₂ is removed using wet isotropic etching. → HF (49%) has an etch rate of 1.8um/min

There are different forms of Nano imprint lithography (NIL)

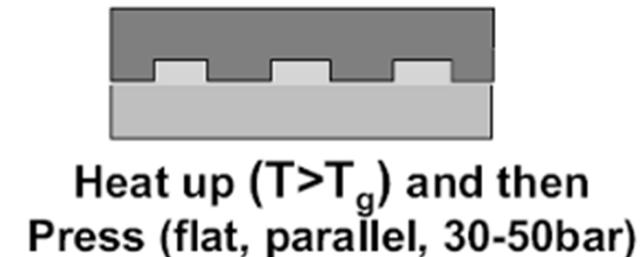
I Soft Lithography
or
 μ -contact printing



II SFIL Step and Flash nanoimprint lithography



III NIL (some people call this Hot embossing)

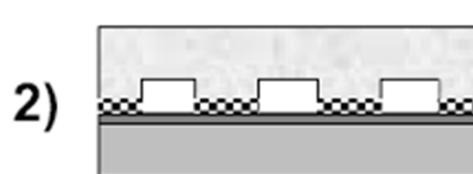


μ CP (Whitesides) Soft stamp

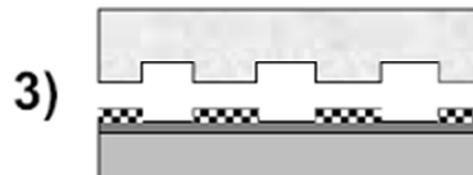
Whitesides et al. Appl. Phys. Lett. 63 2002(1993)



1) Inking and Au coating



2) Transfer Ink from stamp to Au by soft contact



3) Delamination



4) Ashing, etching and pattern transfer

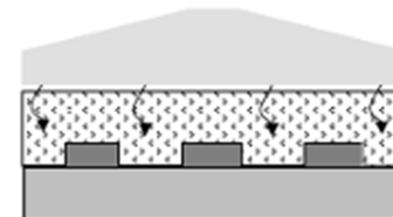
SFIL (Wilson)

Transparent stamp

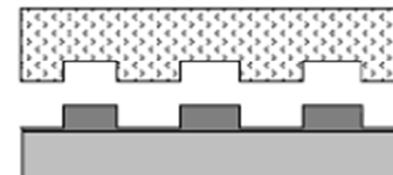
Wilson et al. SPIE, 3676, 379-389(1999)



Dispense UV curable monomer



2) Flood UV exposure



3) Delamination

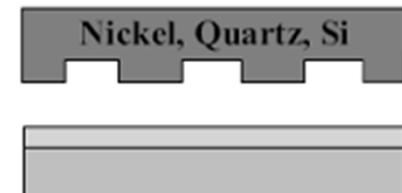


4) Ashing, etching and pattern transfer

NIL (Chou)

Hard stamp

Chou et al. Appl. Phys. Lett. 67 3114(1995)



spin-coated or bulk imprintable polymer



2) Heat up ($T > T_g$) and then
Press (flat, parallel, 30-50bar)



3) Delamination

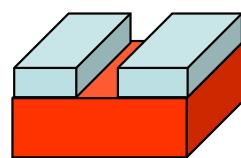


4) Ashing, etching and pattern transfer

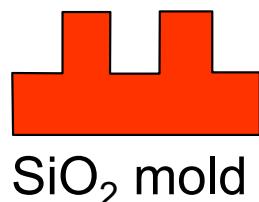
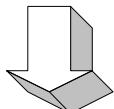
Stamp fabrication for NIL

(nano imprint lithography)

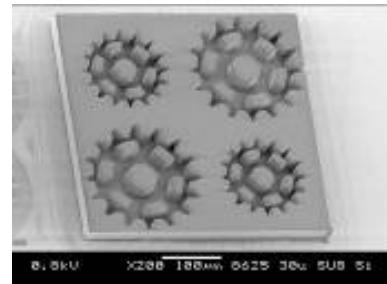
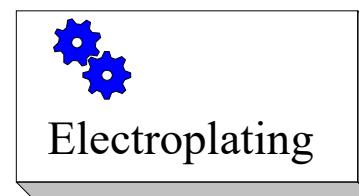
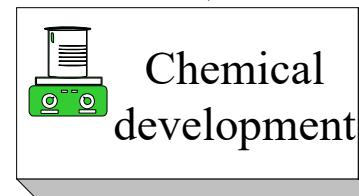
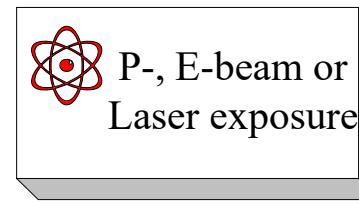
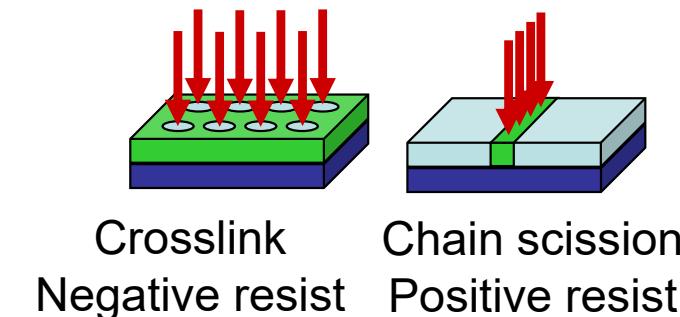
Resist / Hard mask
 Ni
 Seed layer
 SiO₂



Resist or
hard mask



SiO₂ mold



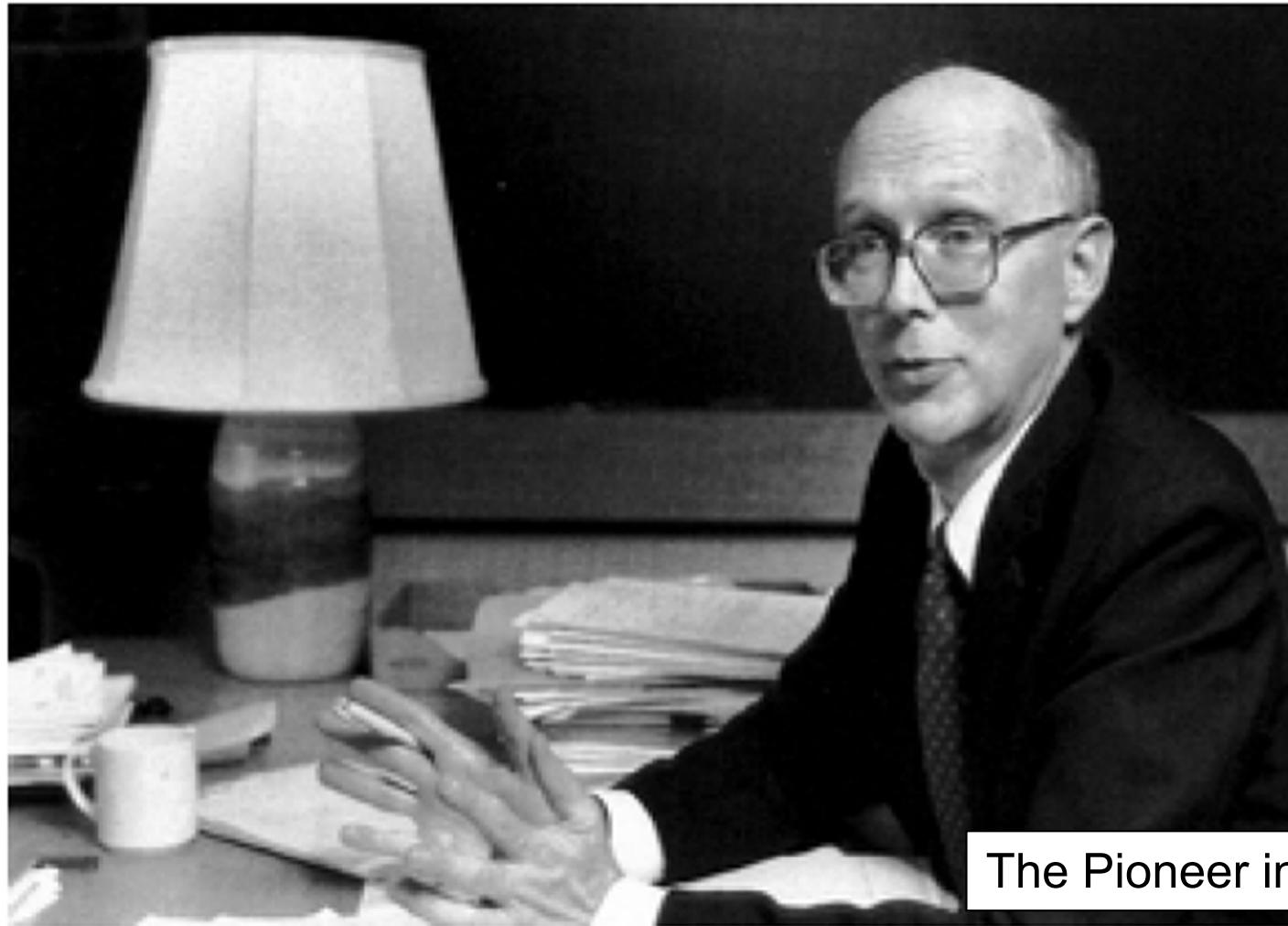
SU-8 mold for
plating



Ni stamp

| Soft Lithography µ-contact printing

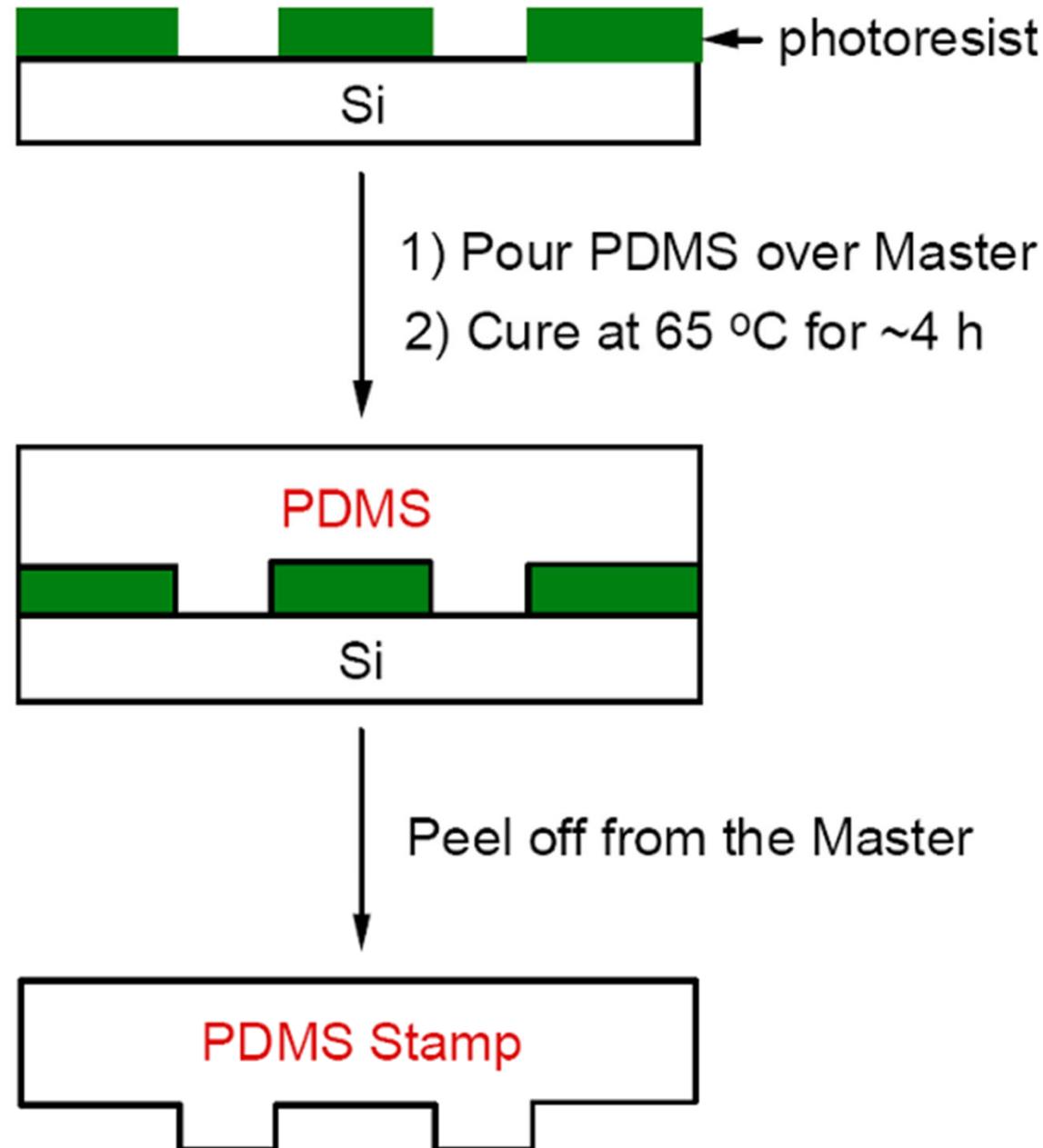
PDMS fabrication for Soft lithography



**George M. Whitesides Group
Harvard University**

| Soft Lithography μ -contact printing

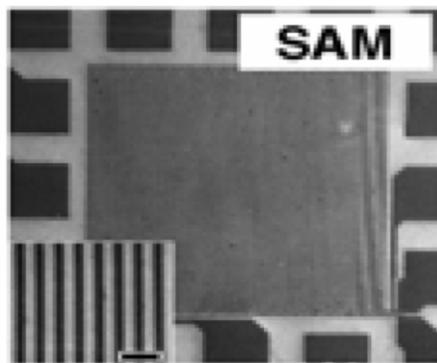
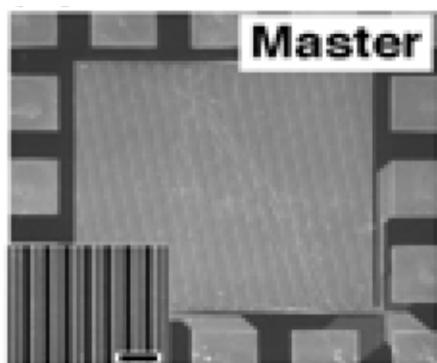
PDMS Fabrication



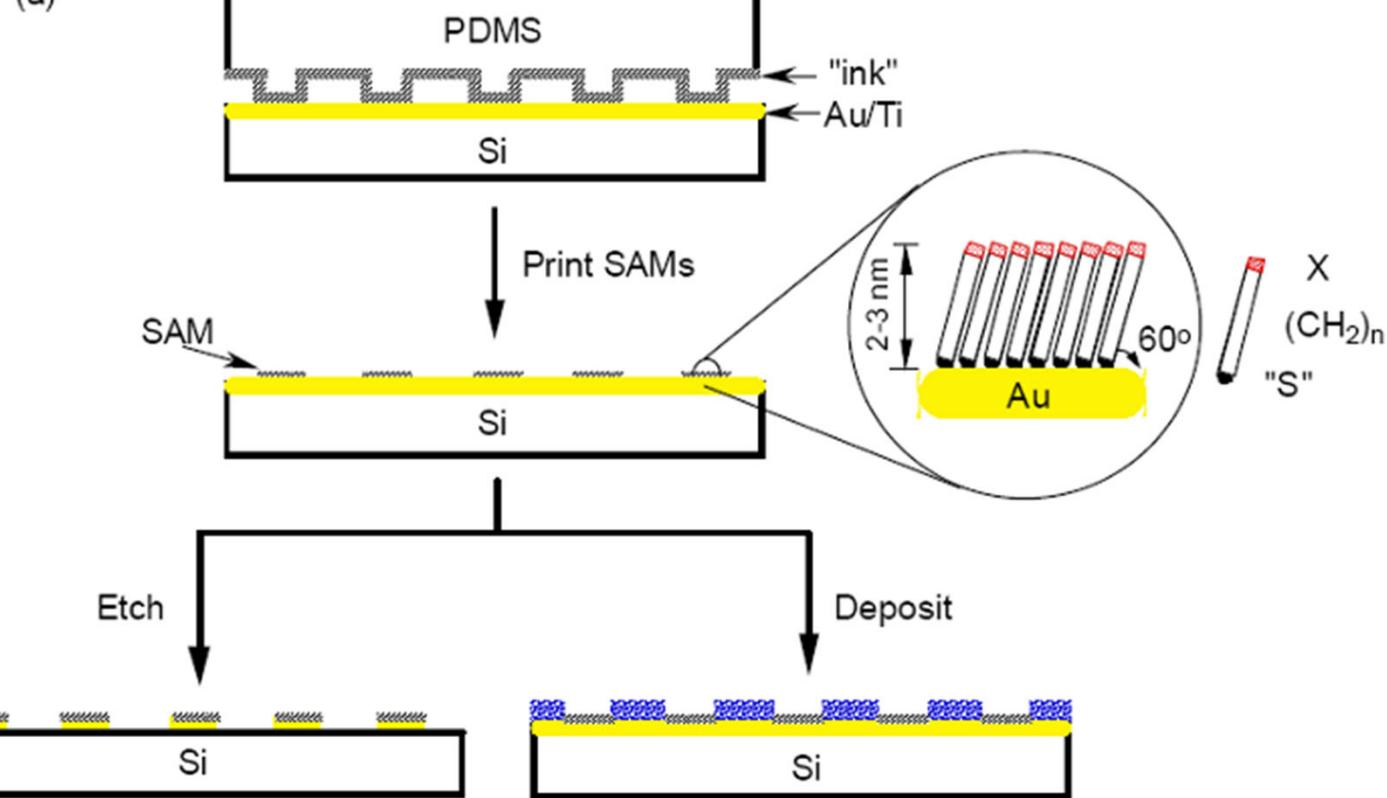
(PDMS: Polydimethylsiloxane)

I Soft Lithography μ -contact printing

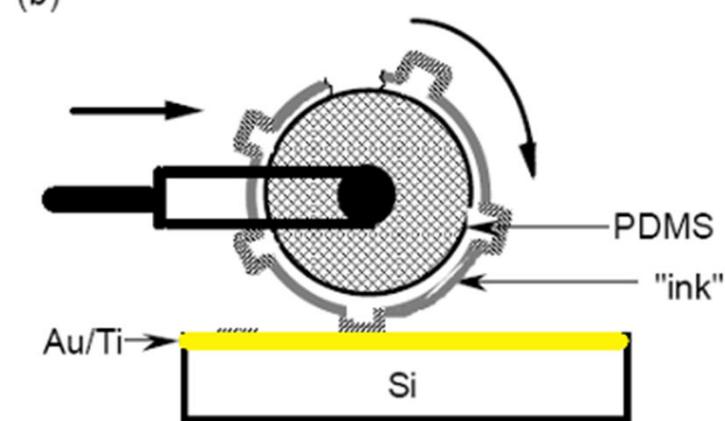
Micro contact Printing



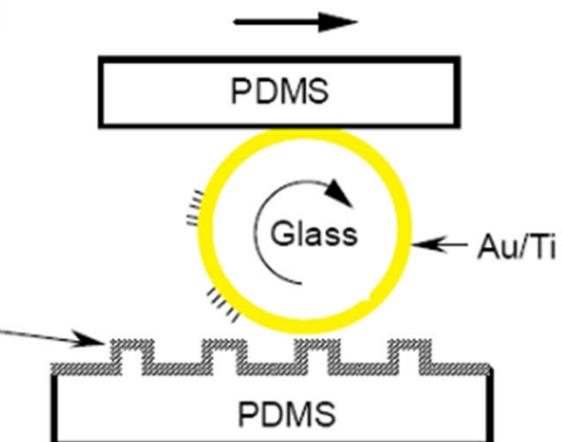
(a)



(b)

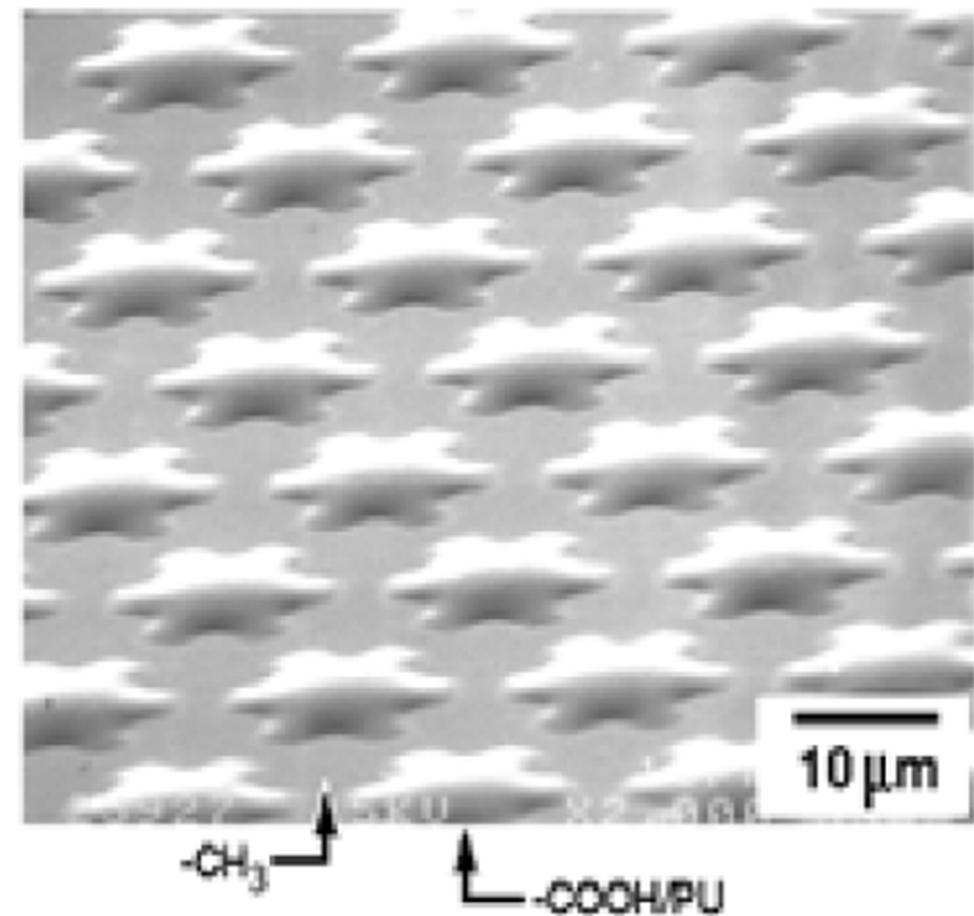
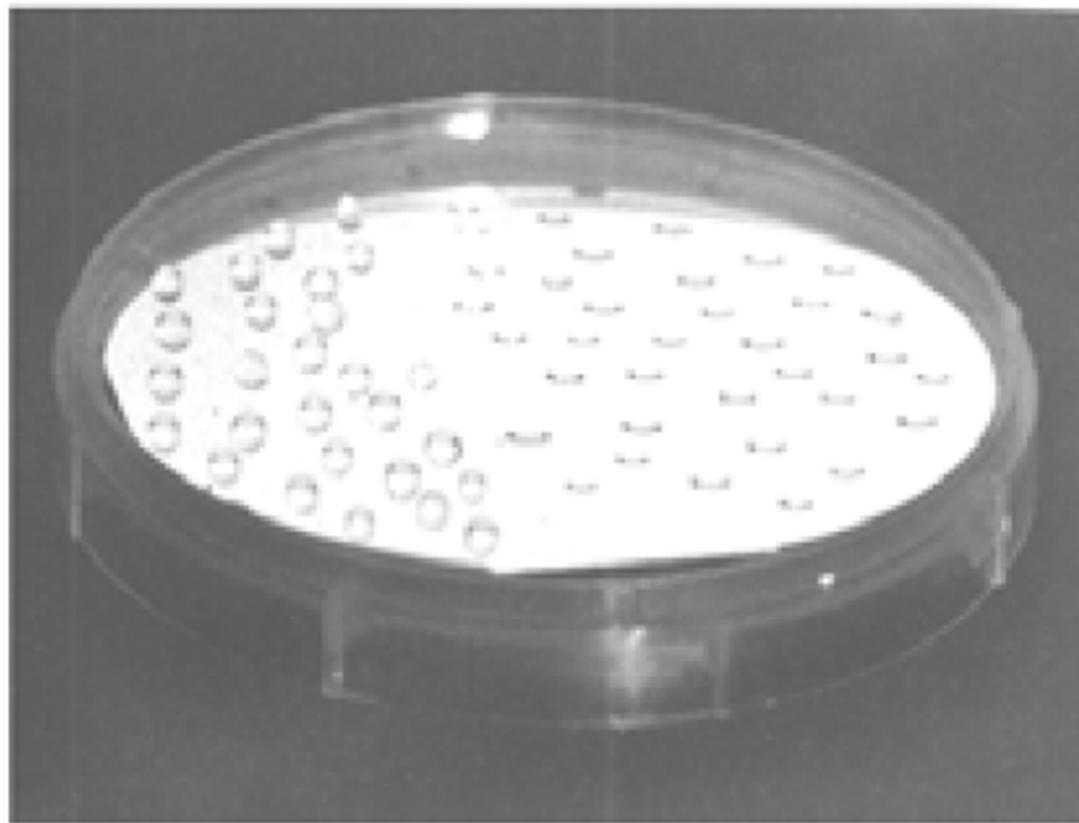


(c)



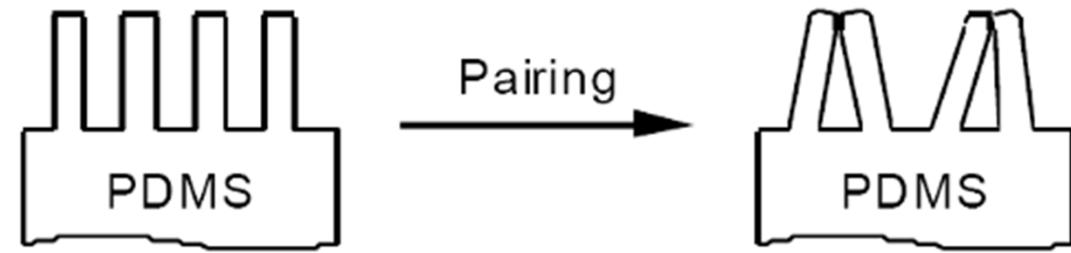
| Soft Lithography μ-contact printing

Wetting Dewetting Hydrophilic / Hydrophobic coating



| Soft Lithography μ -contact printing

Considerations

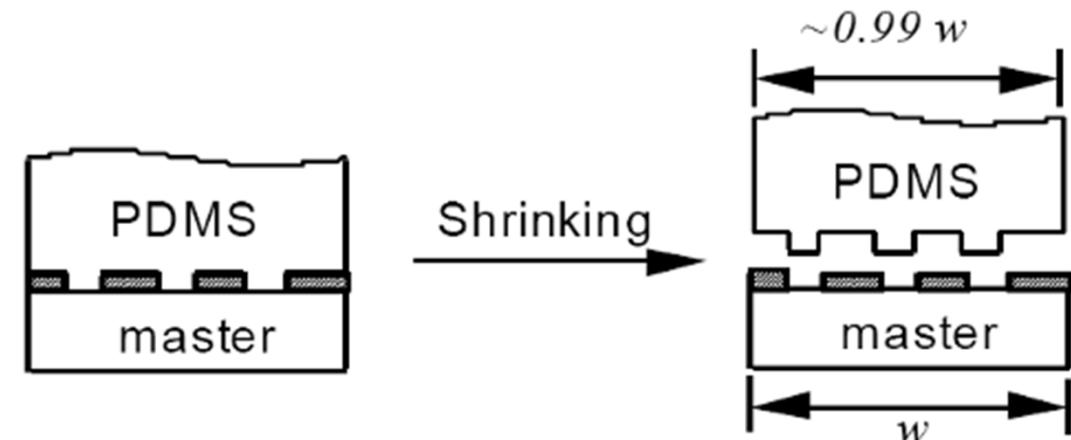


Limit the Aspect ratio to 3!

$1/3 > \text{Height} / \text{Width} < 3$



Multi layer printing is possible



| Soft Lithography μ -contact printing

Large area Printing

