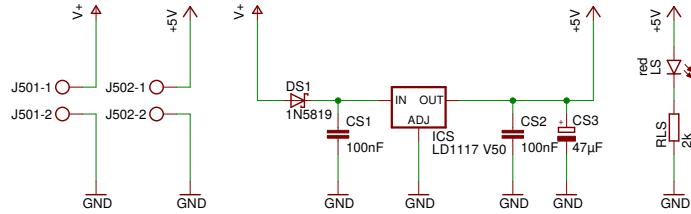


Supply

The diagram shows a 5V voltage regulator circuit. The input (IN) of the 7805 regulator (DS1) is connected to a 5V supply through a 100nF capacitor (CS1). The output (OUT) of the regulator is connected to the ADJ pin and also has a 100nF capacitor (CS2) to ground. A 47µF capacitor (CS3) is connected between the OUT and ADJ pins. The ADJ pin is also connected to a 5V supply through a 2k resistor (RLS). A red LED (LS) is connected between the 5V supply and ground.

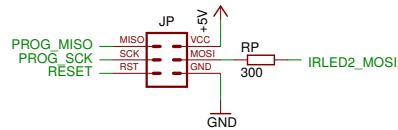


Buttons



Programmer

The diagram shows a programmer circuit. A 5V supply is connected to the VCC pin of a JP header. The MOSI pin is connected to the MOSI pin of an RP header. The RST pin is connected to the RST pin of an RP header. The RP header is connected to a 300 ohm resistor, which is then connected to the IRLED2_MOSI pin. The GND pin is connected to the GND pin of the RP header.



Reserve

The diagram illustrates the pin connections for the Reserve module. It features two headers: JP6 and JT.

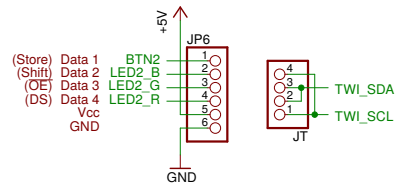
JP6 Header Connections:

- Pin 1: BTN2 (Store)
- Pin 2: LED2_B (Shift)
- Pin 3: LED2_G (Data 3)
- Pin 4: LED2_R (Data 4)
- Pin 5: Vcc
- Pin 6: GND

JT Header Connections:

- Pin 1: TWI_SCL
- Pin 2: TWI_SDA
- Pin 3: TWI_SDA
- Pin 4: TWI_SDA

The module is powered by a +5V supply connected to the Vcc pin (JP6 pin 5) and ground (JP6 pin 6 and JT pin 1).



MCU

MEGA328P-PU

RESET

+5V

GND

CM3 100nF

CM2 100nF

CM1 100nF

CM2 18pF

CM1 18pF

CM3 18pF

CM4 18pF

CM5 18pF

CM6 18pF

CM7 18pF

CM8 18pF

CM9 18pF

CM10 18pF

CM11 18pF

CM12 18pF

CM13 18pF

CM14 18pF

CM15 18pF

CM16 18pF

CM17 18pF

CM18 18pF

CM19 18pF

CM20 18pF

CM21 18pF

CM22 18pF

CM23 18pF

CM24 18pF

CM25 18pF

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CM175 18pF

CM176 18pF

CM177 18pF

CM178 18pF

CM179 18pF

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CM184 18pF

CM185 18pF

CM186 18pF

CM187 18pF

CM188 18pF

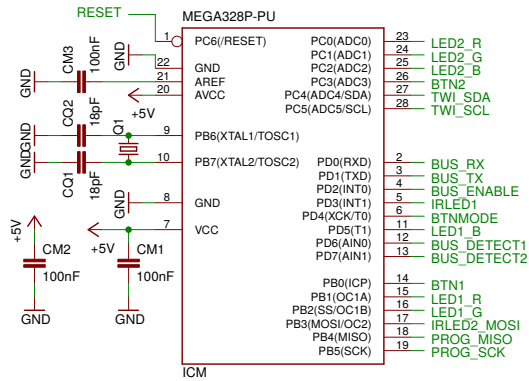
CM189 18pF

CM190 18pF

CM191 18pF

CM192 18pF

CM193 18pF



Note: Using atmega8 schematics.
Pin-layout is the same.

LED-Driver

LED

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