



Copyright WUT 2017

This documentation describes Open Hardware and is licensed under the CERN OHL v1.1. You may redistribute and modify this documentation under the terms of the CERN OHL v1.1. (<http://ohw.org/CERNOHL>). This documentation is distributed WITHOUT ANY EXPRESS OR IMPLIED WARRANTY, INCLUDING OF MERCHANTABILITY, SATISFACTORY QUALITY AND FITNESS FOR A PARTICULAR PURPOSE. Please see the CERN OHL v1.1 for applicable conditions.

Project/Equipment		SINARA	
Cannot open file D:\Dropbox\DESIGN\MTCA projects\SI	<div style="text-align: center;"> <h1>RTM SATA adapter</h1> </div>	Designer	G.K.
		Drawn by	G.K.
		Check by	-
		Last Mod.	-
		File	PCB_RTM_SATA.SchDoc
		Print Date	22.07.2017 16:02:19
Warsaw University of Technology <i>ISE</i> Nowowiejska 15/19		Sheet	- of -
		Size	A3
			Rev

















