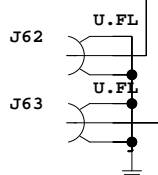
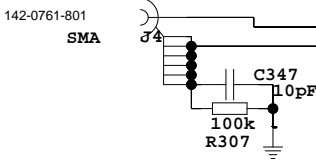
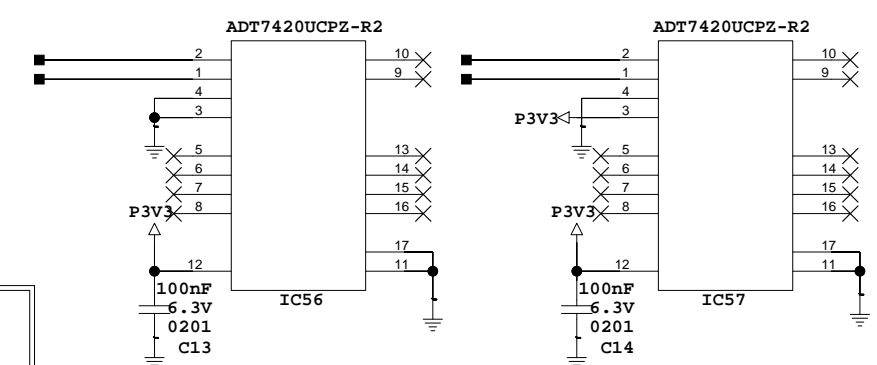
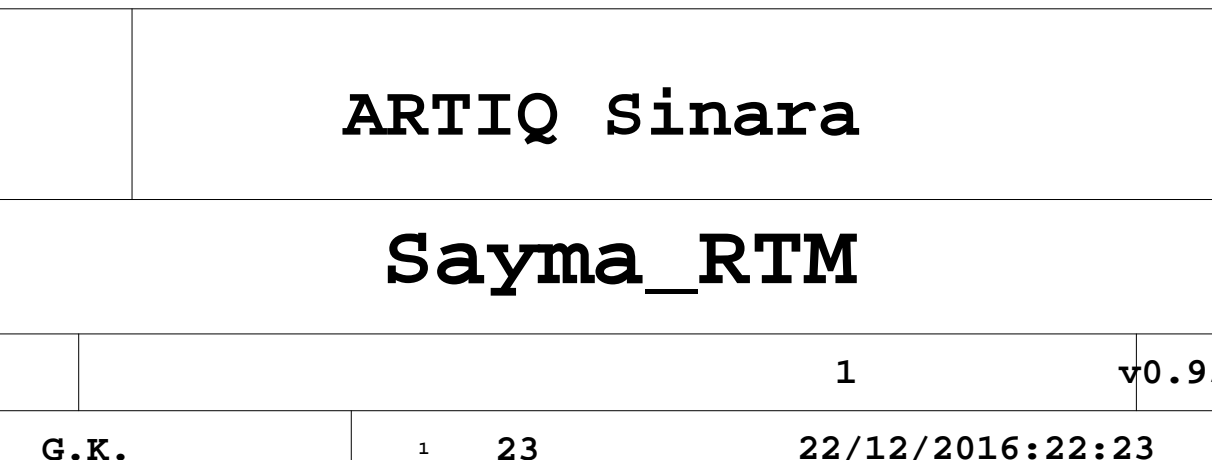
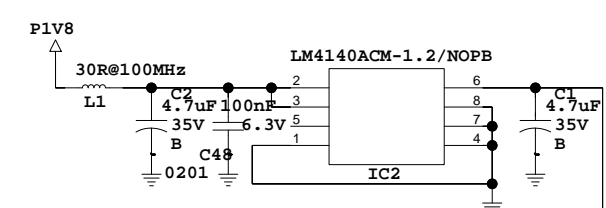
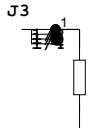
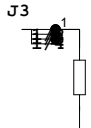
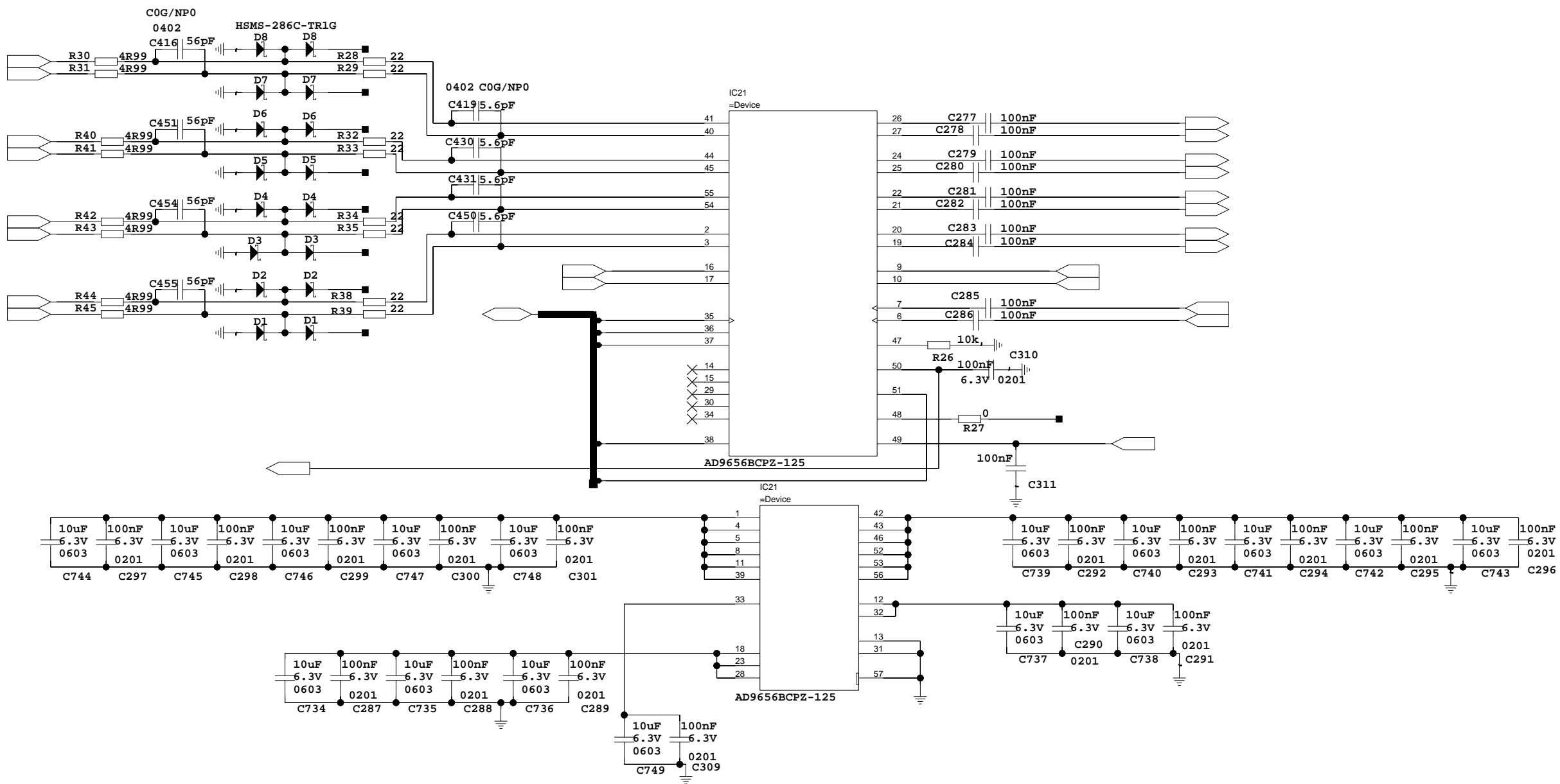


4





ADC_JESD204B

ARTIQ Sinara

1

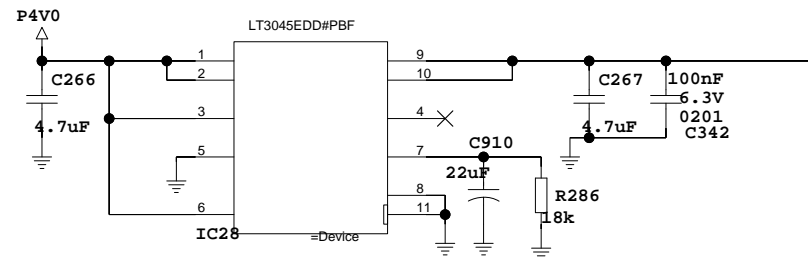
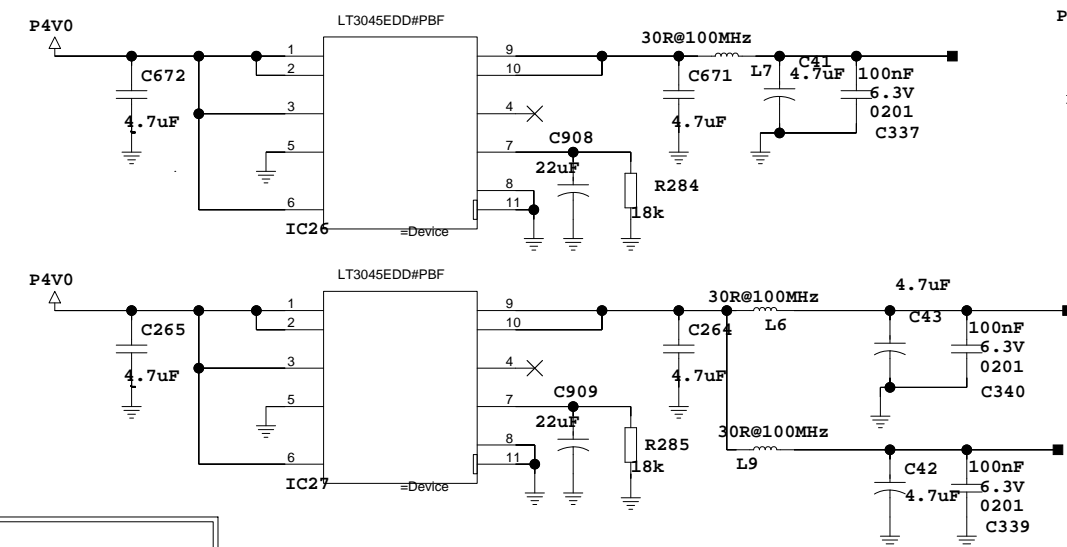
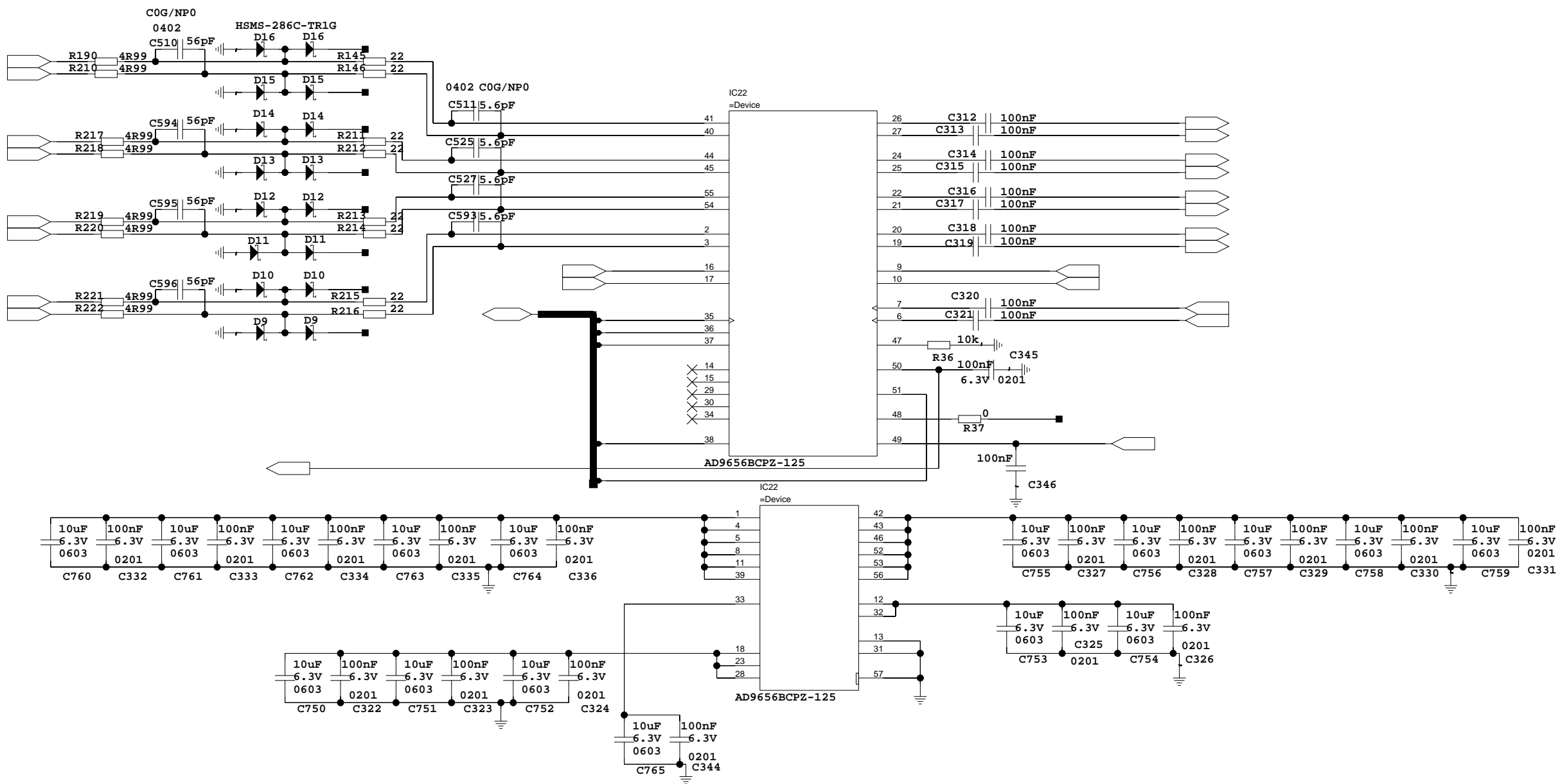
v0.95

G.K.

3

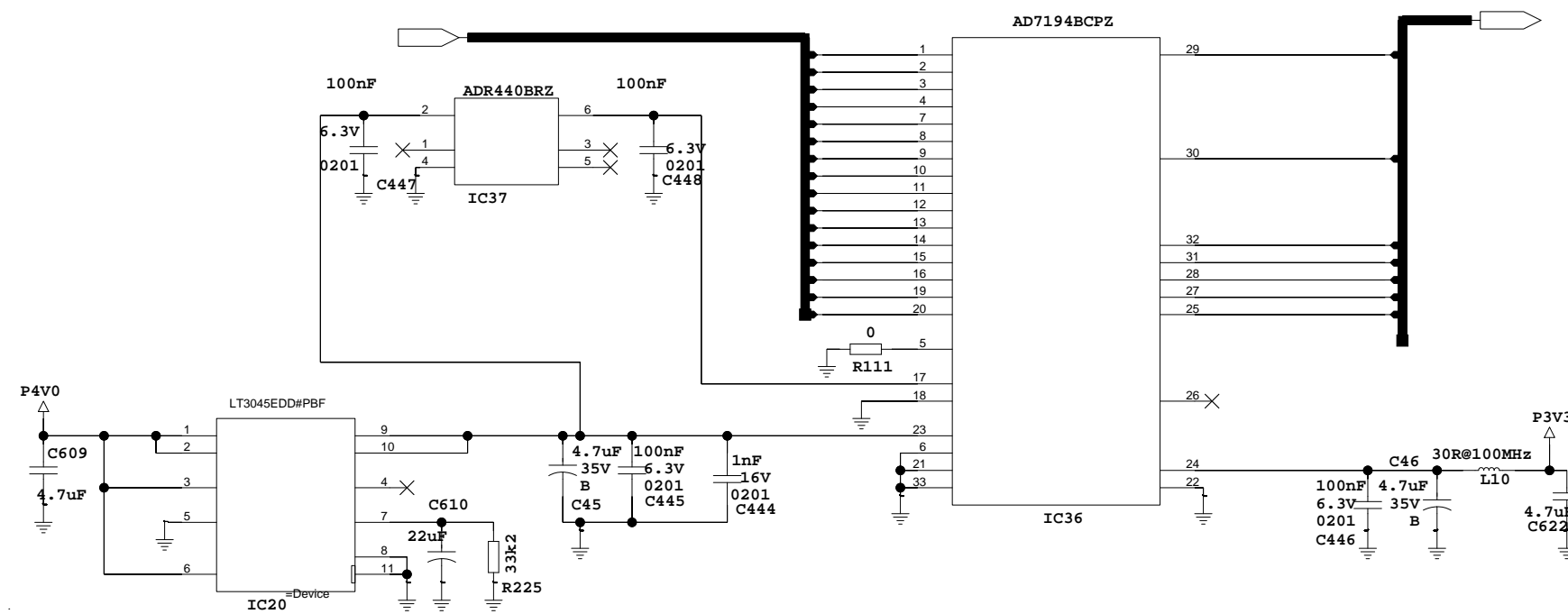
23

22/12/2016:10:55



ADC_JESD204B

ARTIQ Sinara



ARTIQ Sinara

CAL_ADC

1

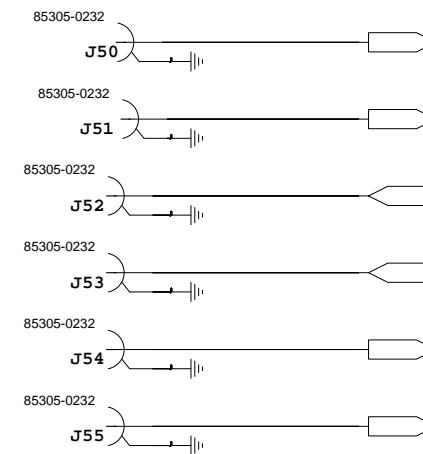
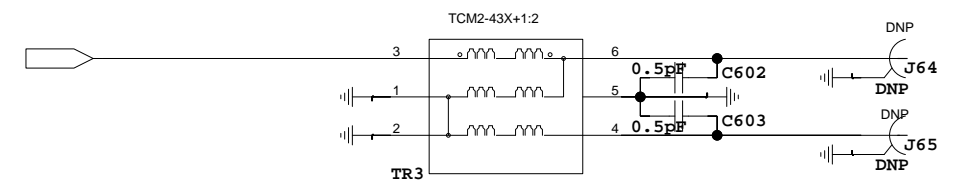
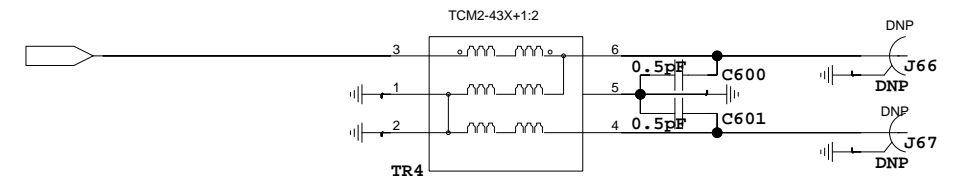
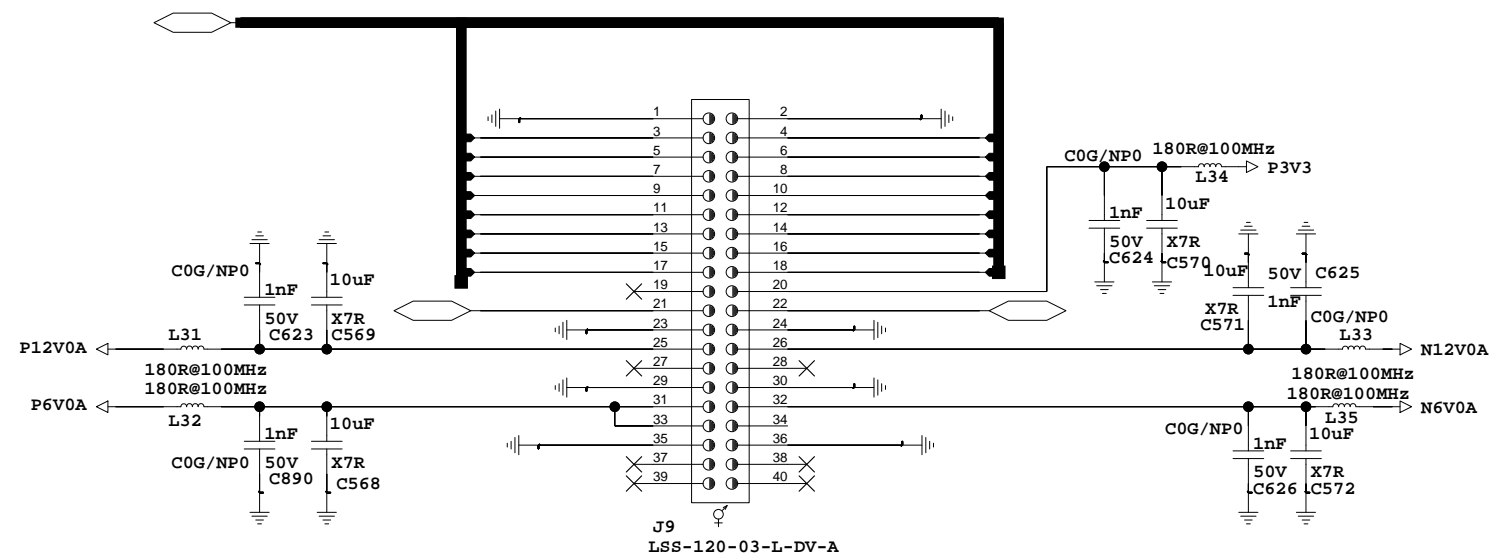
v0.95

G.K.

5

23

22/12/2016:22:41



ARTIQ Sinara

CLK_Mezzanine

1

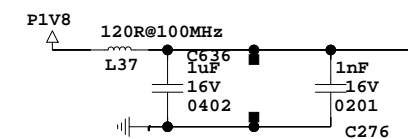
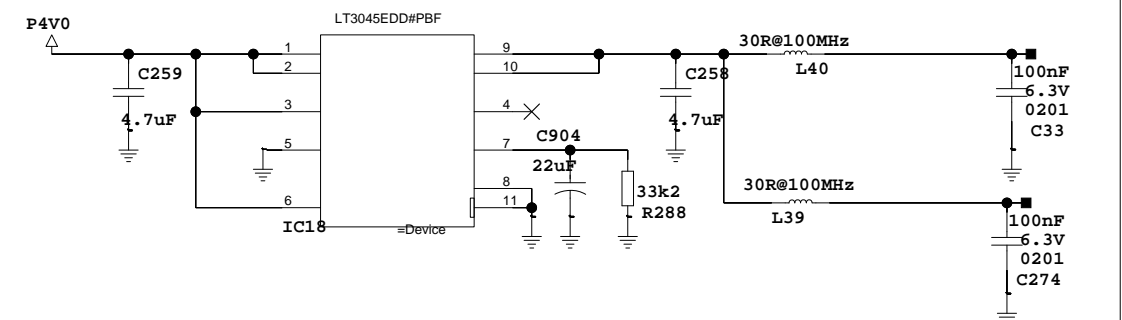
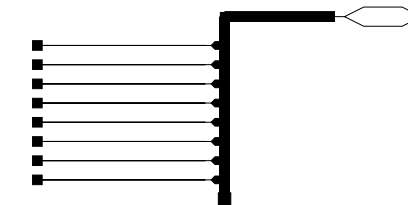
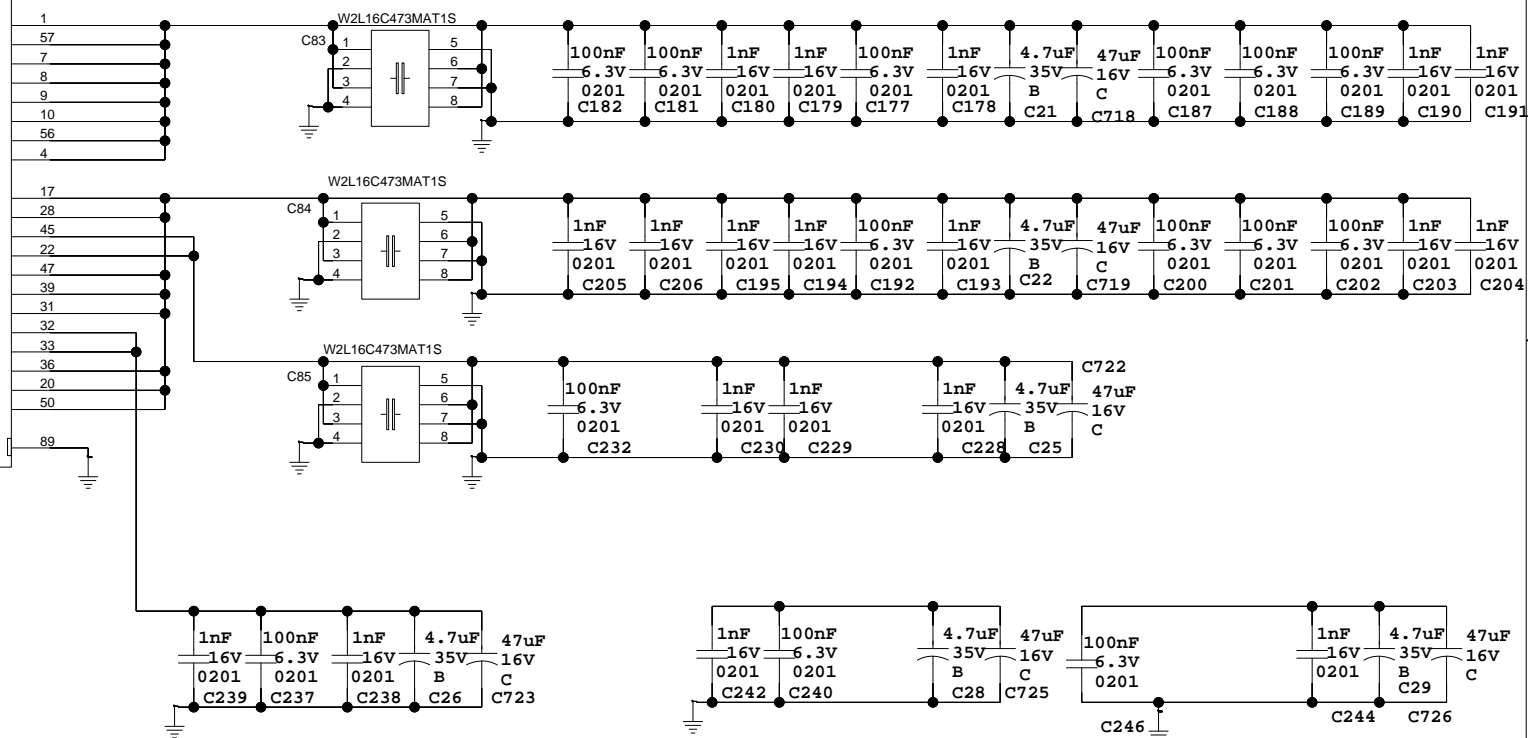
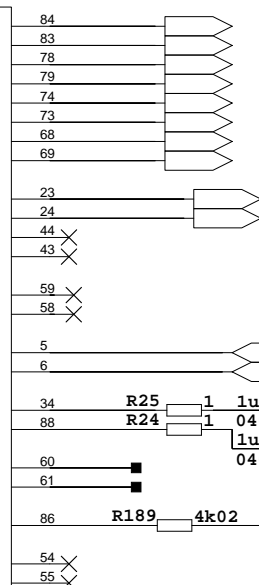
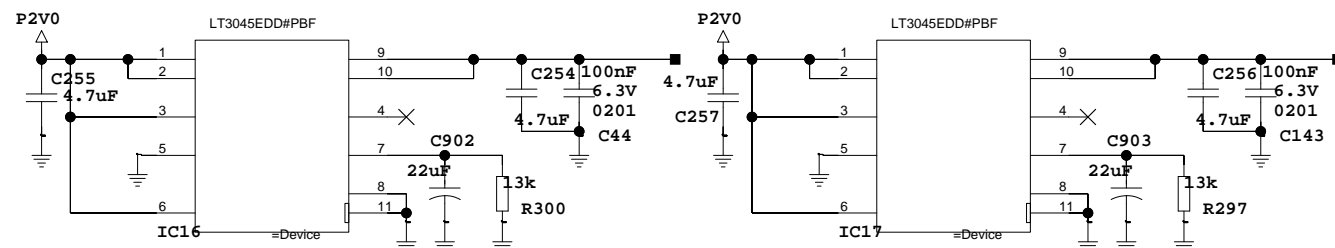
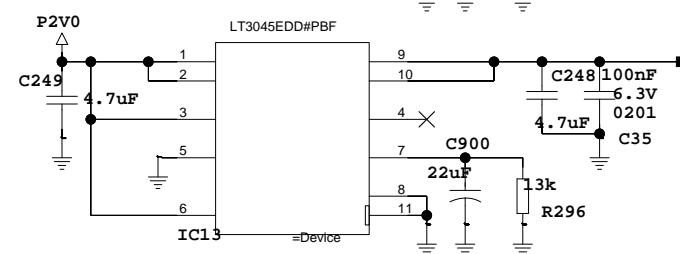
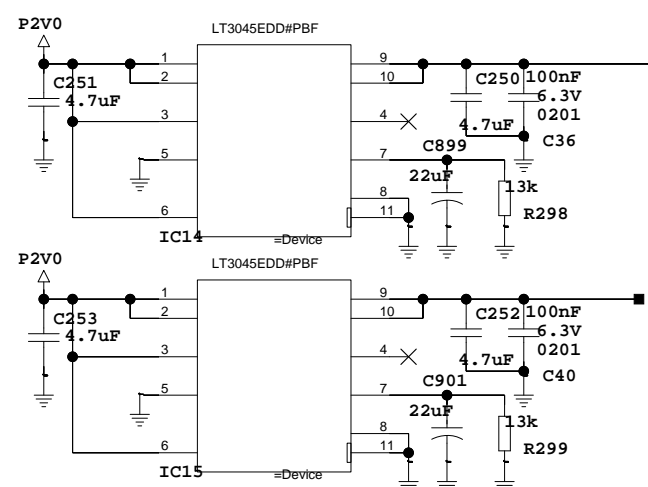
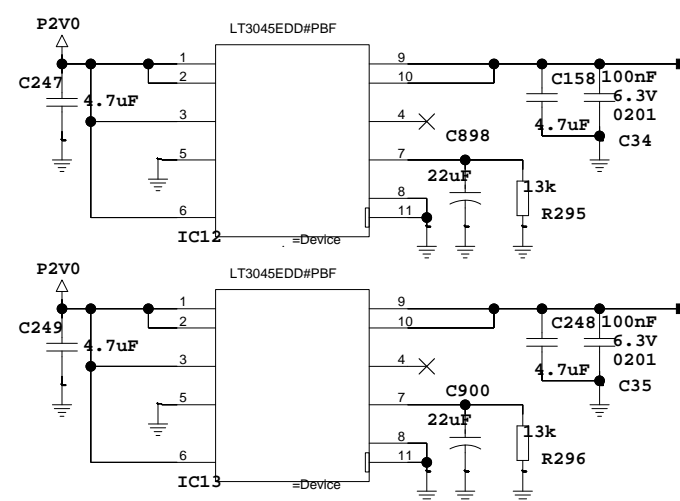
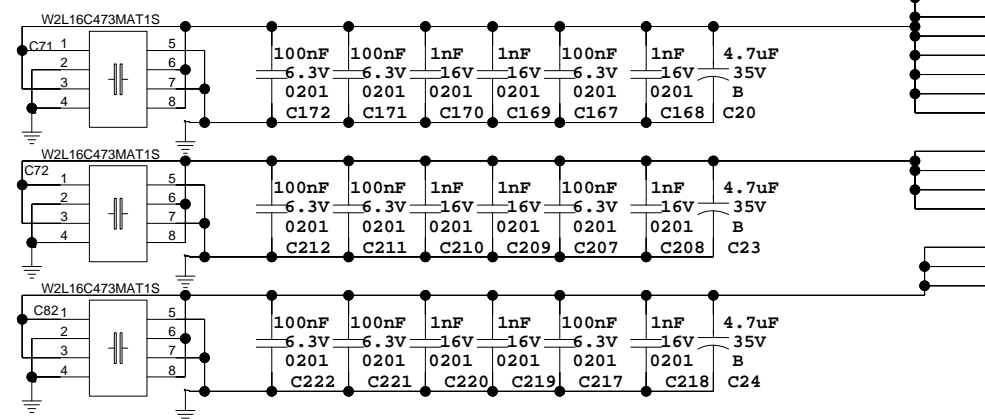
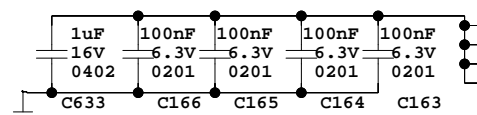
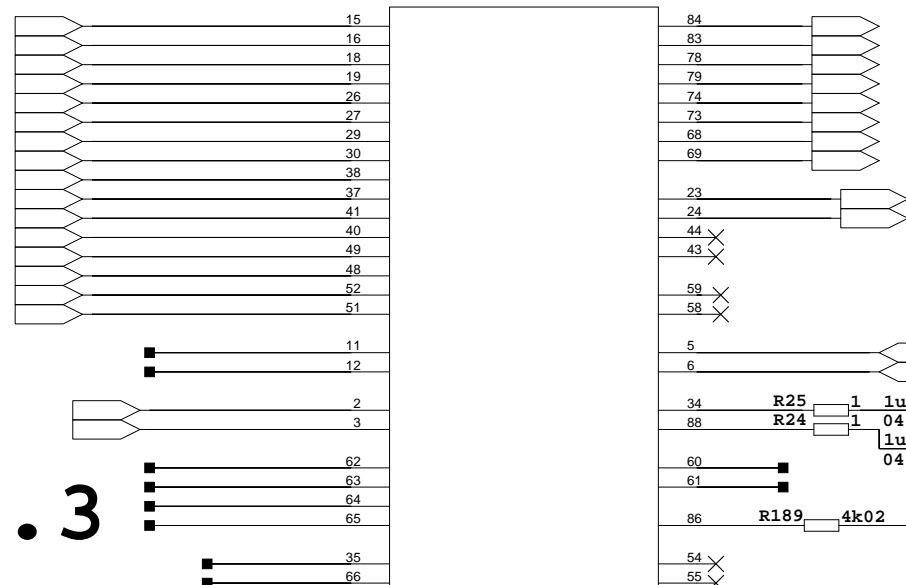
v0.95

G.K.

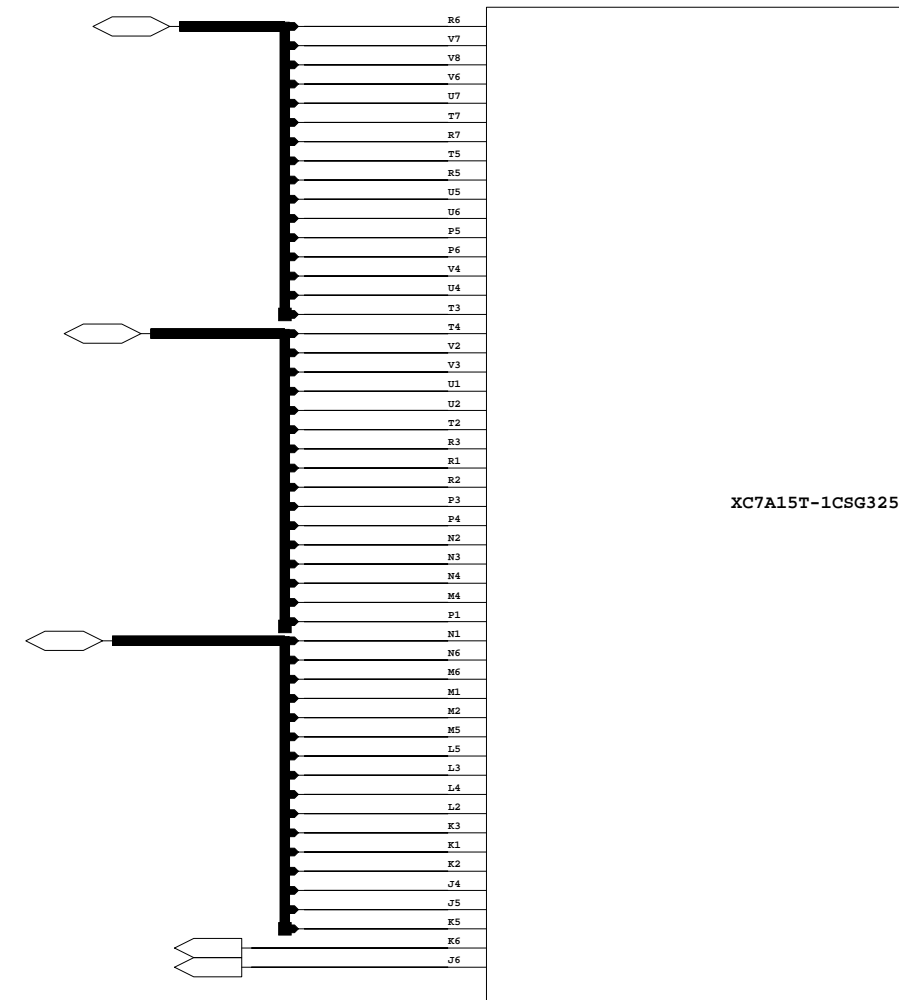
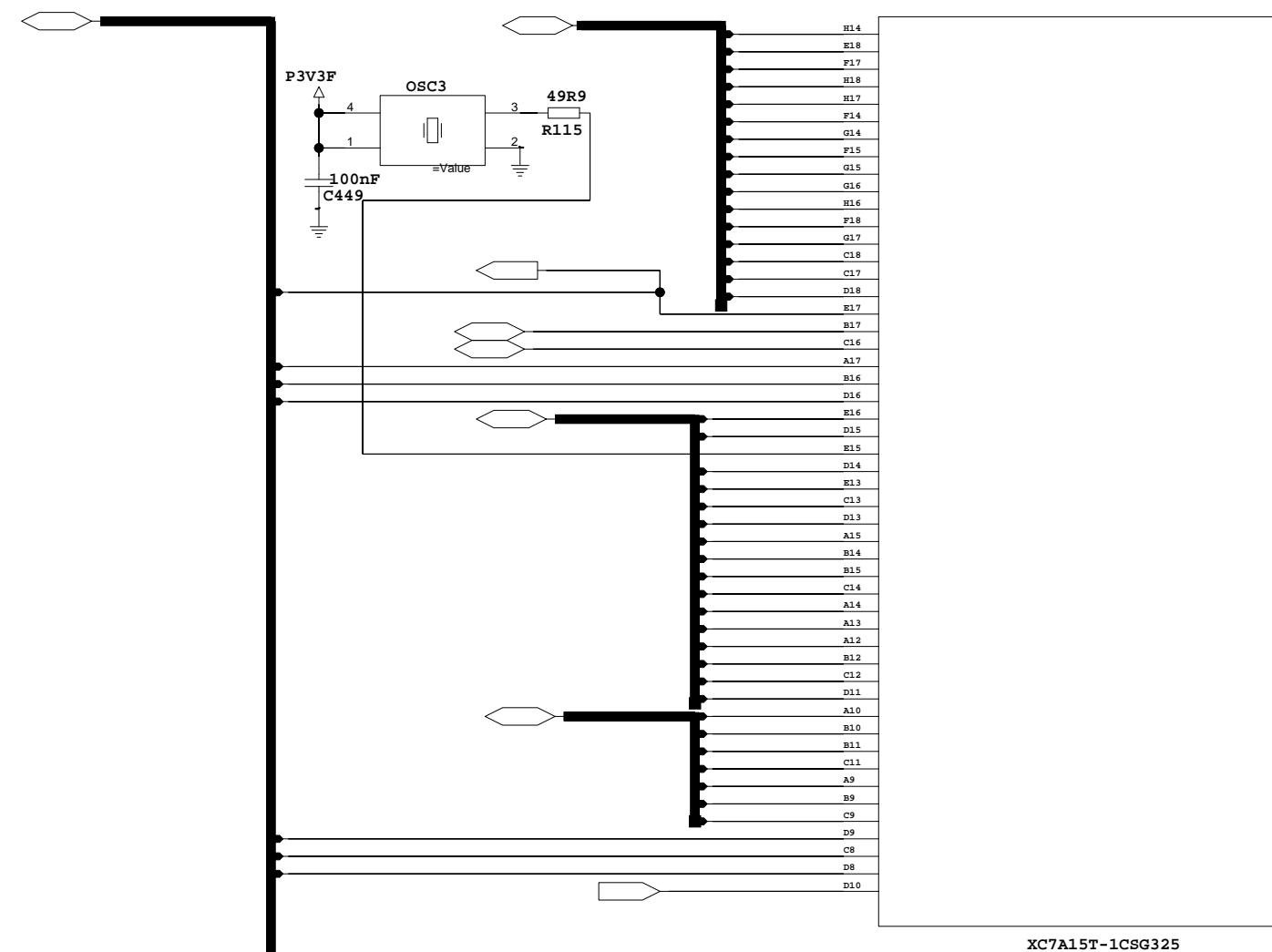
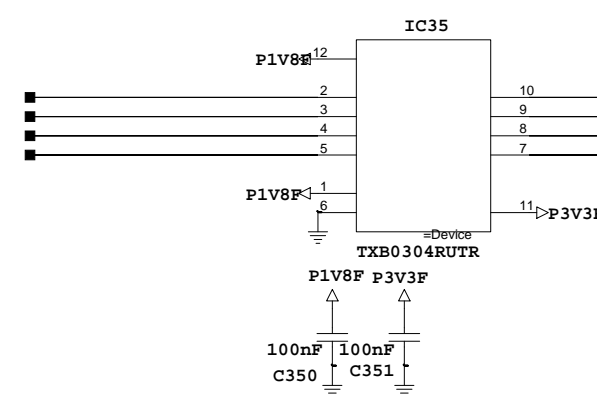
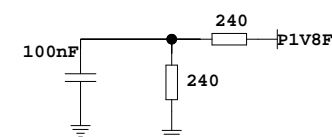
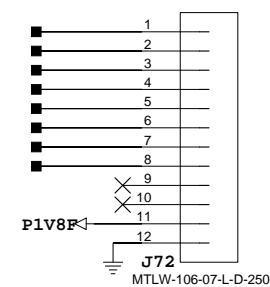
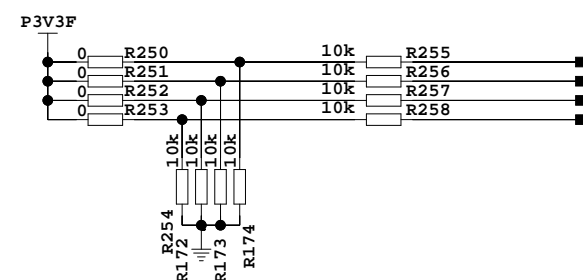
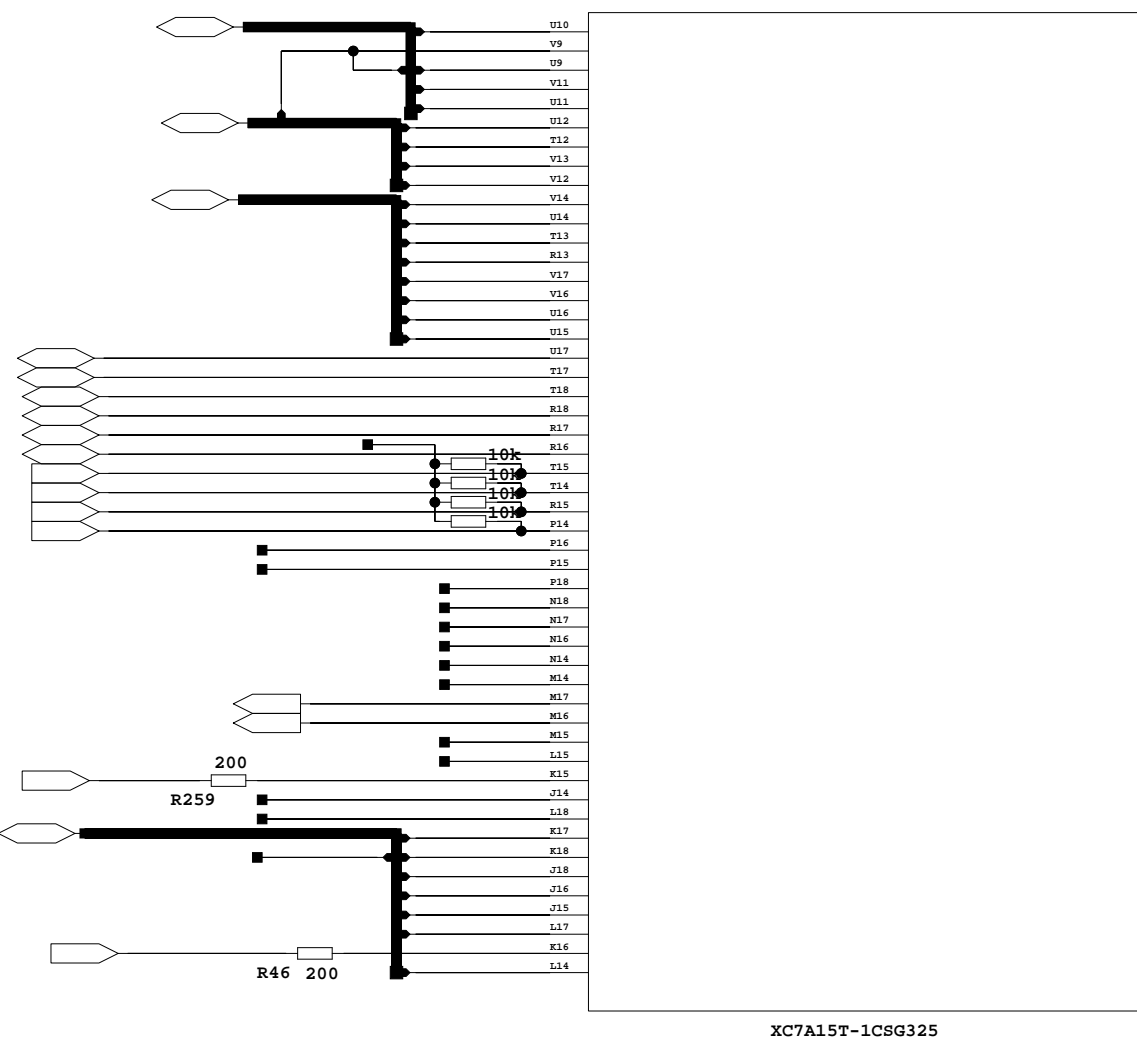
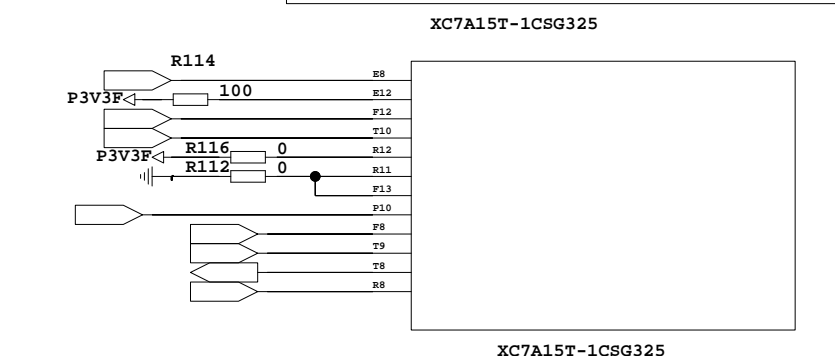
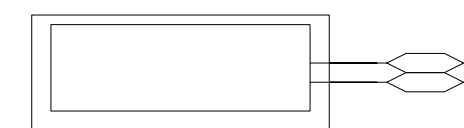
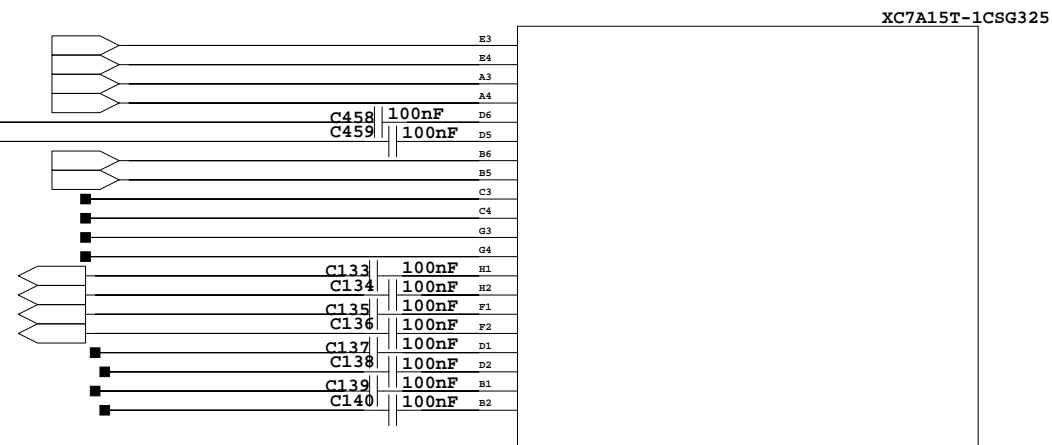
6

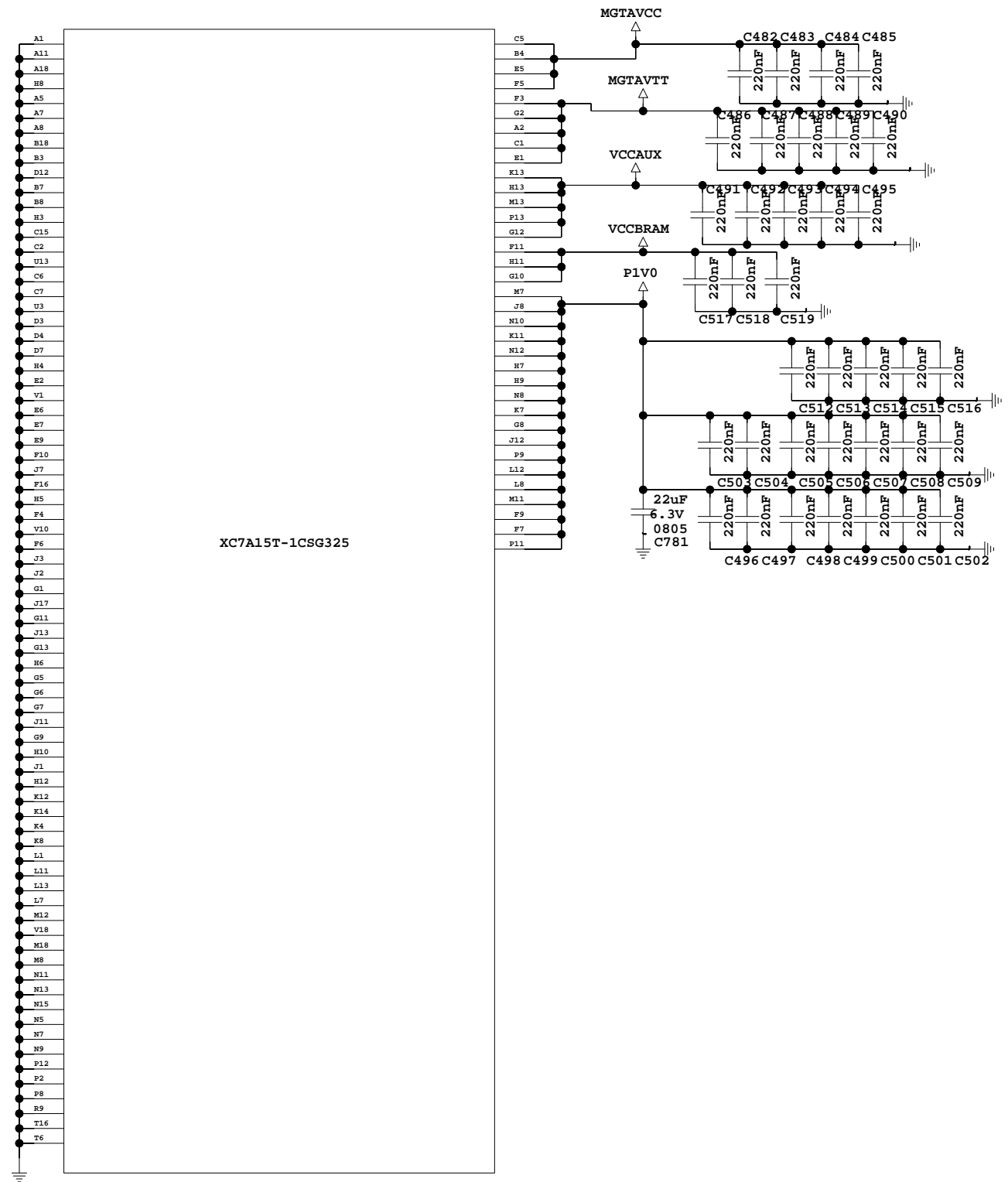
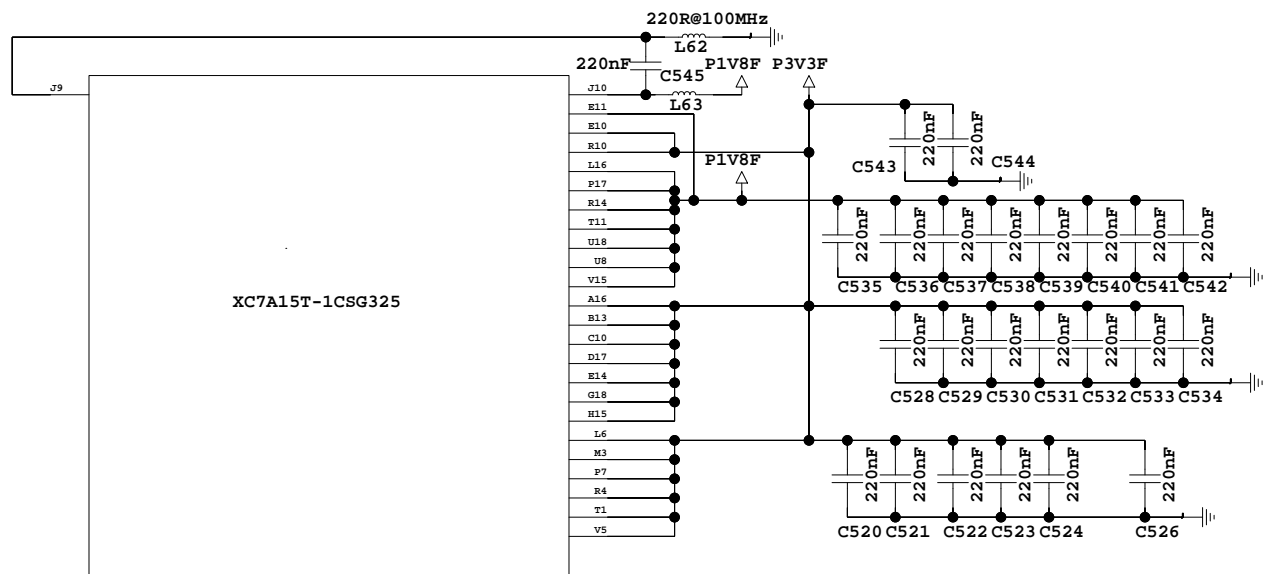
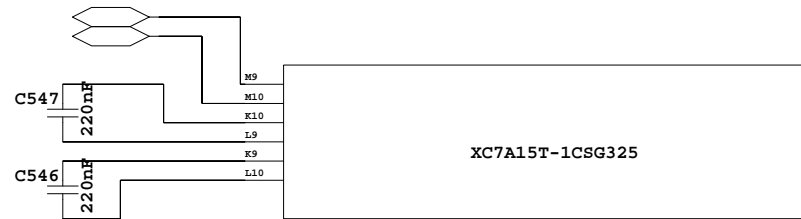
23

21/12/2016:23:49



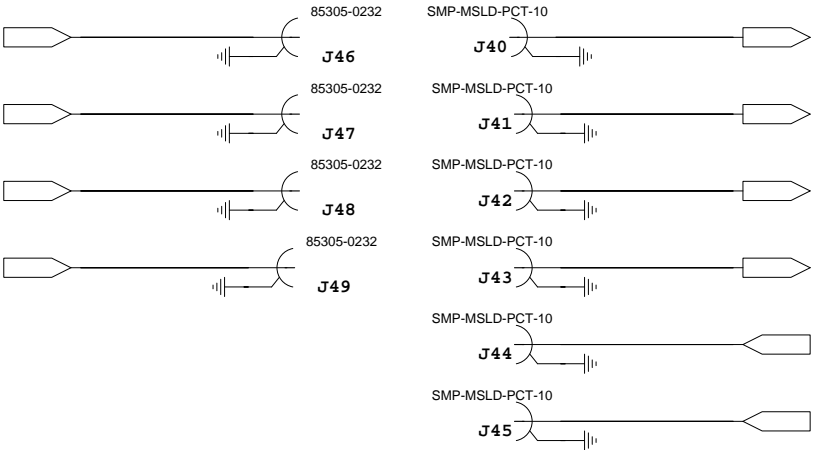
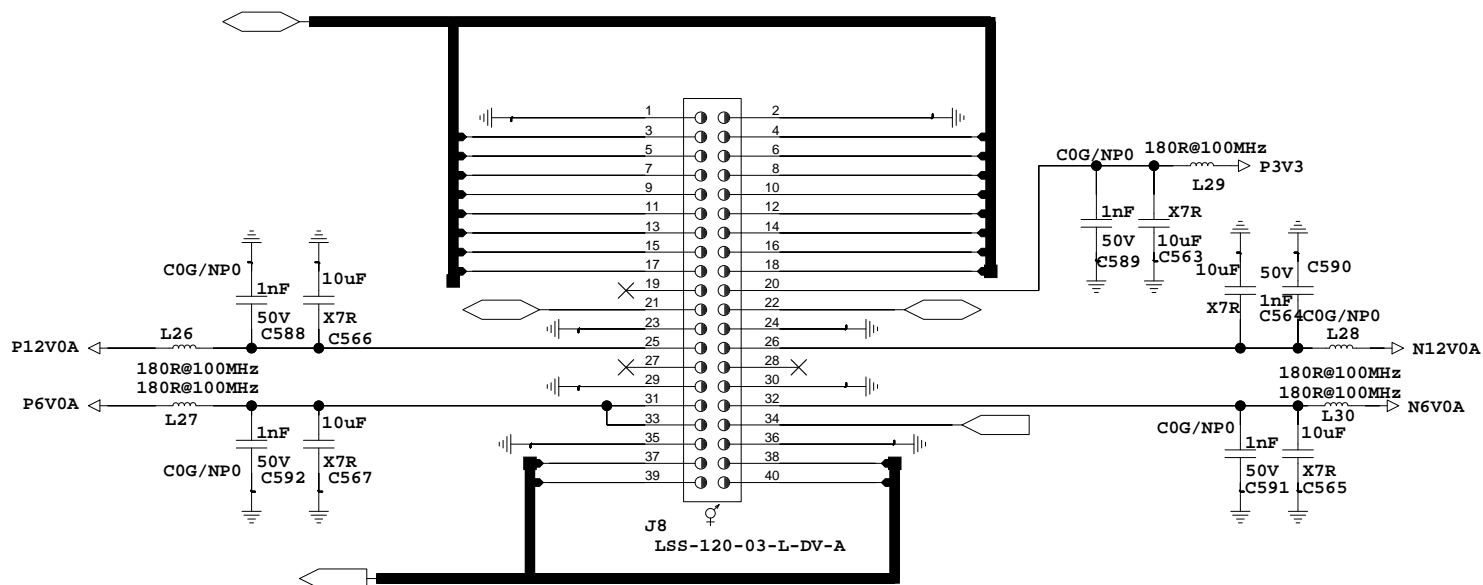
DAC_JESD204B





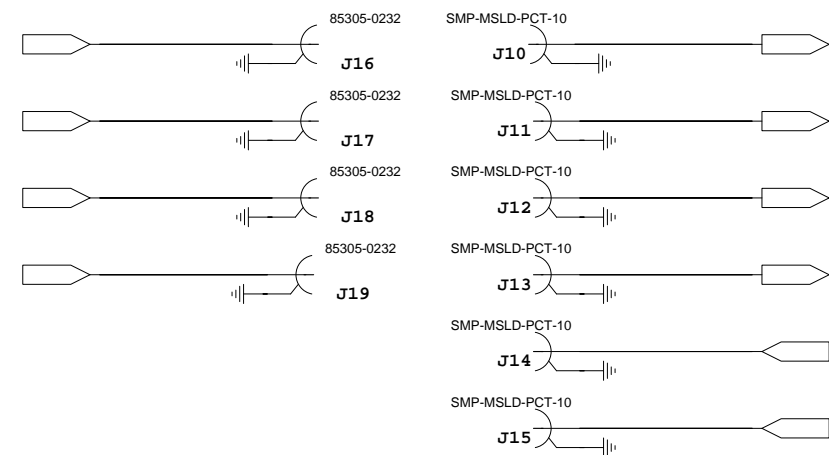
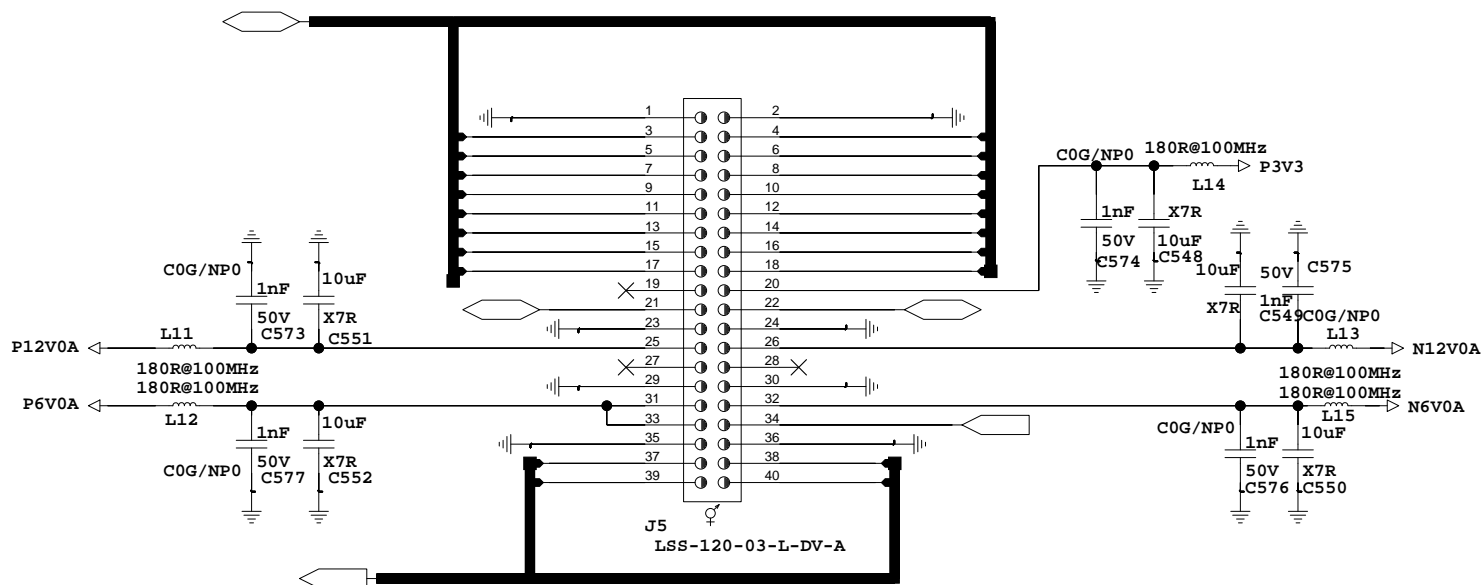
ARTIQ Sinara

FPGA_7A15T_POWER



ADC_DAC_AFE_Mezzanine

ARTIQ Sinara



ADC_DAC_AFE_Mezzanine

ARTIQ Sinara

1

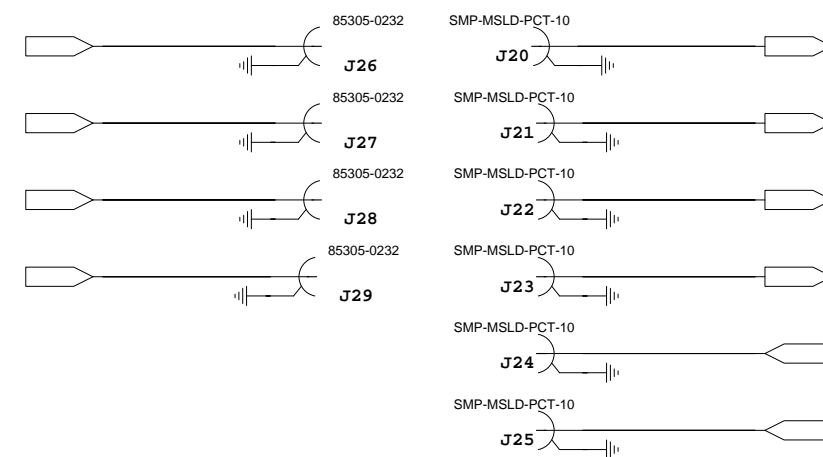
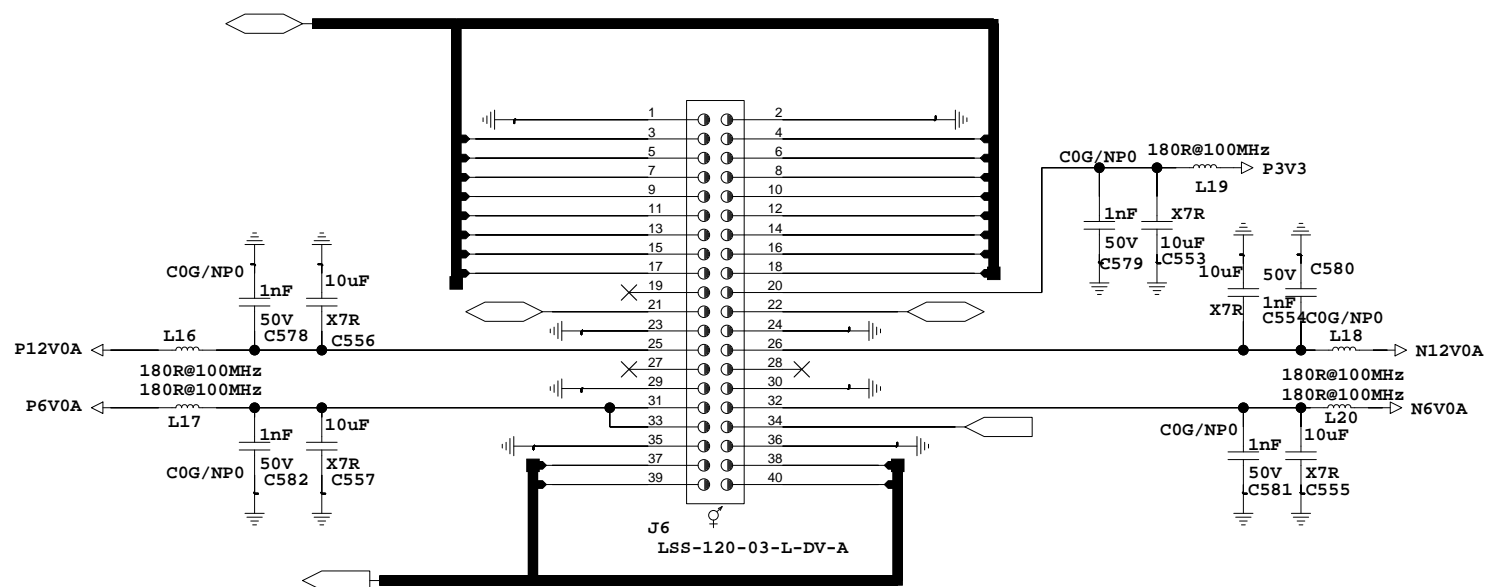
v0.95

G.K.

12

23

22/12/2016:09:55



ADC_DAC_AFE_Mezzanine

ARTIQ Sinara

1

v0.95

G.K.

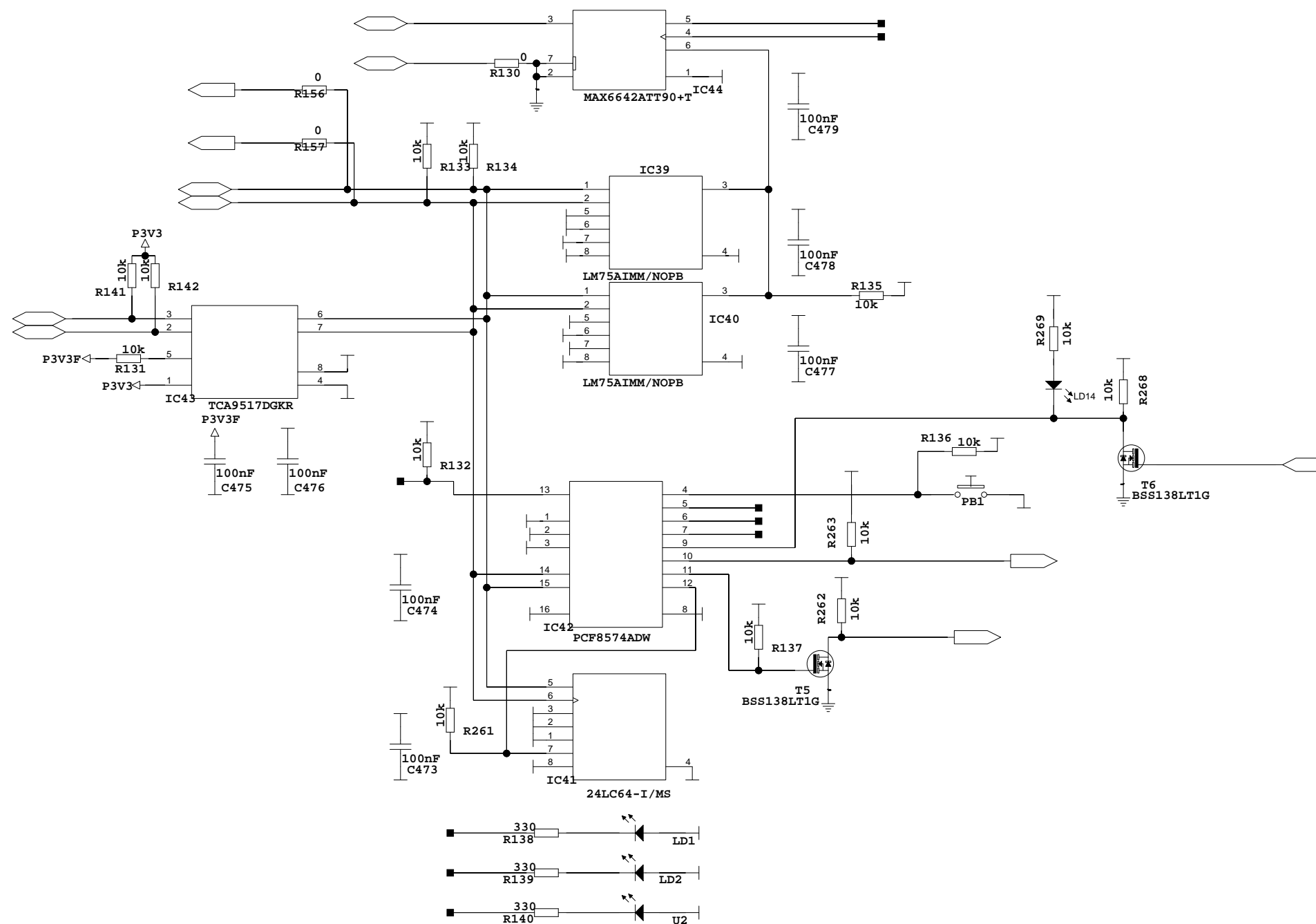
13

23

22/12/2016:09:55



ARTIQ Sinara



check addresses ARTIQ Sinara

RTM_IPMI

1

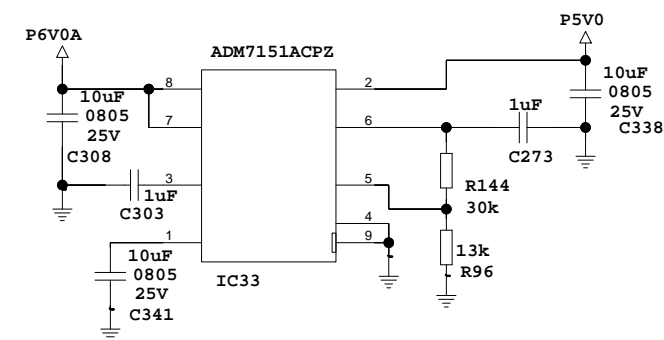
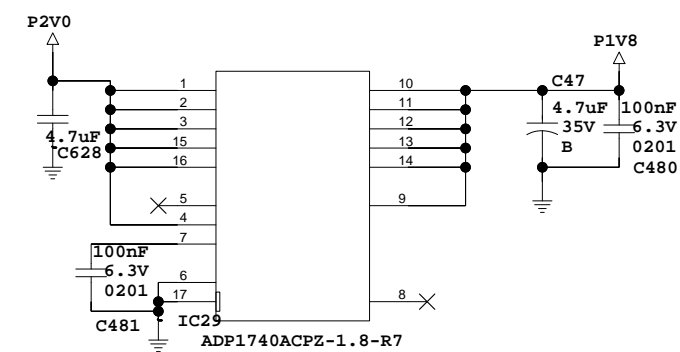
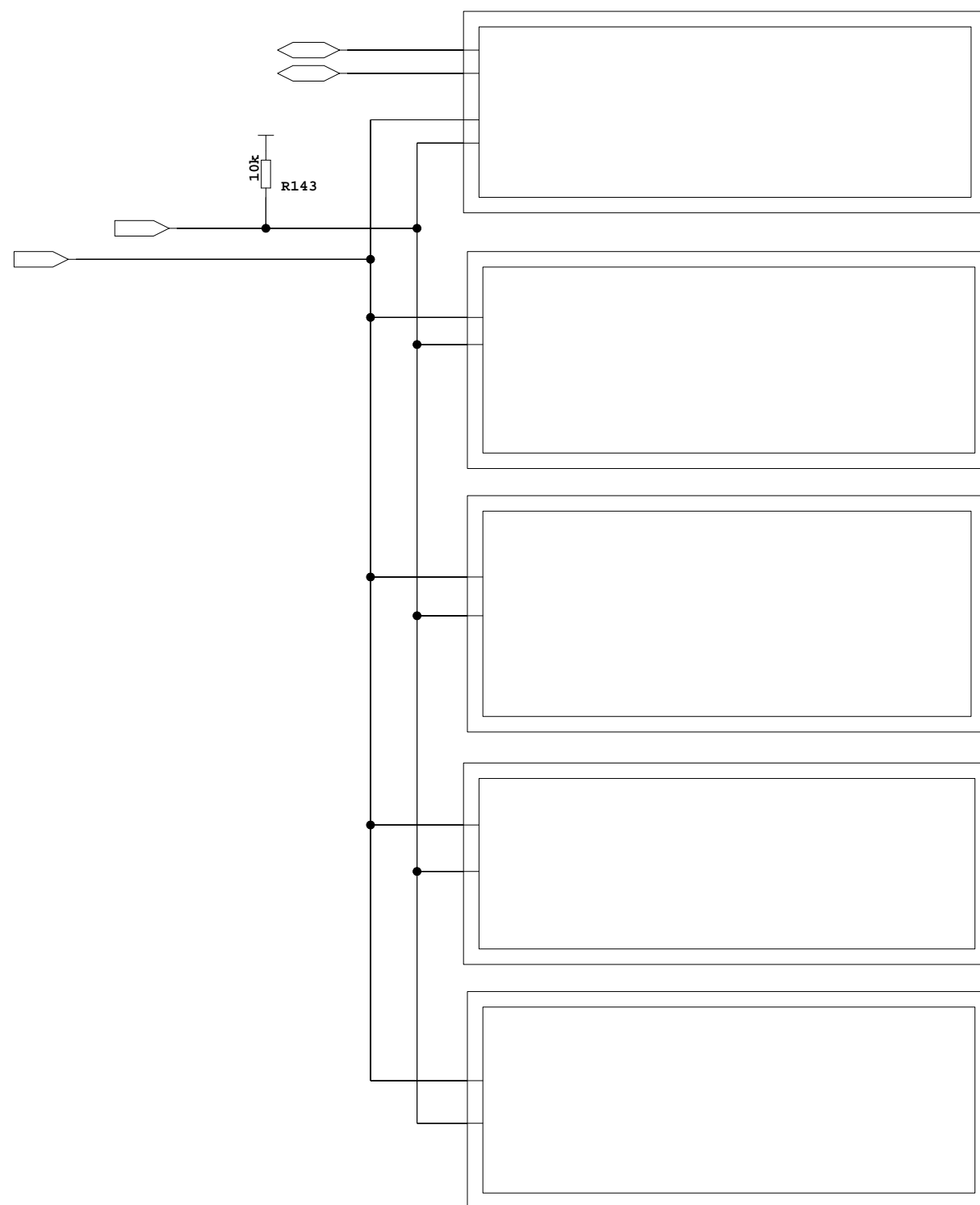
v0.95

G.K.

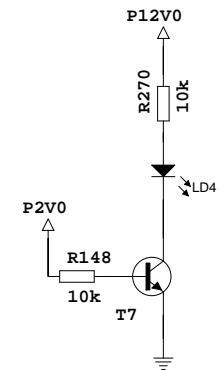
15

23

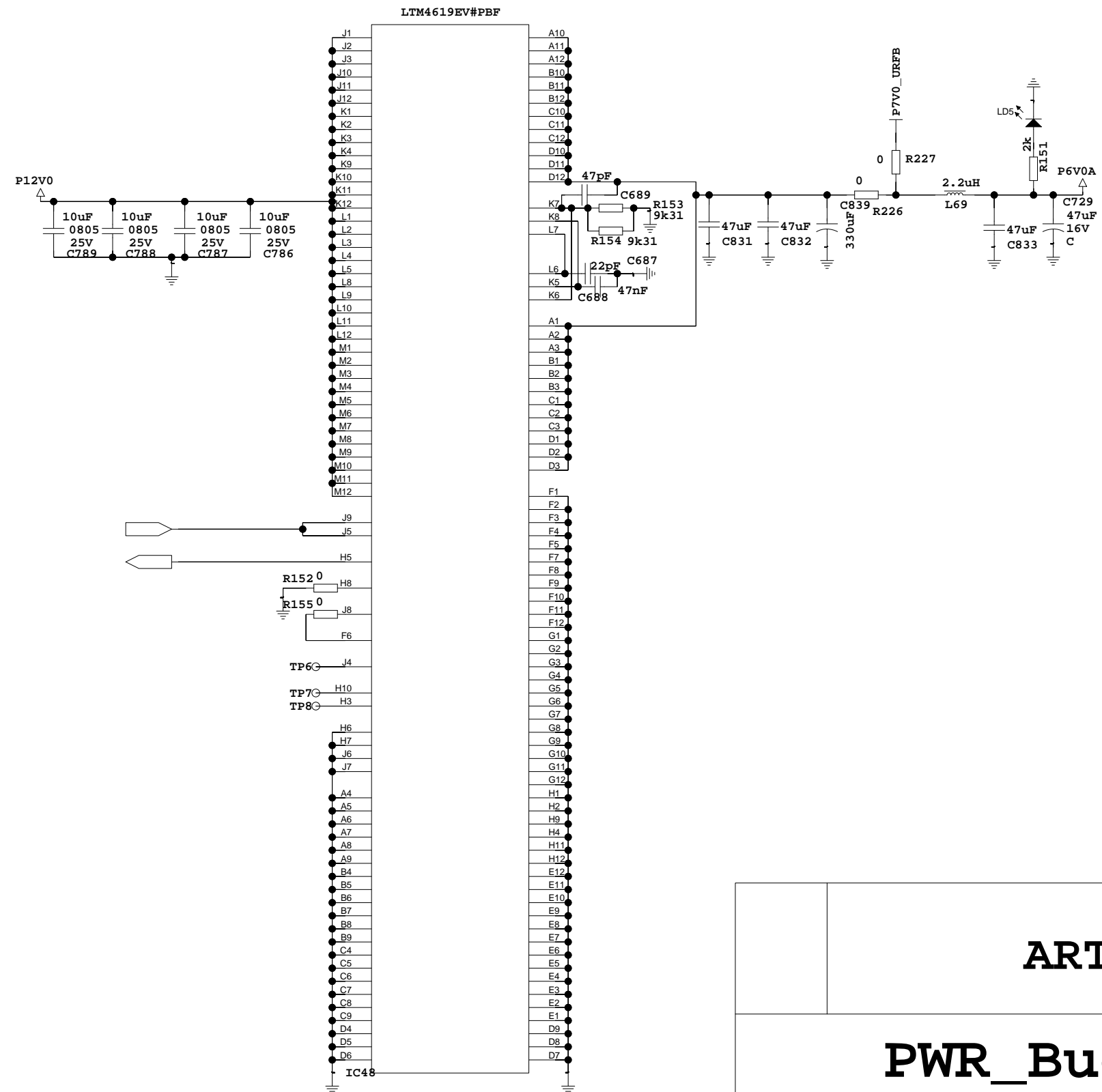
21/12/2016:23:49



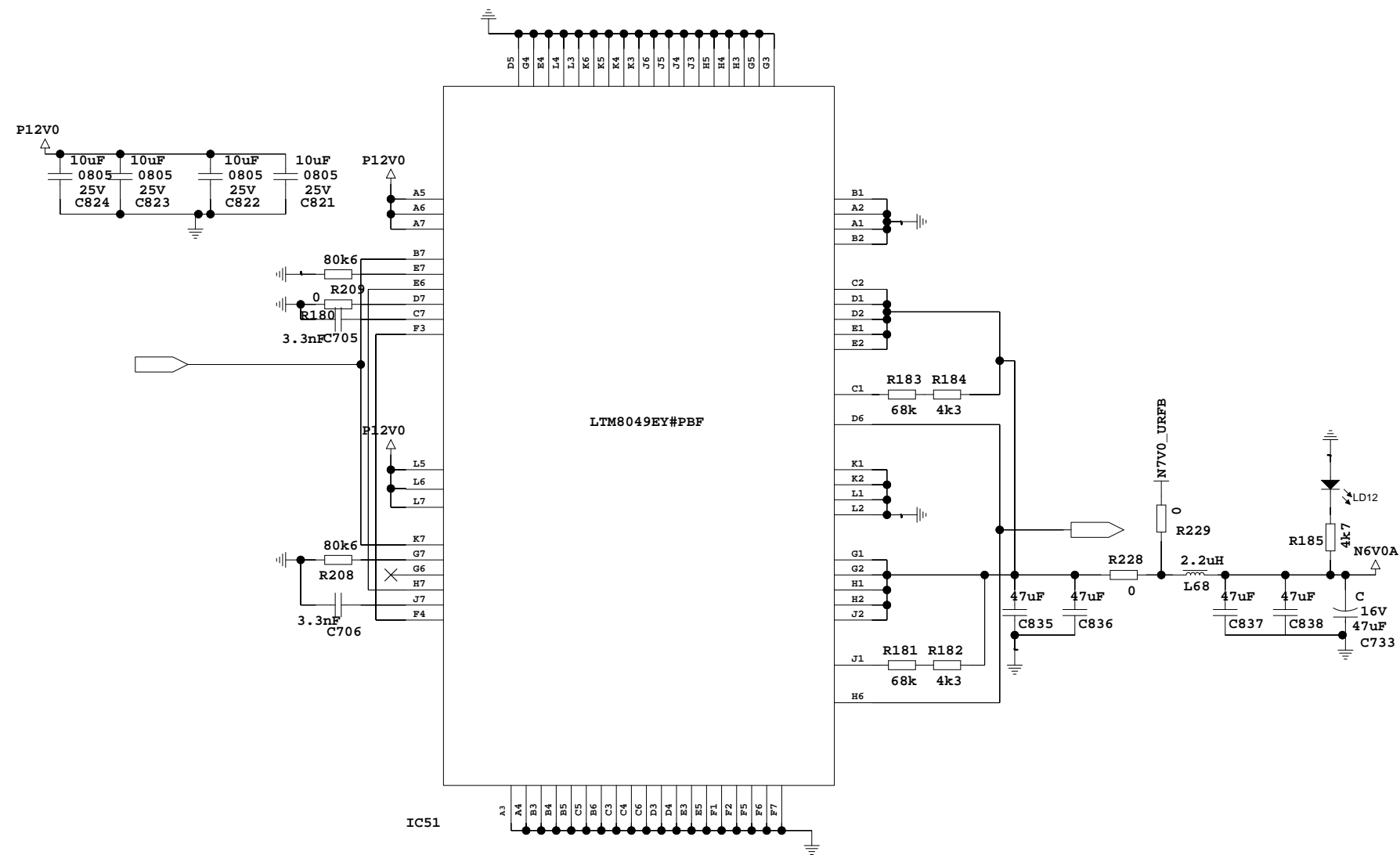
ARTIQ Sinara	
RTM_POWER_SUPPLY	
1	v0.95
G.K.	23 23/12/2016:22:20



PWR_Buck_2V_4V_4A



ARTIQ Sinara	
PWR_Buck_6V_8A	
1	v0.95
G.K.	21/12/2016:23:49



ARTIQ Sinara

PWR_SEPIC_N6V

1

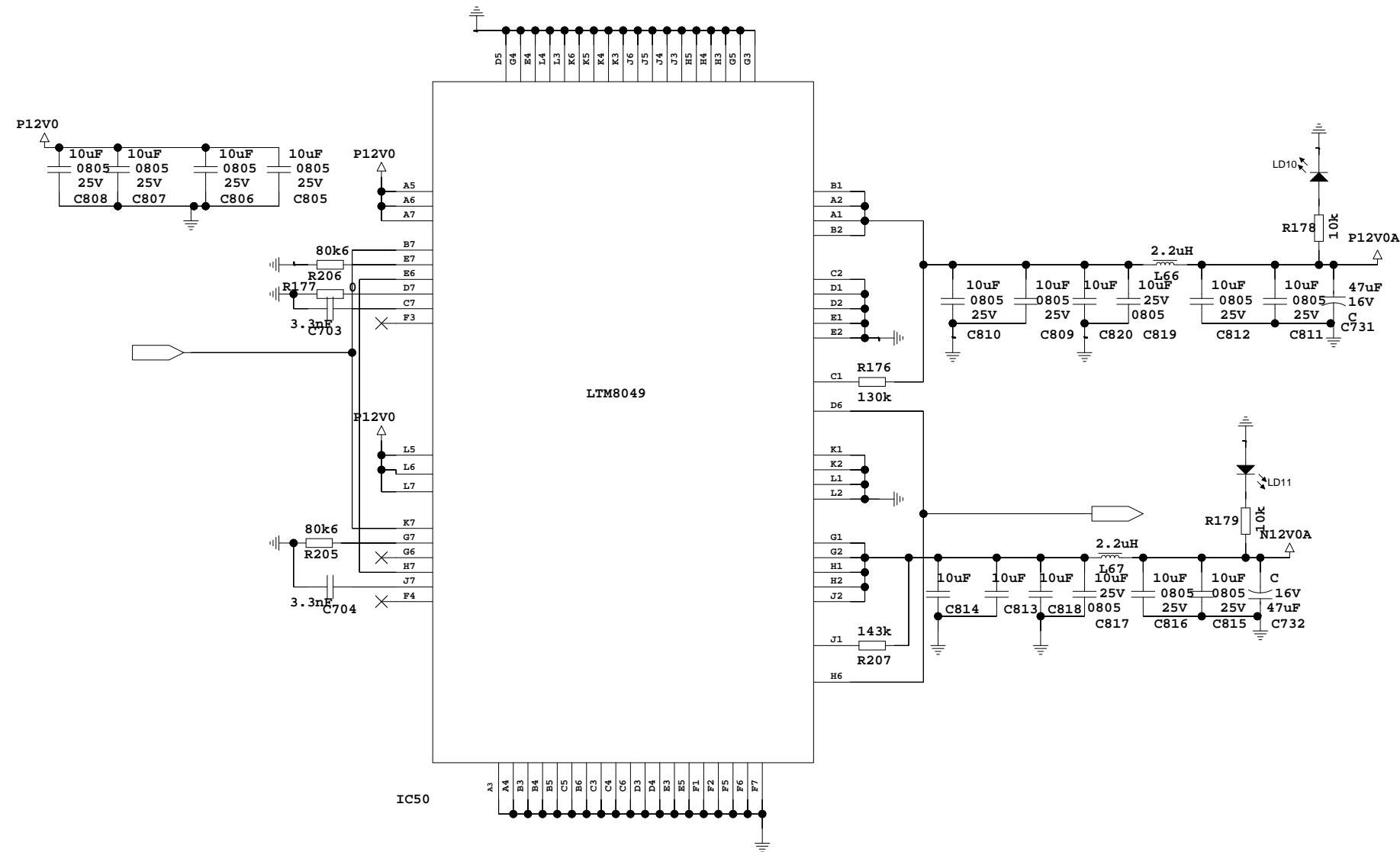
v0.95

G.K.

19

23

22/12/2016:22:39



ARTIQ Sinara

PWR_SEPIC_P12V_N12V

1

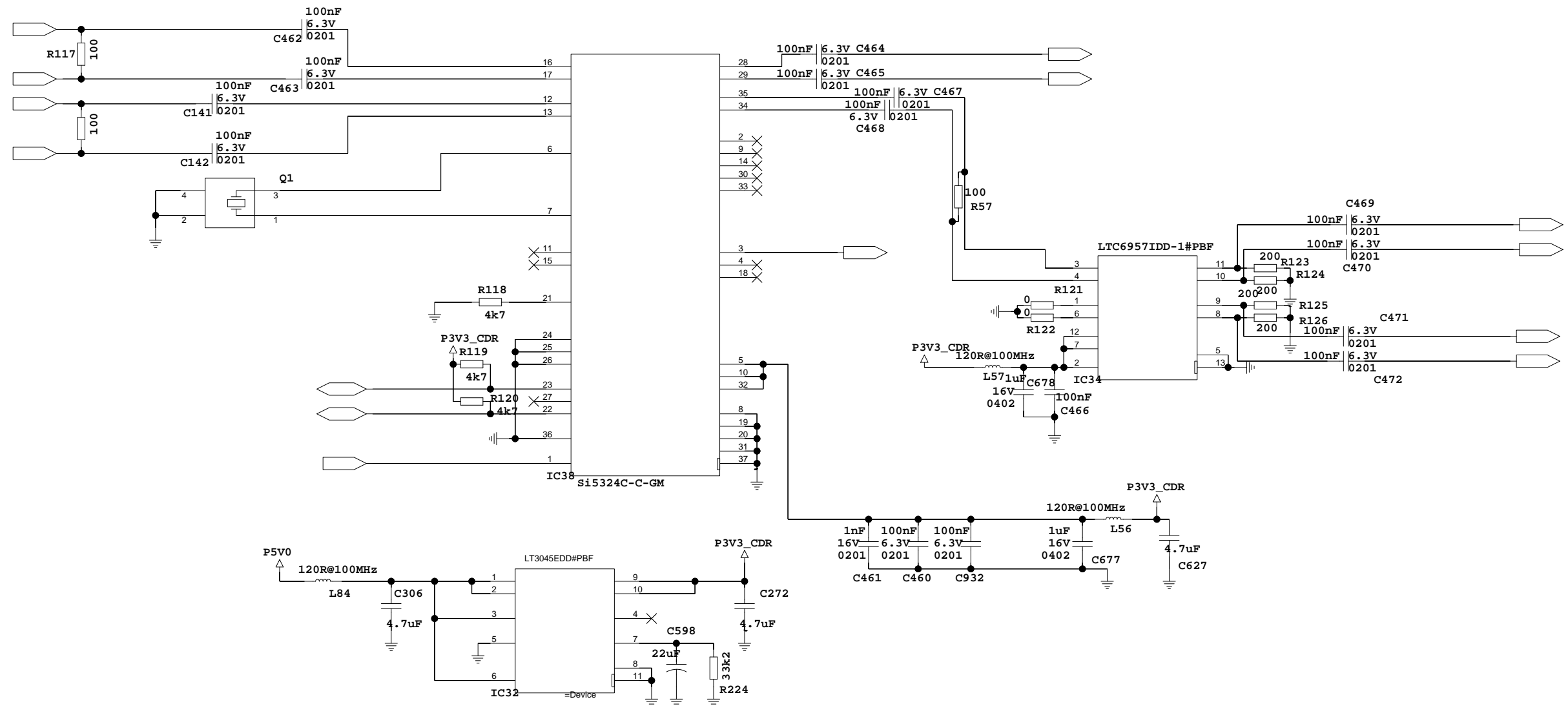
v0.95

G.K.

20

23

22/12/2016:22:39



ARTIQ Sinara

SI5324_CLK_RECOVERY

1

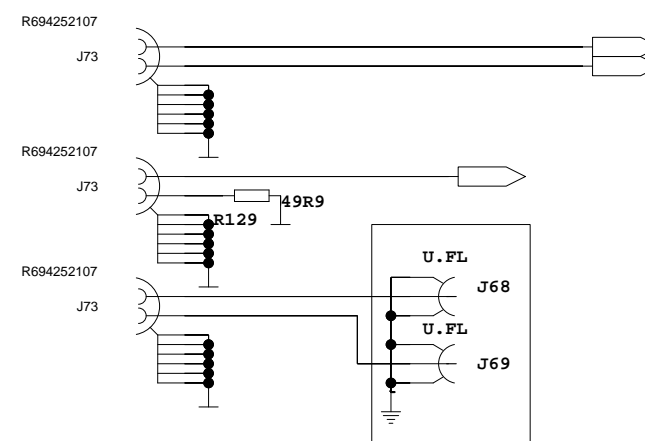
v0.95

G.K.

22

23

08/01/2017:17:21



G.K.

v0.95

23

21/12/2016:23:49

