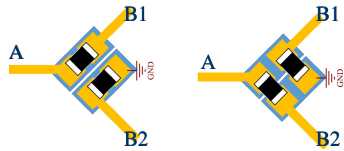
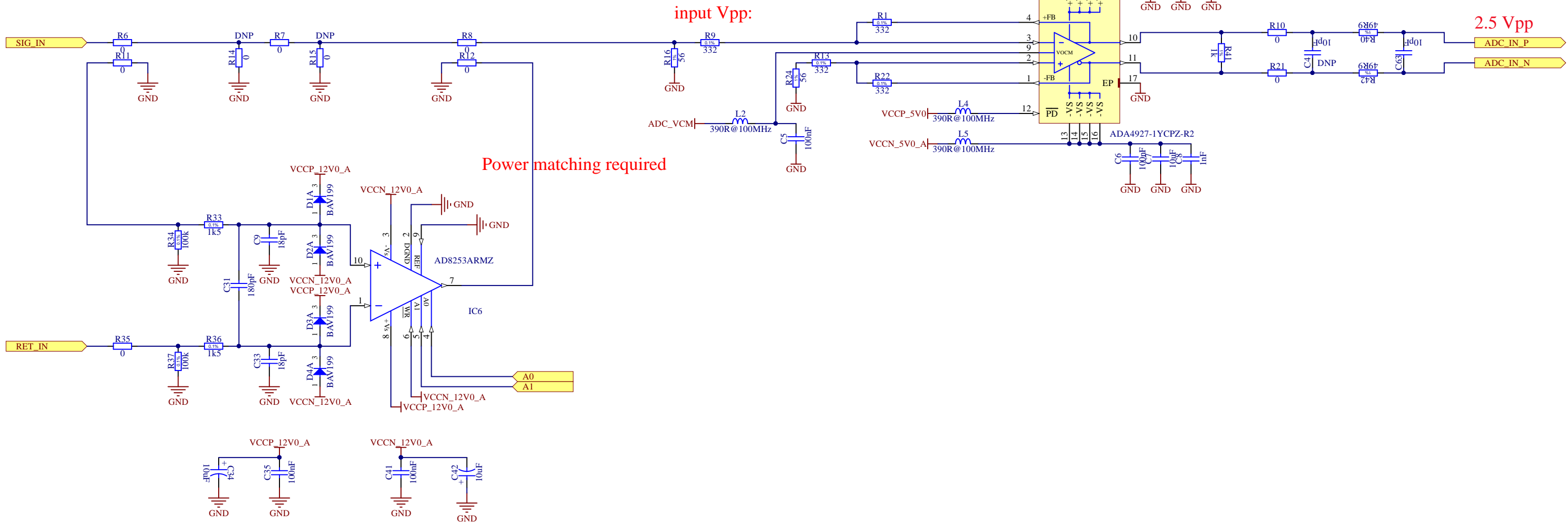



Setting 1: A-B1; B2 grounded or matched

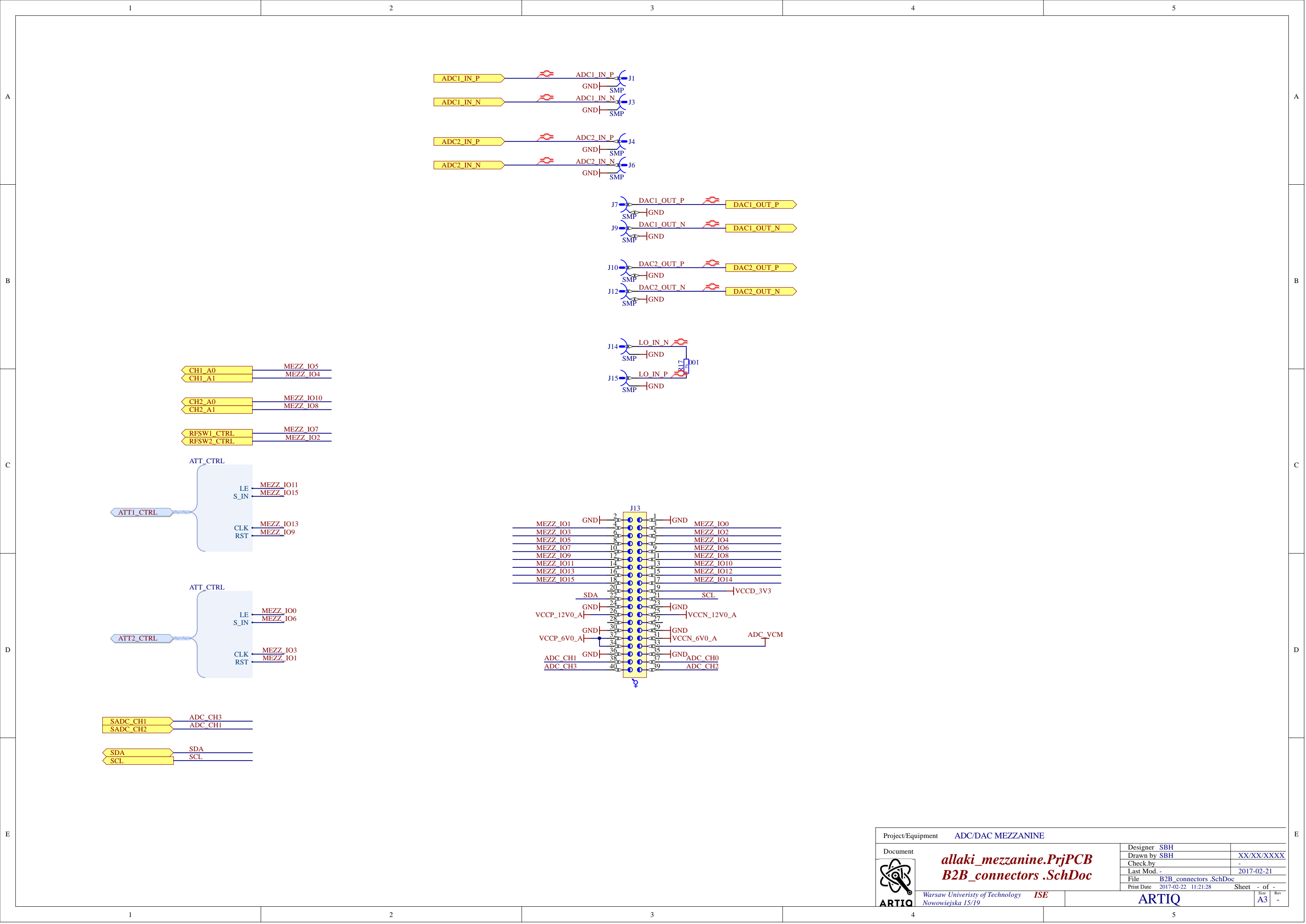
Setting 2: A-B2; B1 grounded or matched



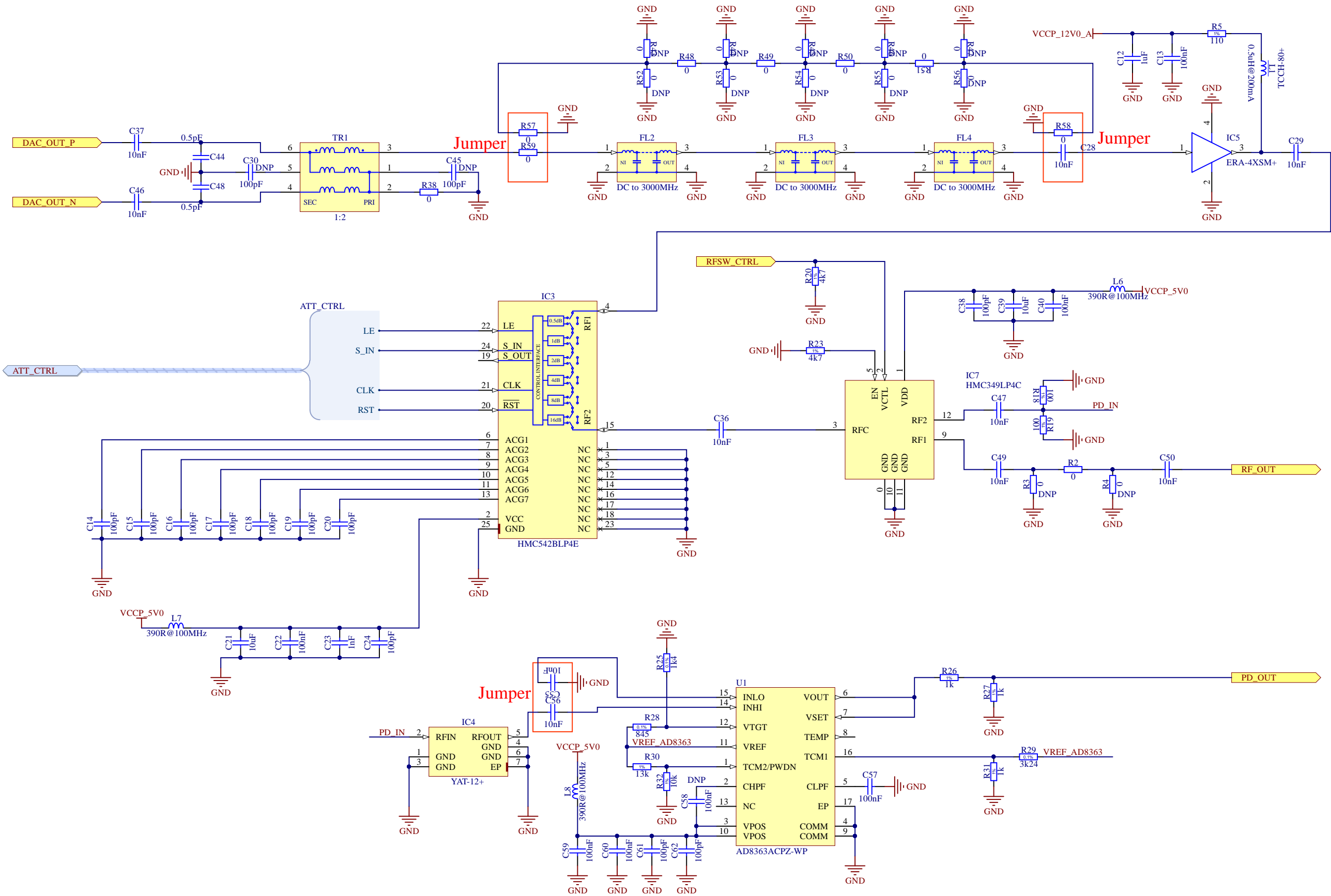
Jumper layout drawing




| | | | |
|---|---|--------------------------------|--|
| Project/Equipment | | ADC/DAC MEZZANINE | |
| Document | | Designer SBH | |
|  | <i>allaki_mezzanine.PrjPCB</i> <i>ADC_channel.SchDoc</i> | Drawn by SBH | |
| | | Check.by - | |
| | | Last Mod. - | |
| | | File ADC_channel.SchDoc | |
| | | Print Date 2017-02-22 11:21:28 | |
| Warsaw University of Technology ISE | | Sheet - of - | |
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| | | Rev - | |



discrete component filter 11th order + MWO



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|---|-----------|---|------------|
| Project/Equipment | | ADC/DAC MEZZANINE | |
| Document | | allaki_mezzanine.PrjPCB DAC_channel.SchDoc | |
|  | Designer | SBH | |
| | Drawn by | SBH | XX/XX/XXXX |
| | Check by | | |
| | Last Mod. | | 2017-02-21 |
| File | | DAC_channel.SchDoc | |
| Print Date | | 2017-02-22 11:21:28 | |
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