



ARTIQ

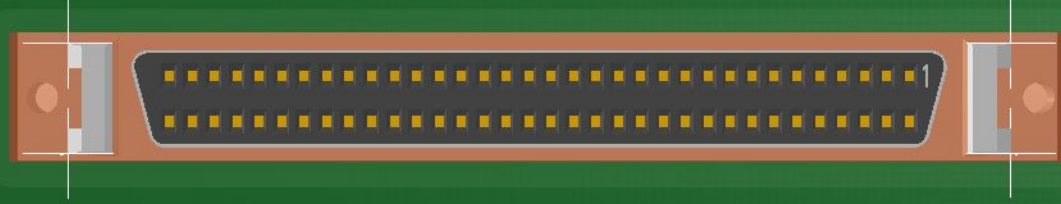


EEM 32x  
16bit DAC

TEMPERATURE



HTROK Hi Lo



MODE  
0 1 2 3

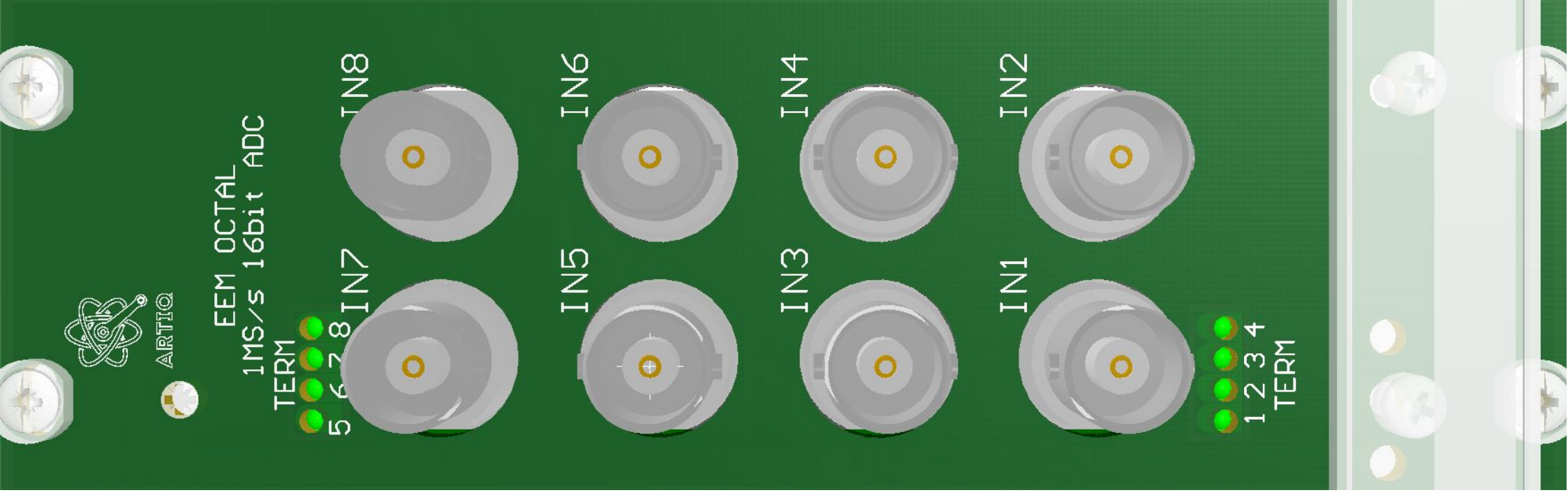


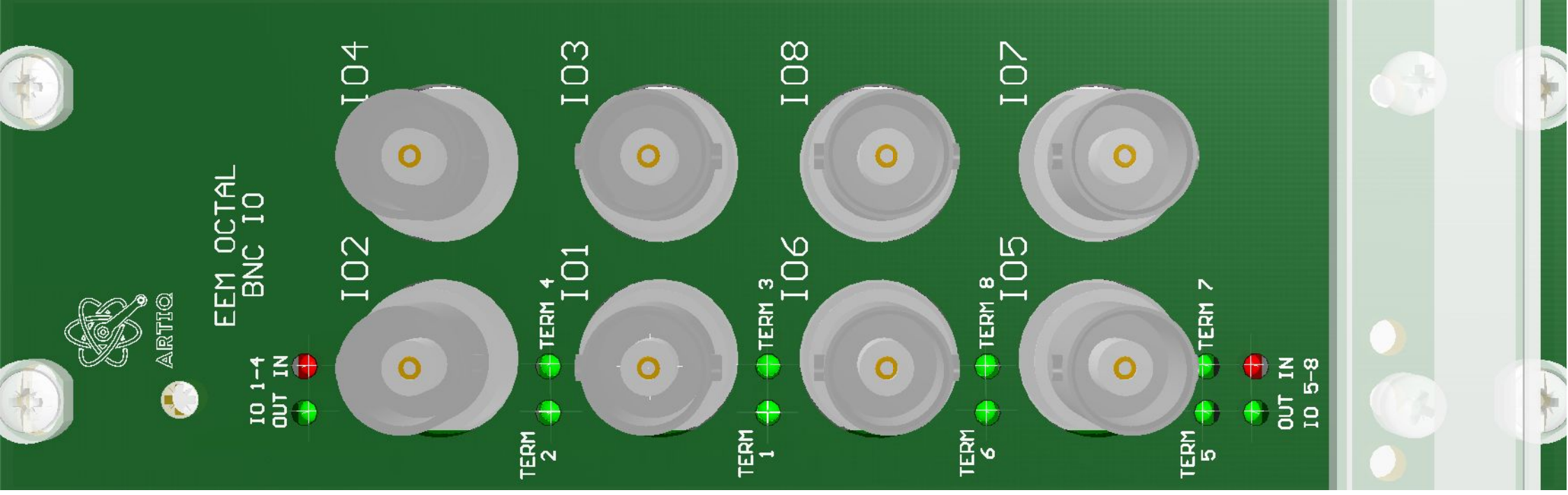
4 5 6 7



STAT









ARTIQ



EEM 16x  
LVDS

5 6 7 8

1 2 3 4

13 14 15 16

9 10 11 12

DIRECTION ON=INPUT

ONLY SHIELDED CONNECTORS AND CABLES

CH 1..4

CH 5..8

CH 9..12

CH 13..16





ARTIQ

VHDCI 8x  
EEM CARRIER



LVDS 1..4

LVDS 5..8



POWER 12V 5A





ARTIQ

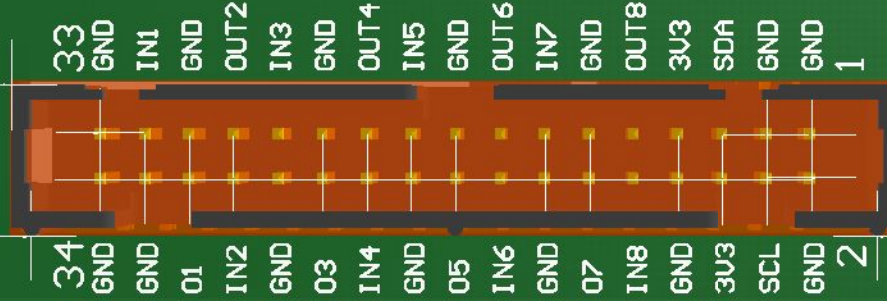


# EEM OCTAL TTL IO / TESTER

1 2 3 4

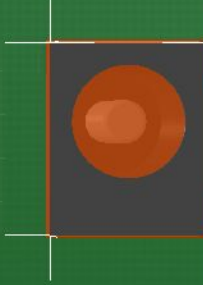


OUTPUTS ENABLED



OUTPUTS ENABLED

5 6 7 8



DC IN/OUT 12V







ARTIQ

EEM OCTAL  
SMA IO



I04

TERM



TERM



I03

IO 1-4

OUT



IN



I02

TERM



TERM



I01



I08

TERM



TERM



I07

IO 5-8

OUT



IN



I06

TERM



TERM



I05