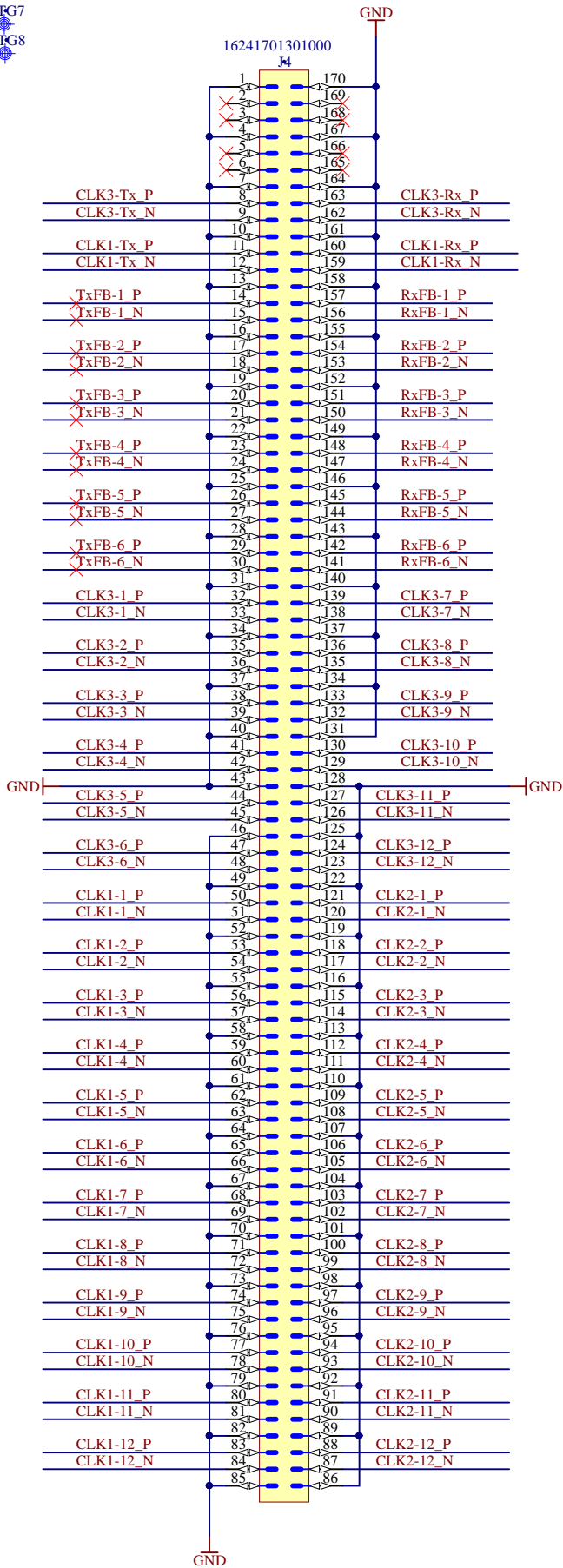


Copyright WUT 2017

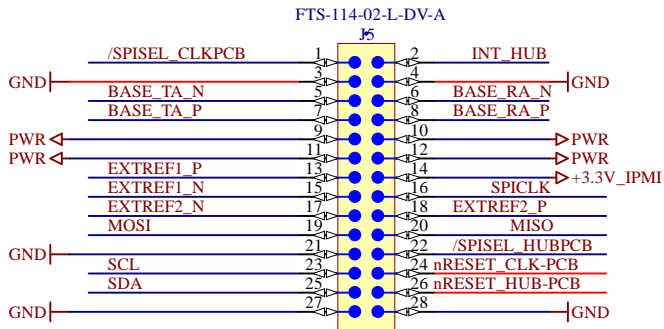
This documentation describes Open Hardware and is licensed under the CERN OHL v.1.1. You may redistribute and modify this documentation under the terms of the CERN OHL v.1.1. (<http://ohwr.org/CERNOHL>). This documentation is distributed WITHOUT ANY EXPRESS OR IMPLIED WARRANTY, INCLUDING OF MERCHANTABILITY, SATISFACTORY QUALITY AND FITNESS FOR A PARTICULAR PURPOSE. Please see the CERN OHL v.1.1 for applicable conditions.

Block

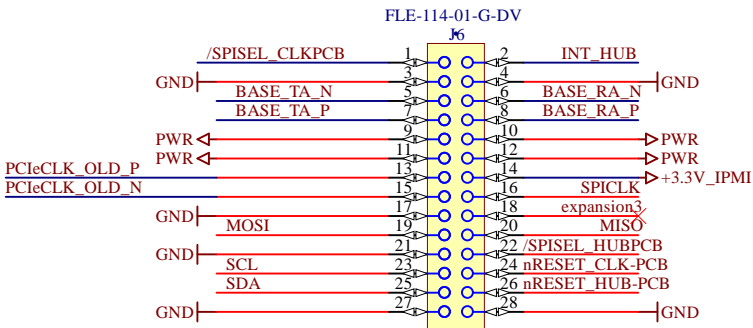
FTG5  
FTG6  
FTG7  
FTG8



### to 1st PCB



### to 3rd PCB



Project/Equipment		SINARA	
Document		Designer	G.K.
		Drawn by	G.K.
		Check by	-
Last Mod.			03.02.2017
File		PCB_MCH_mordovia.SchDoc	
Print Date		03.02.2017 01:54:41	Sheet - of -
Warsaw Univeristy of Technology Nowowiejska 15/19		ISE	Size
			A3
ARTIQ			Rev
			-

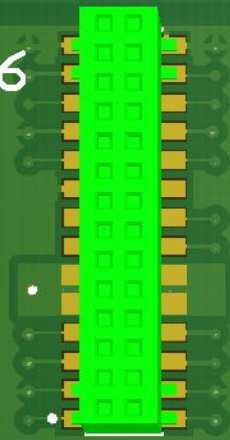
**Mordovia**  
**Tongue 2 adapter**

ARTIQ

B1



J6



J4

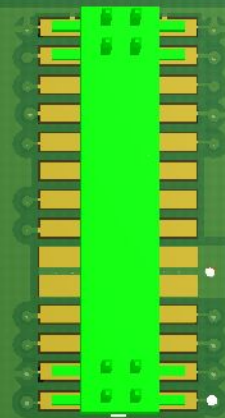


ARTIQ

PCB MCH Mordovia rev1.0



J5



ARTIQ

PCB MCH Mordovia rev1.0

B1

J6

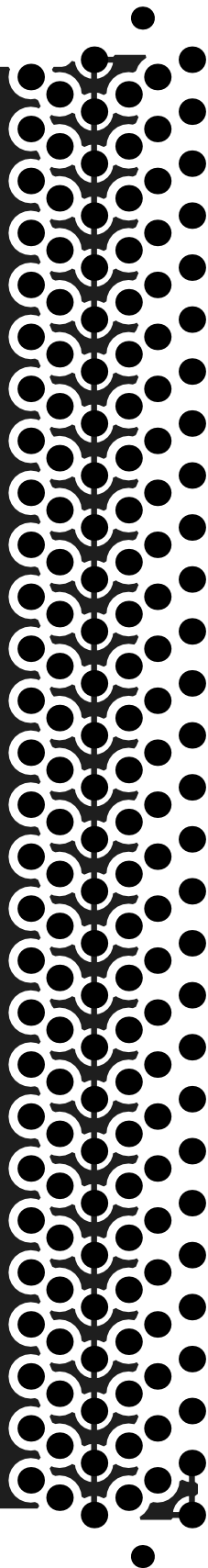
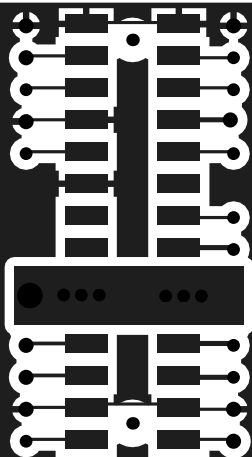
J4

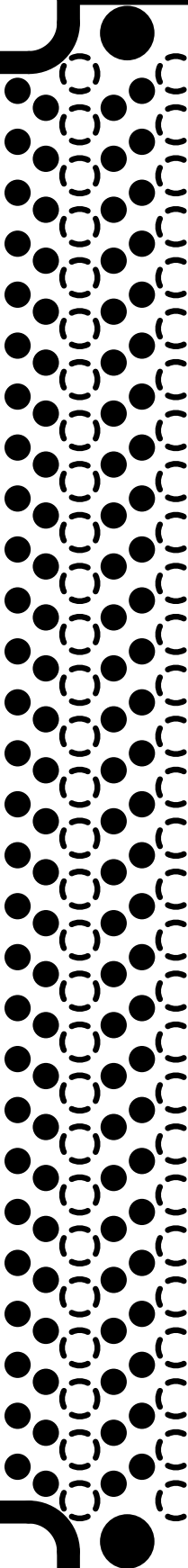
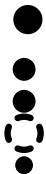
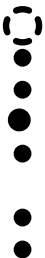
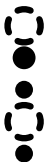
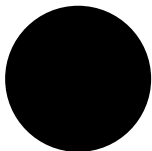


ARTIQ

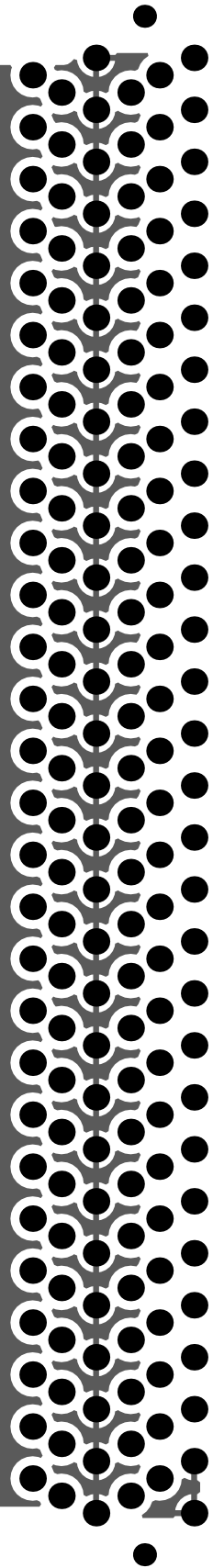
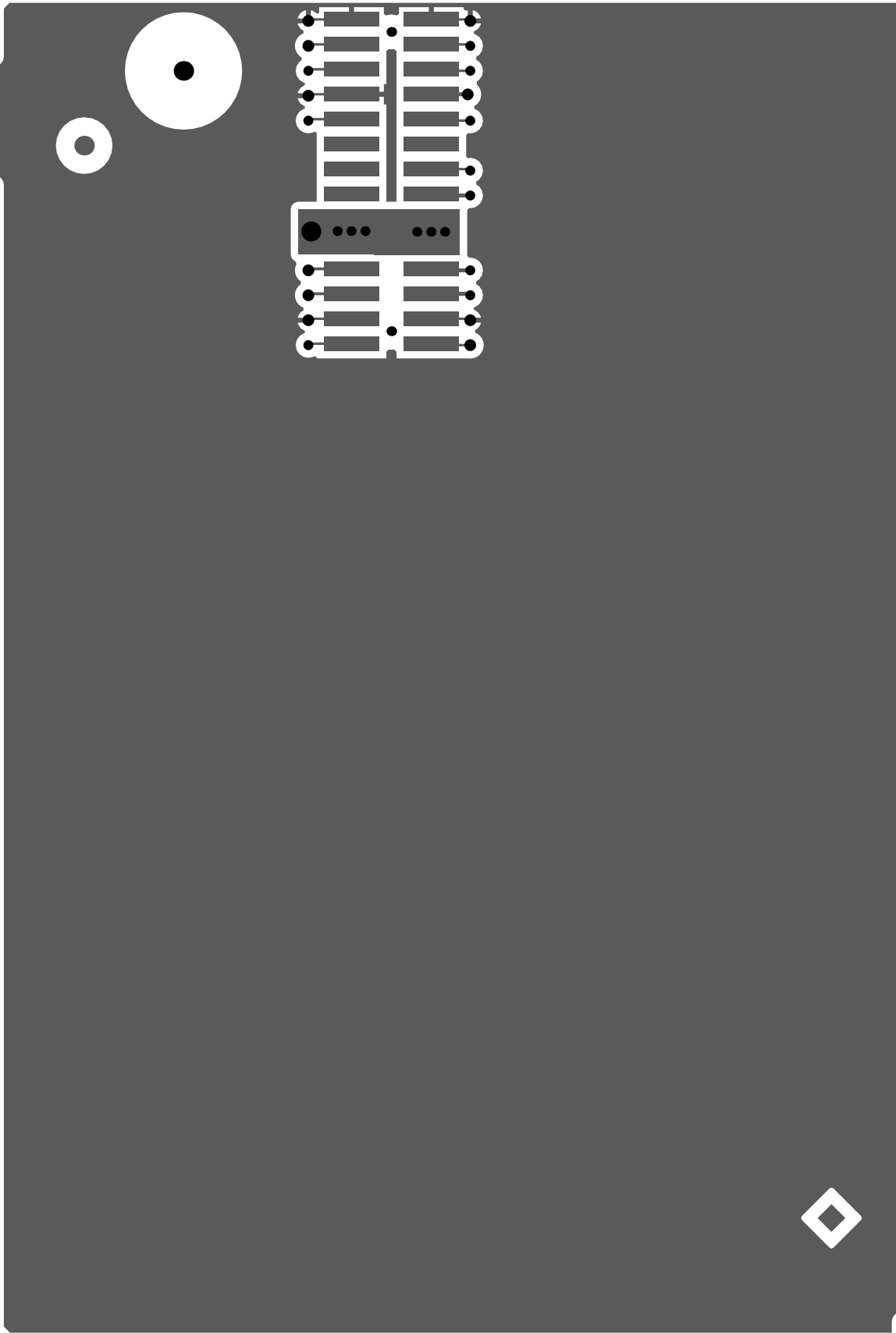
PCB MCH Mordovia rev1.0













12



ARL

PCB MCH Morpovia rev1.0