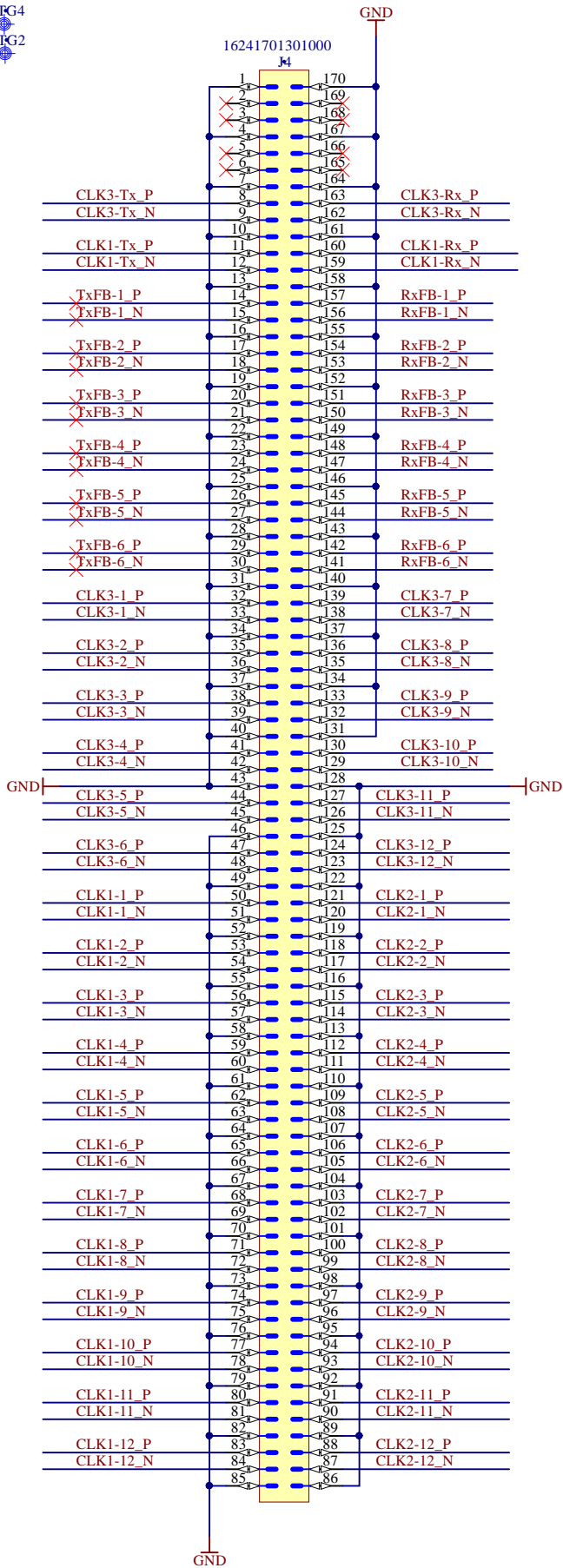
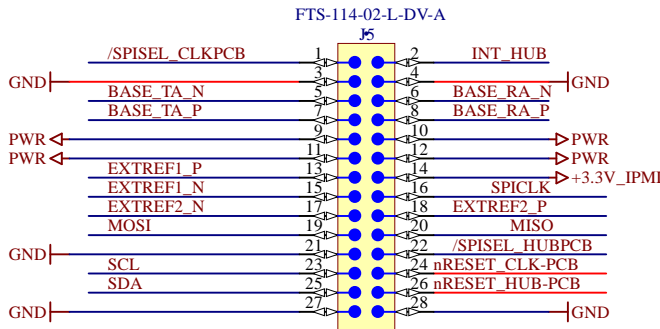


BIQ

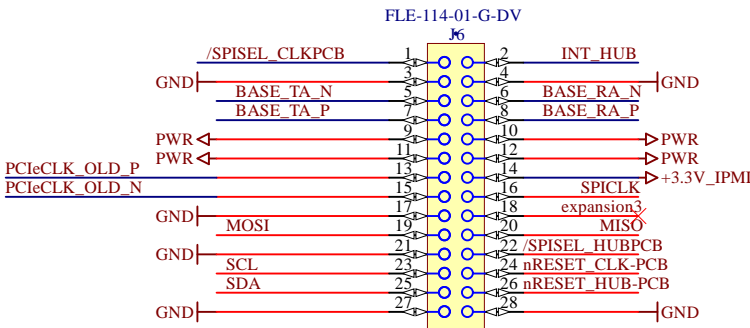
FTG1  
FTG3  
FTG4  
FTG2



### to 1st PCB



### to 3rd PCB



Project/Equipment		ADC/DAC MEZZANINE	
Document		Designer	G.K.
		Drawn by	G.K.
		Check by	-
		Last Mod.	-
		File	PCB_MCH_mordovia.SchDoc
		Print Date	21.12.2016 16:47:19
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		Size	A3
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Title  
Title2

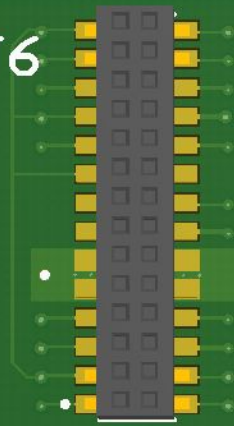
ISE

ARTIQ

B1



J6



J4

