

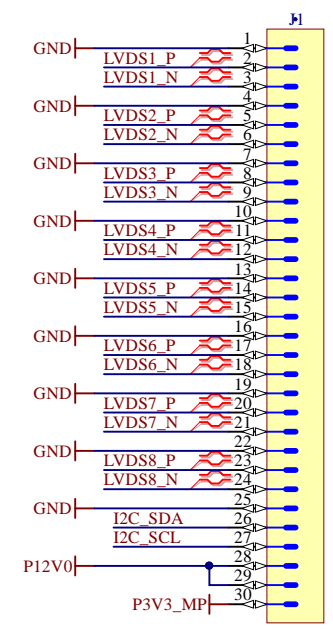
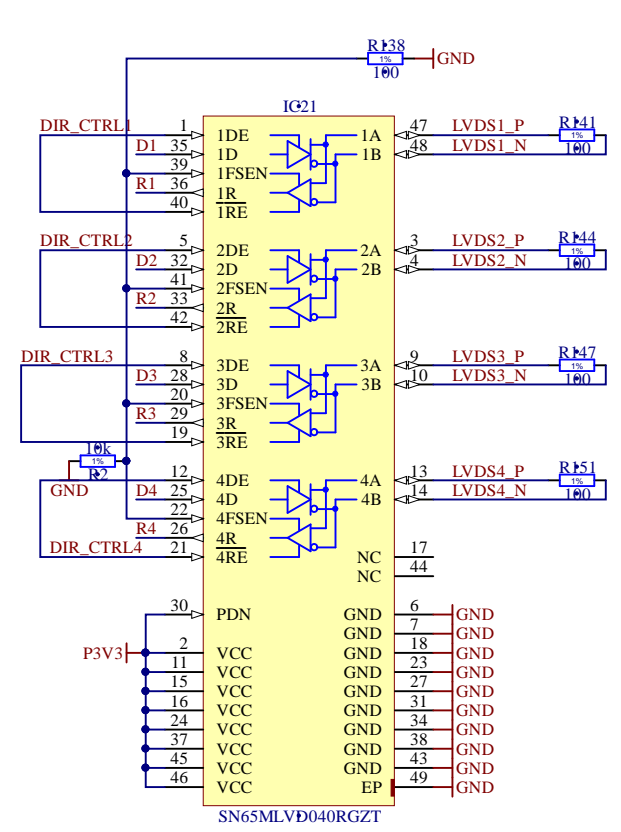
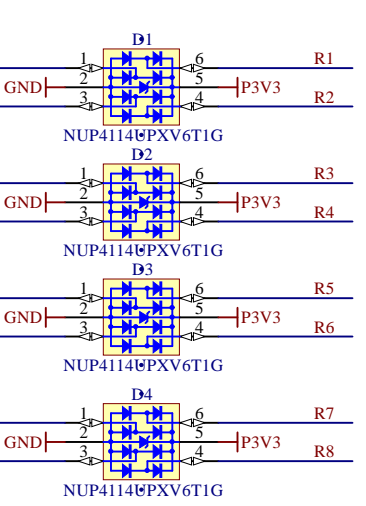
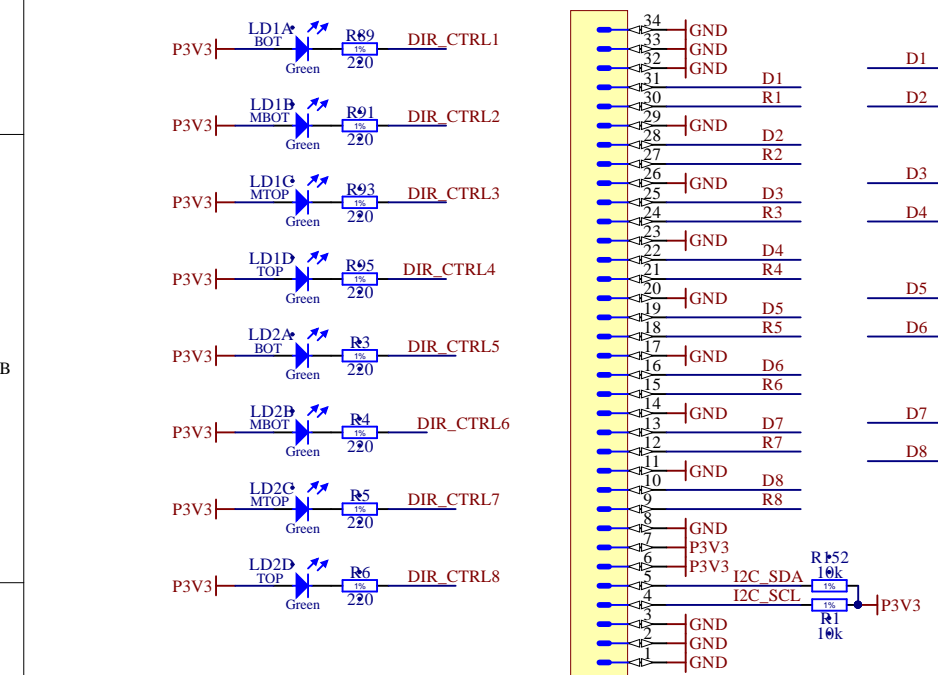
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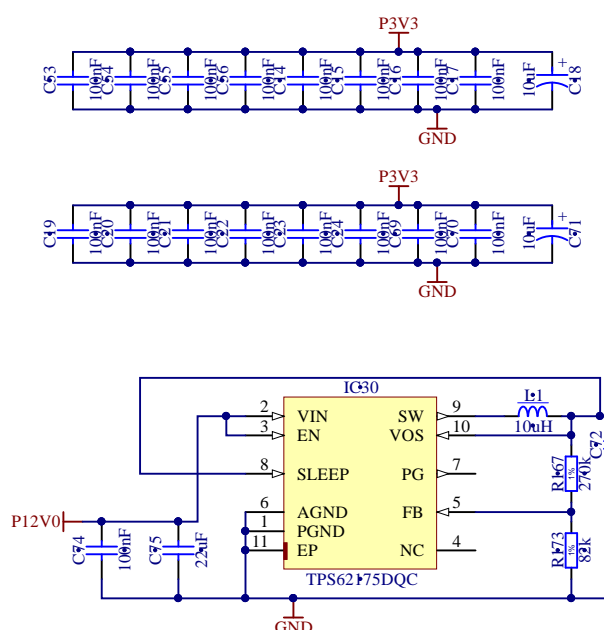
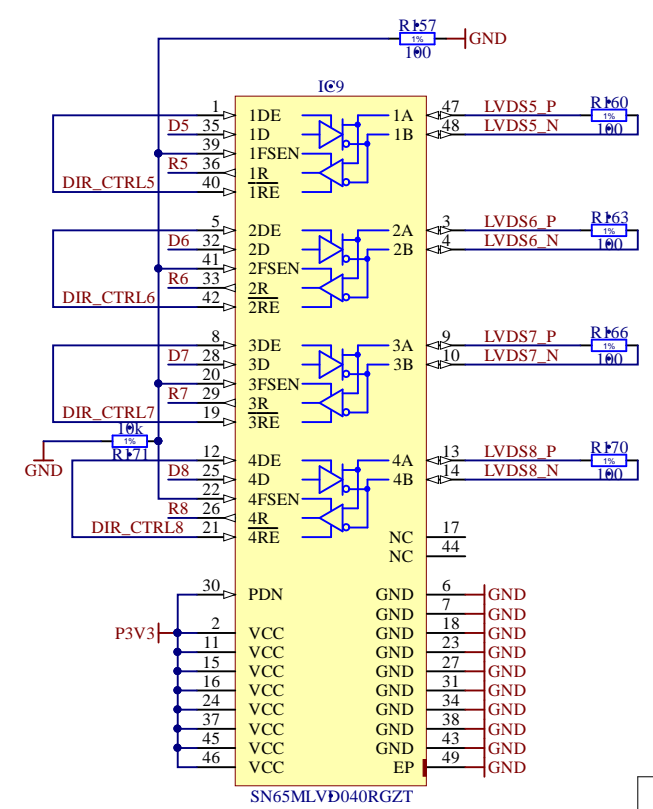
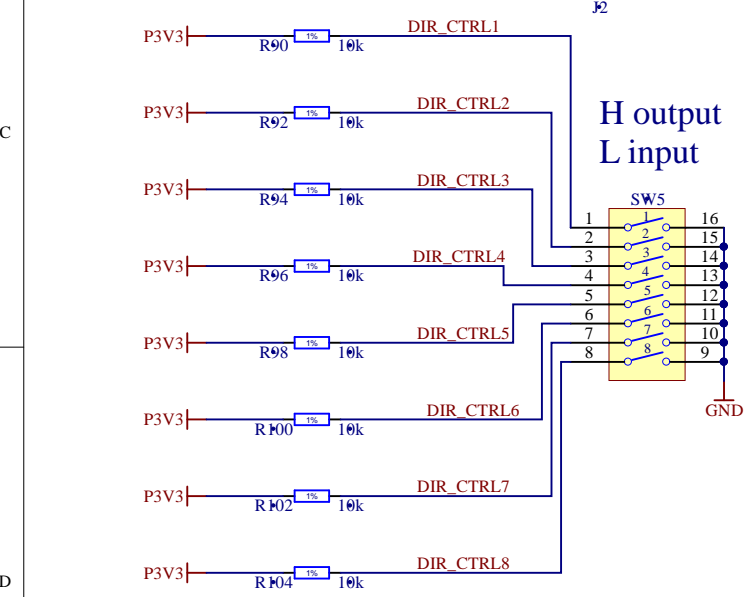
C

D

E



DIRECTION



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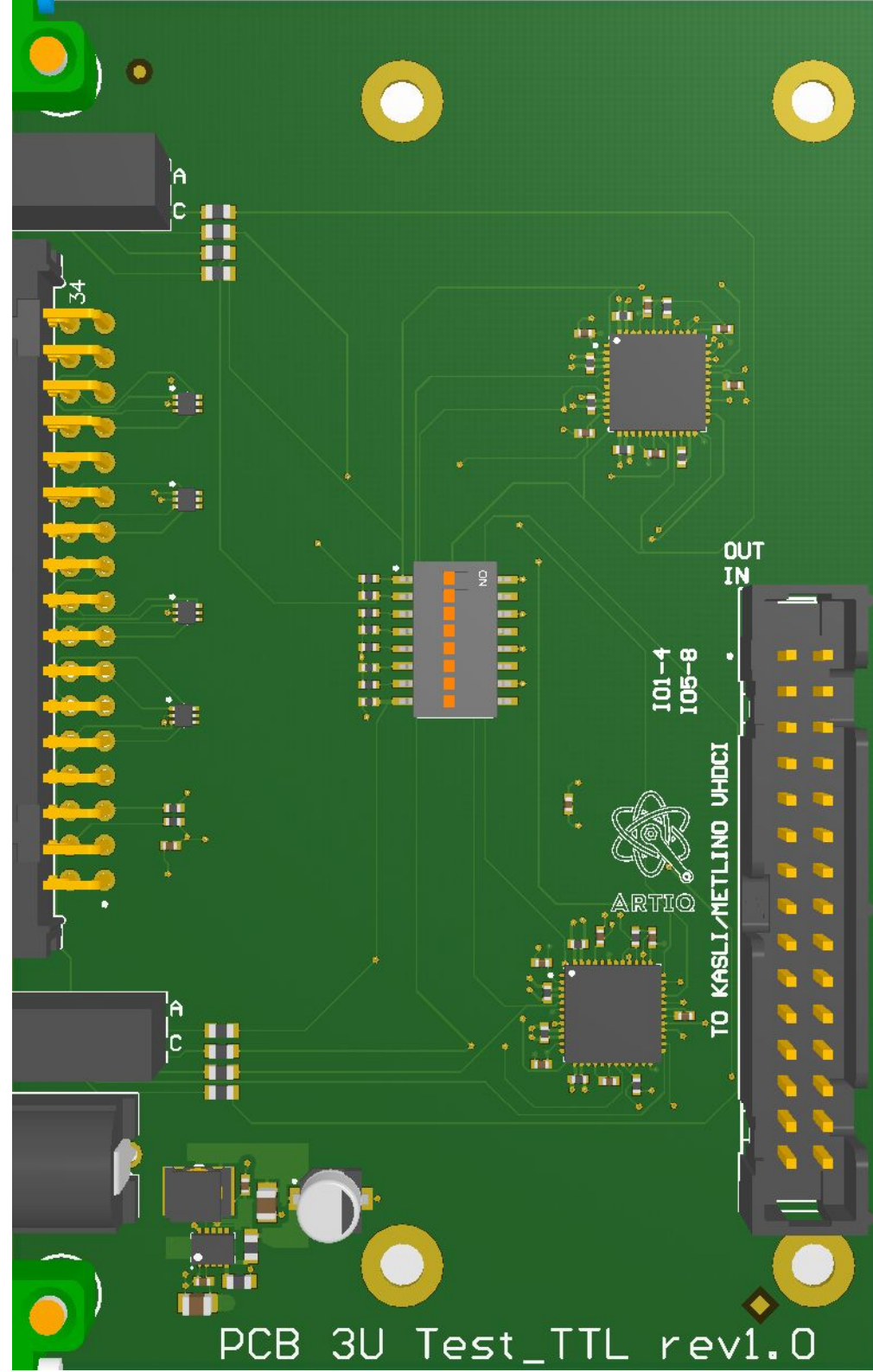
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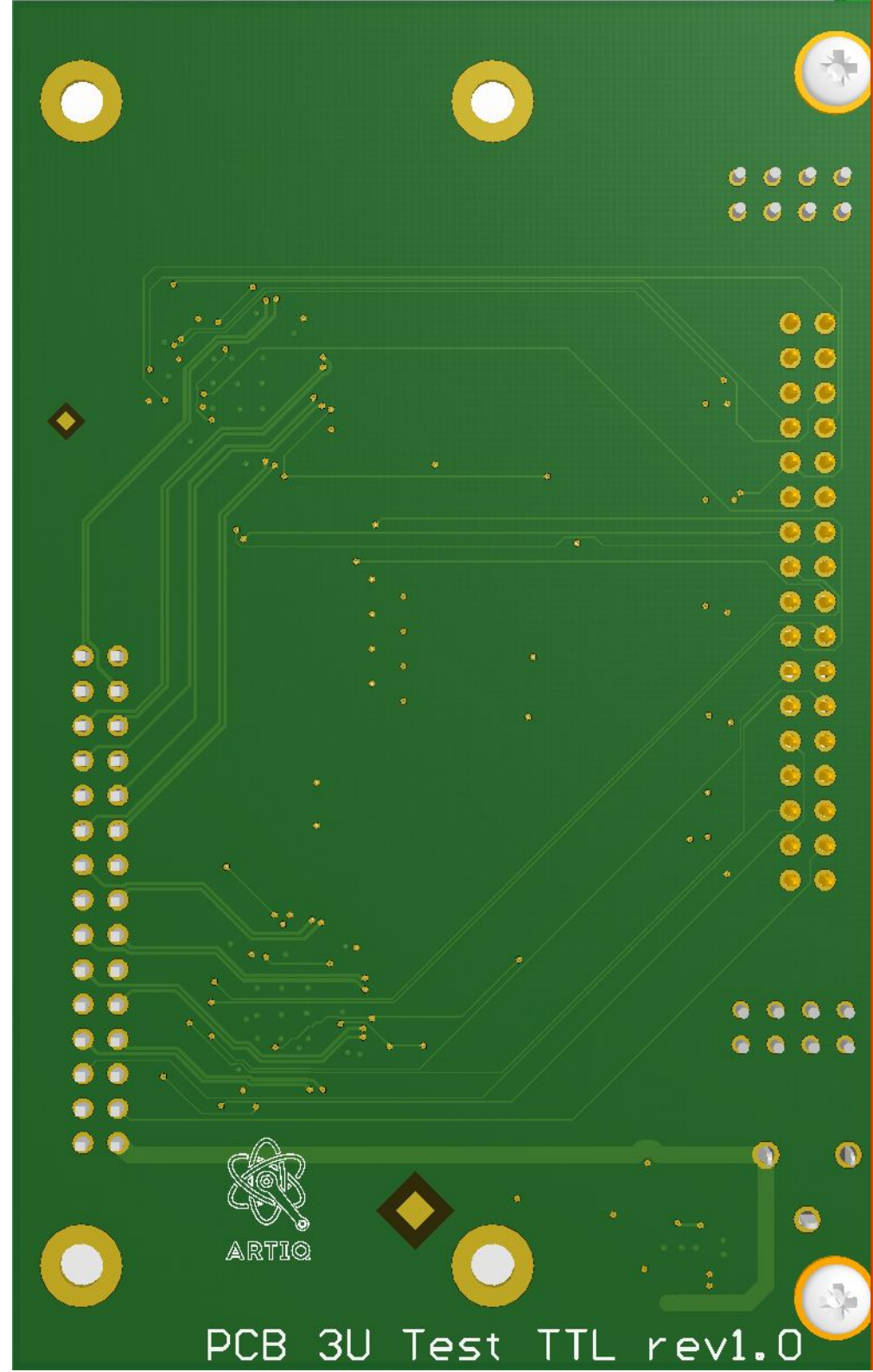
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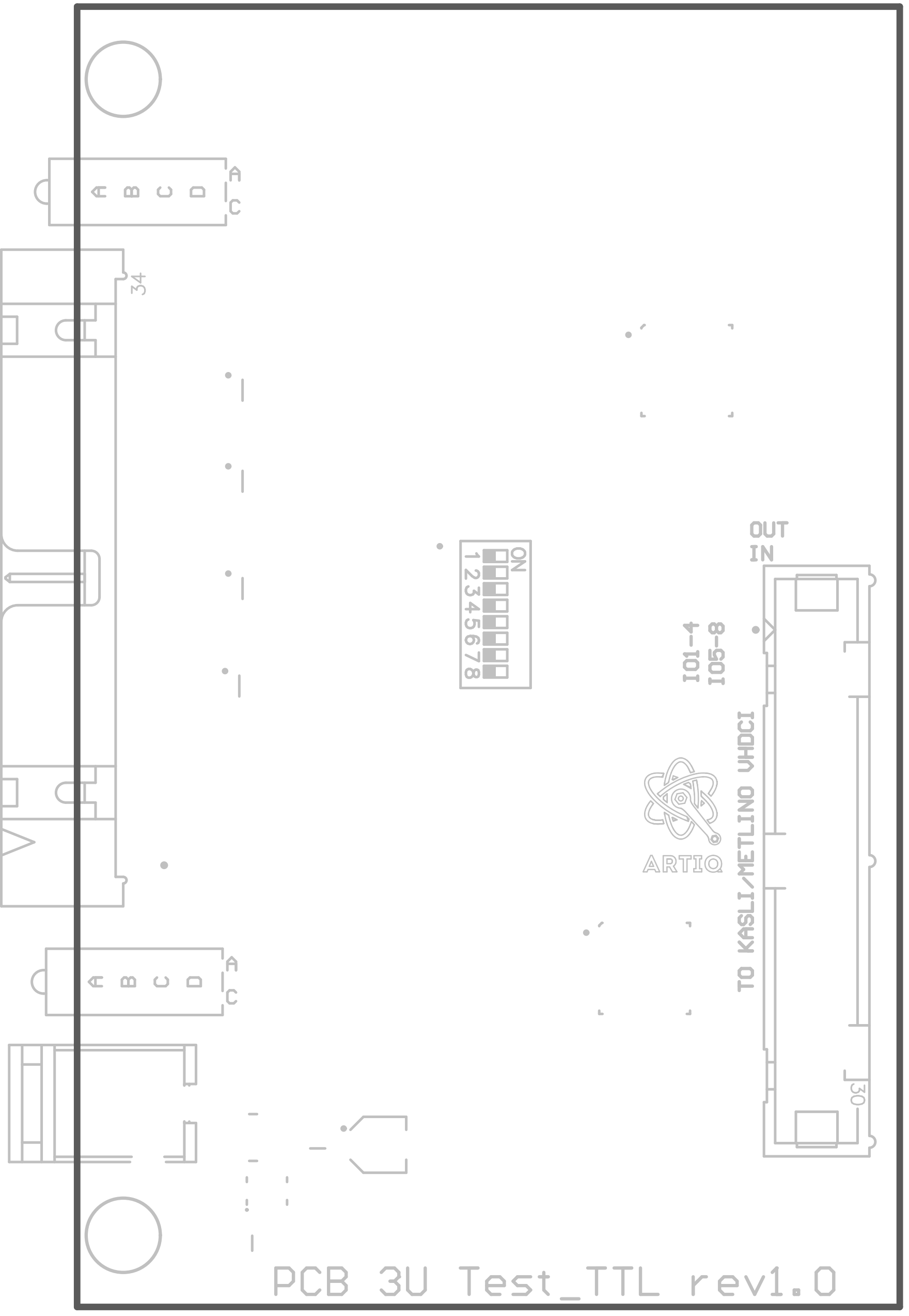
Warsaw University of Technology ISE  
Nowowiejska 15/19

ARTIQ





PCB 3U Test TTL rev1.0



PCB 3U Test\_TTL rev1.0

