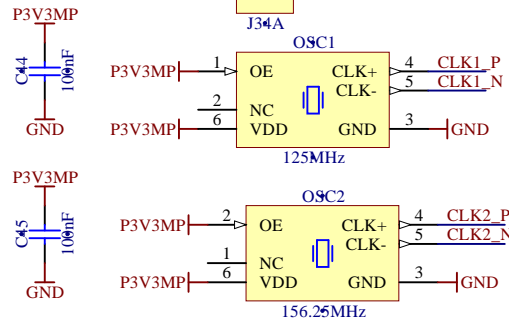
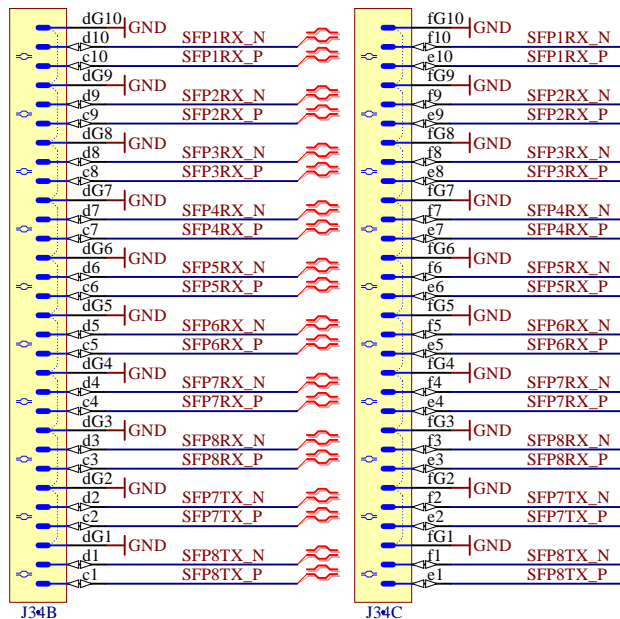
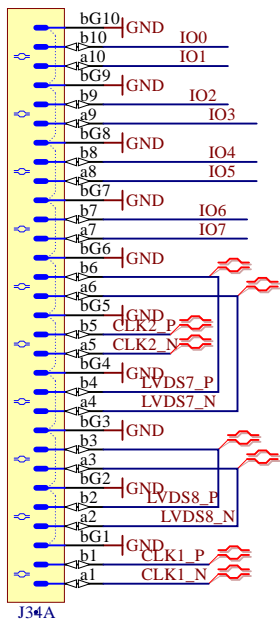
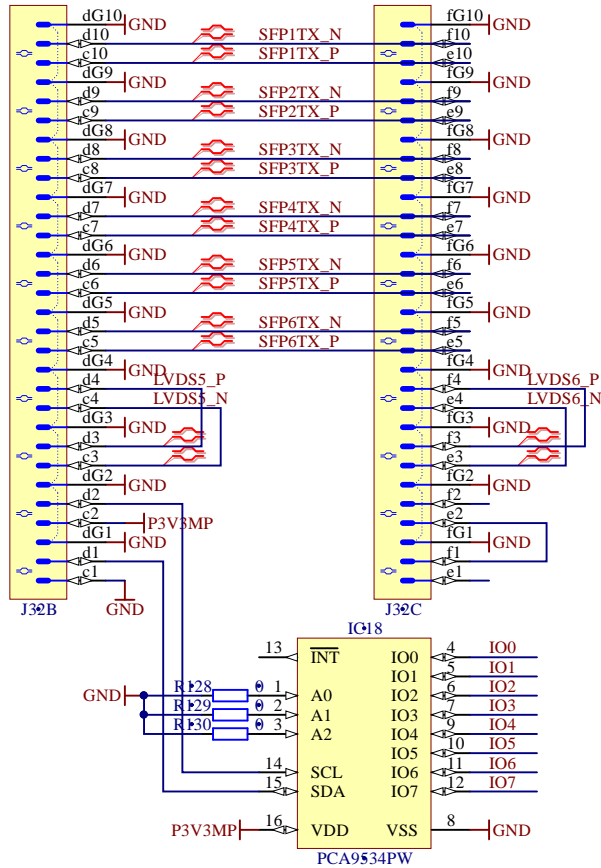
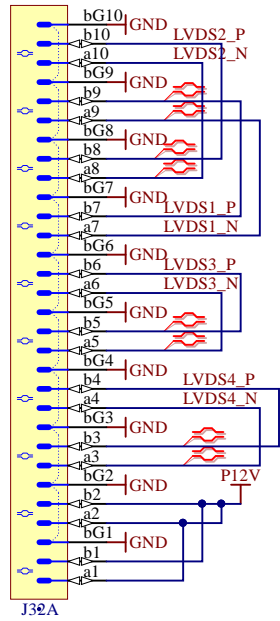


FT613  
FT614  
FT615  
FT616



Copyright WUT 2017

This documentation describes Open Hardware and is licensed under the CERN OHL v.1.1. You may redistribute and modify this documentation under the terms of the CERN OHL v.1.1. (<http://ohwr.org/CERNOHL>). This documentation is distributed WITHOUT ANY EXPRESS OR IMPLIED WARRANTY, INCLUDING OF MERCHANTABILITY, SATISFACTORY QUALITY AND FITNESS FOR A PARTICULAR PURPOSE. Please see the CERN OHL v.1.1 for applicable conditions.

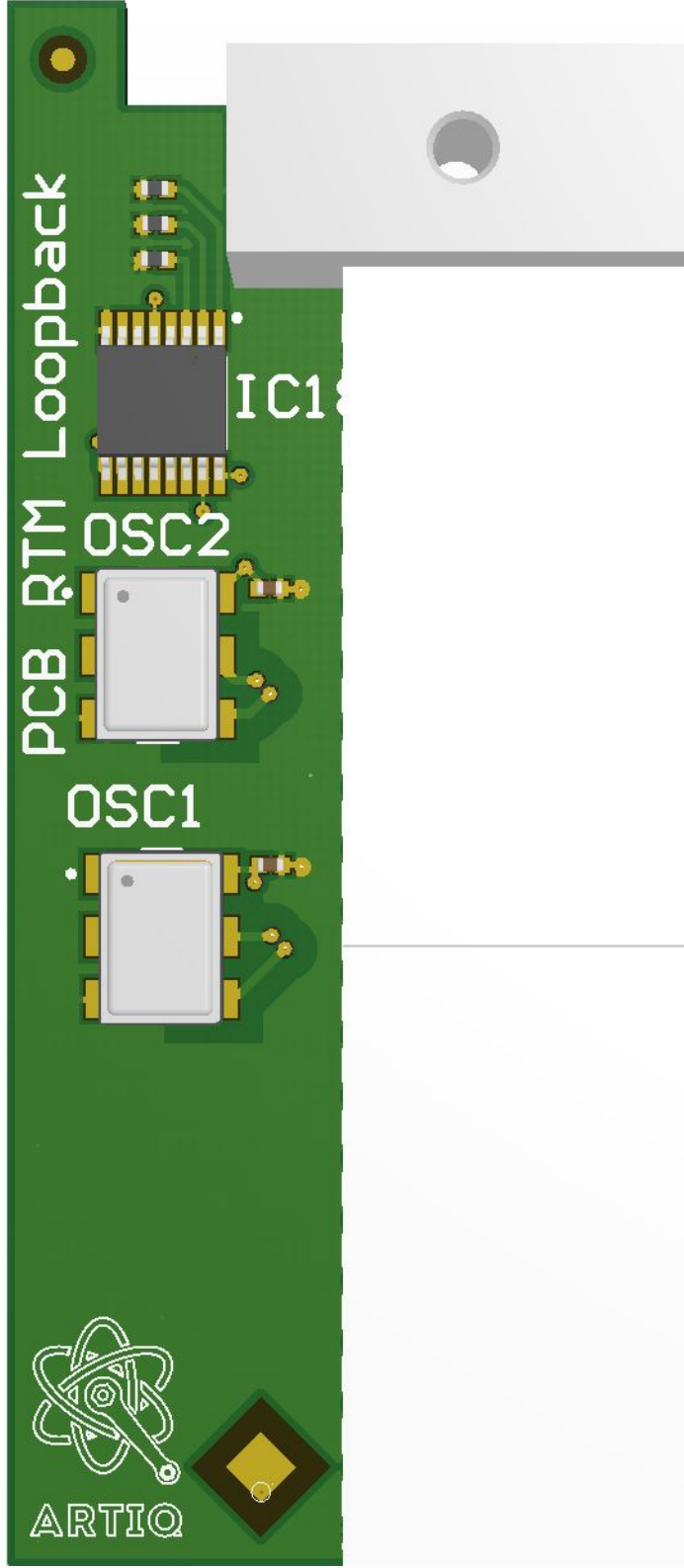
Project/Equipment		SINARA	
Document		Designer	G.K.
		Drawn by	G.K.
		Check by	-
		Last Mod.	-
		File	PCB_RTM_Loopback.SchDoc
		Print Date	03.02.2017 01:55:23
		Sheet	- of -
		Size	A3
		Rev	-



**RTM Loopback  
Test module**

Warsaw Univeristy of Technology **ISE**  
Nowowiejska 15/19

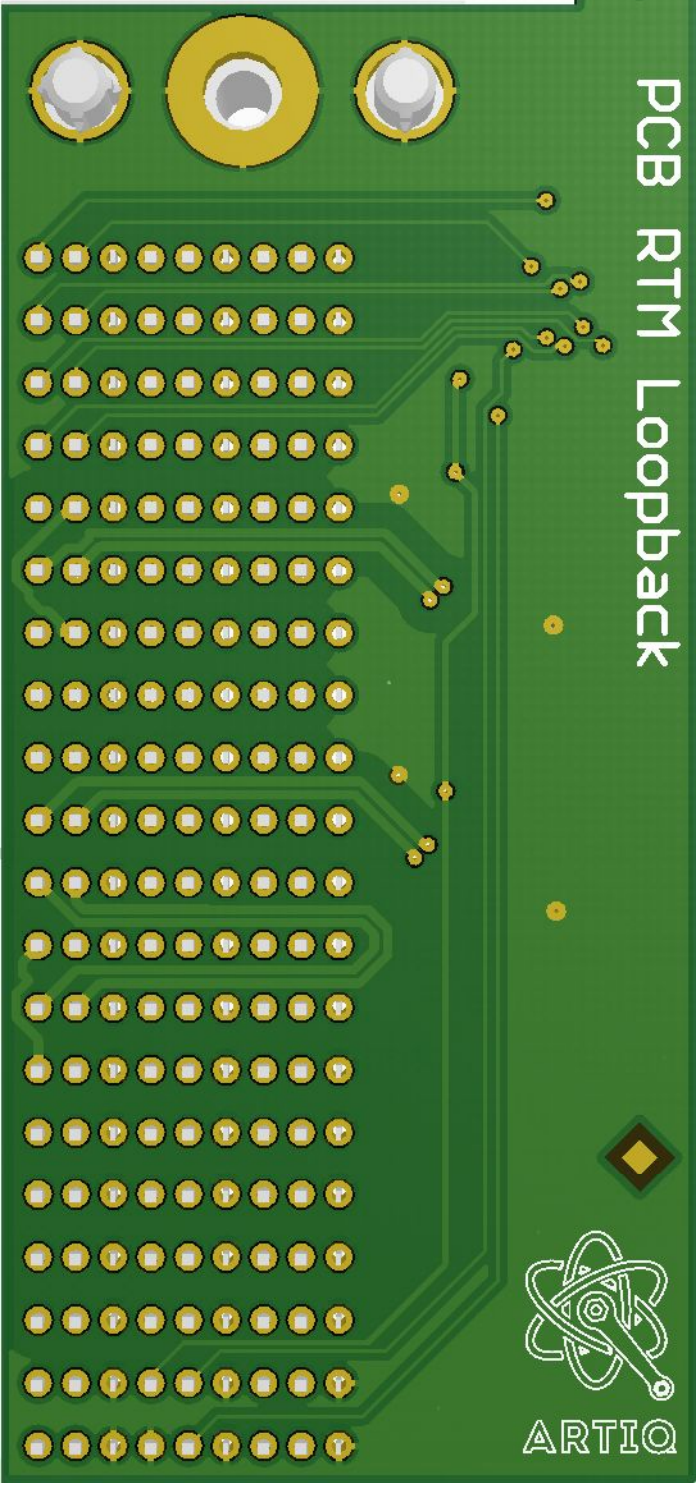
**ARTIQ**



PCB RTM Loopback



ARTIQ





ARTIQ

PCB RTM Loopback

OSC1

OSC2

IC18

A1

A10 A1

A10

Z32



