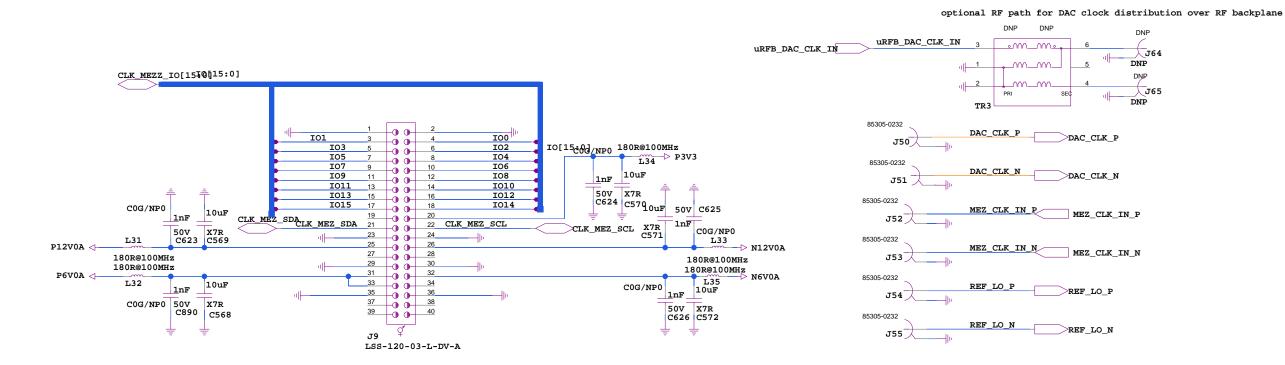


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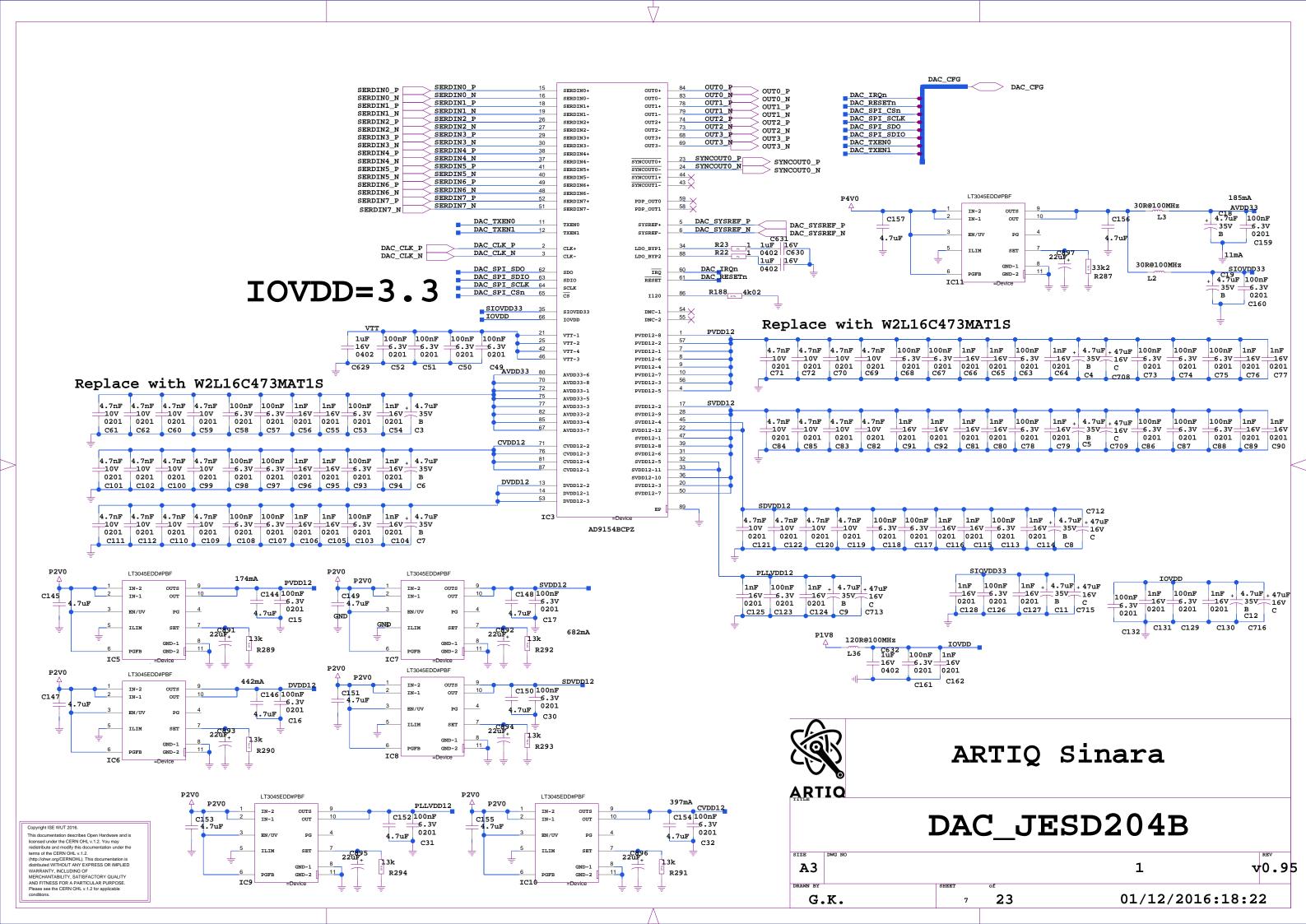


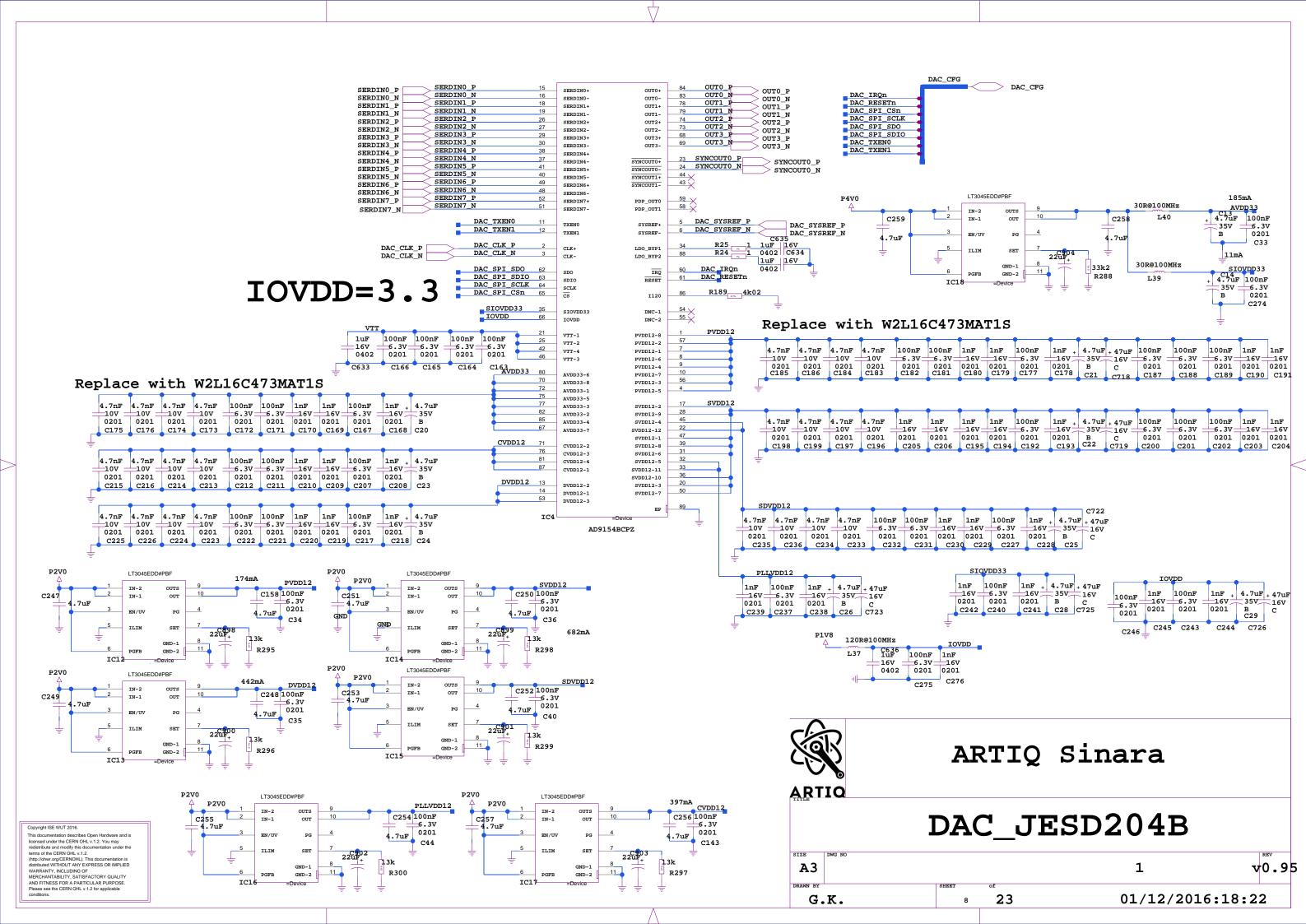
- +12VDC @ 200 mA, max 1 mV p-p noise in 20 Hz-20 MHz bandwidth
- -12VDC @ 50 mA, max 1 mV p-p noise in 20 Hz-20 MHz bandwidth
- +6VDC @ 1.5 A, max 1 mV p-p noise in 20 Hz-20 MHz bandwidth
- -6VDC rail @ 100 mA,max 1 mV p-p noise in 20 Hz-20 MHz bandwidth
- +3.3VDC @ 1 A, max 10 mV p-p noise in 20 Hz-20 MHz

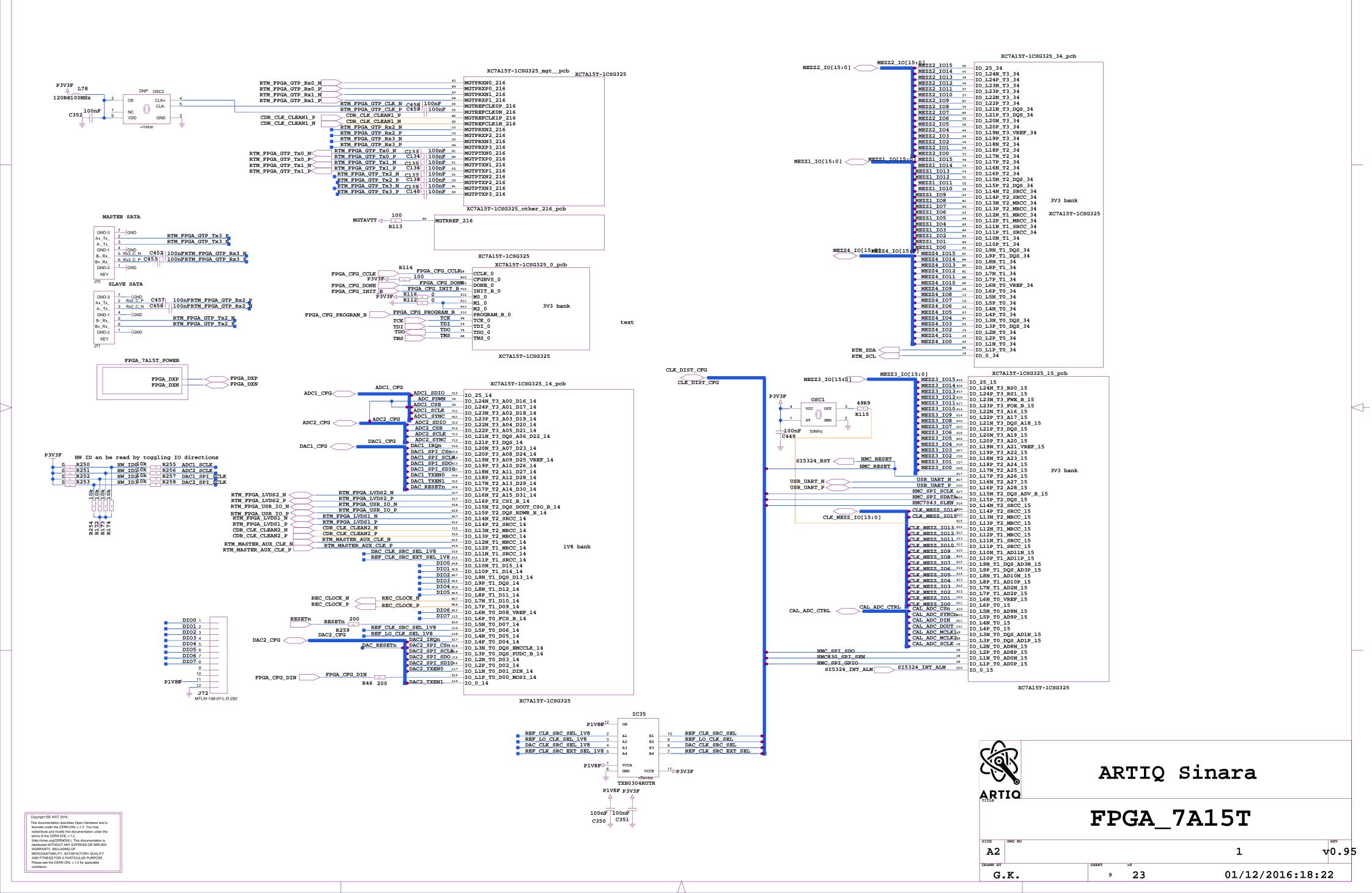


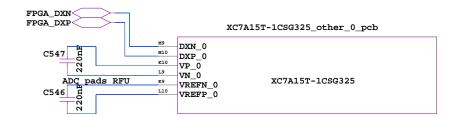
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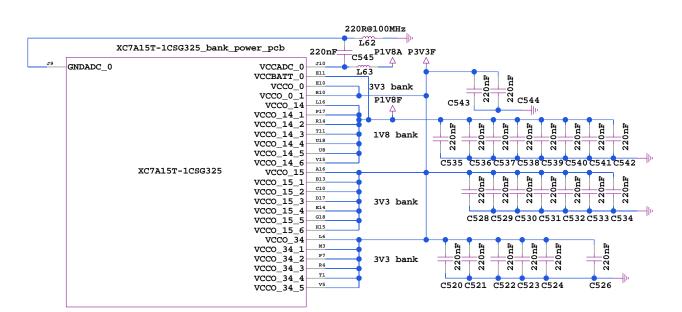
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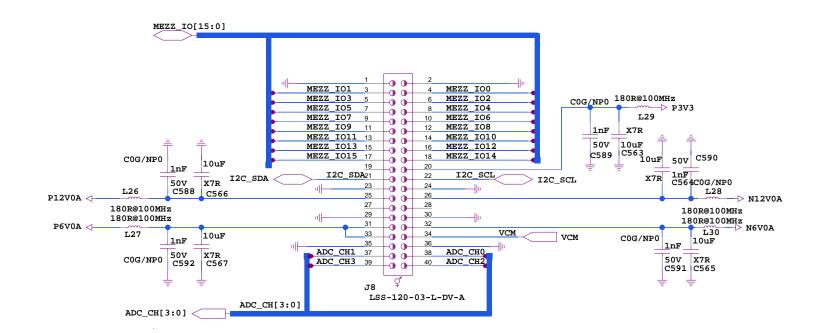
ARTIQ Sinara

FPGA\_7A15T\_POWER

FPGA\_7A15T\_POWER

SIZE DAY 10 DAY





+12VDC @ 200 mA, max 1 mV p-p noise in 20 Hz-20 MHz bandwidth

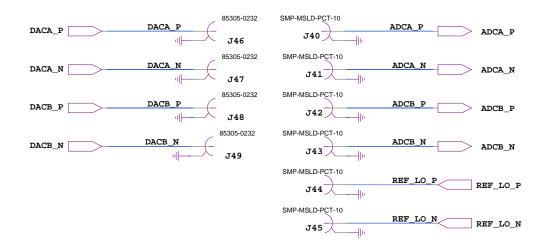
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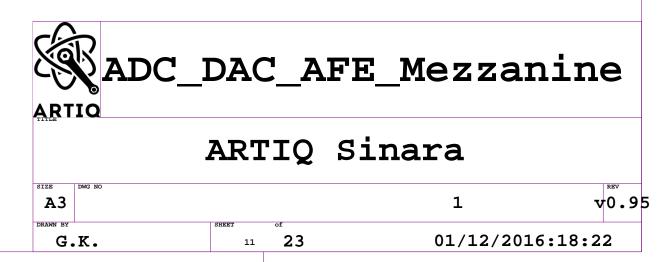
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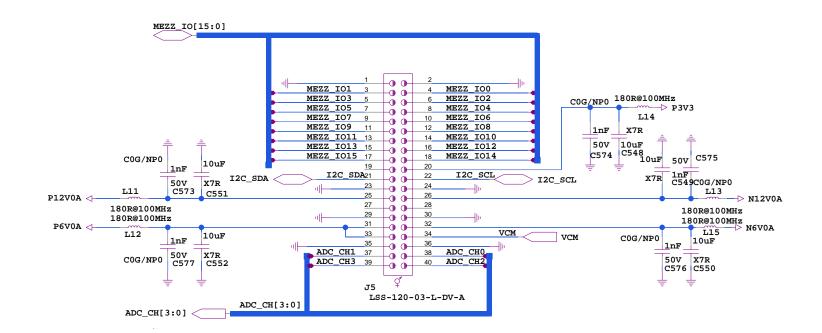
-6VDC rail @ 100 mA, max 1 mV p-p noise in 20 Hz-20 MHz bandwidth

+3.3VDC @ 1 A, max 10 mV p-p noise in 20 Hz-20 MHz

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+12VDC @ 200 mA, max 1 mV p-p noise in 20 Hz-20 MHz bandwidth

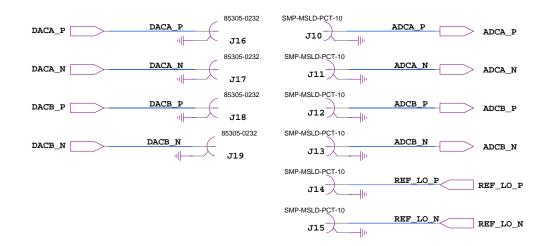
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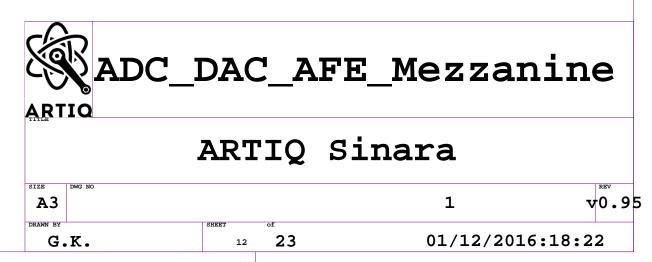
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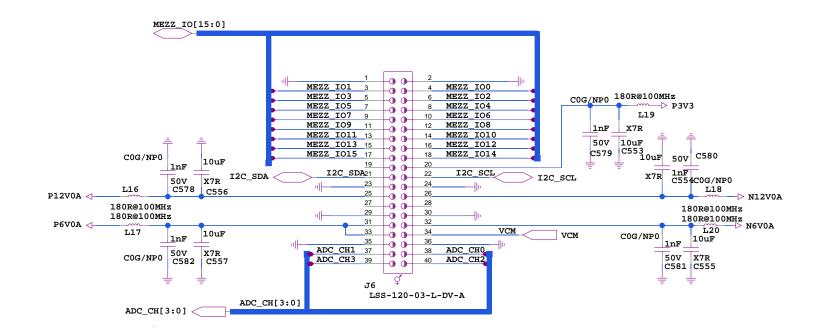
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+3.3VDC @ 1 A, max 10 mV p-p noise in 20 Hz-20 MHz

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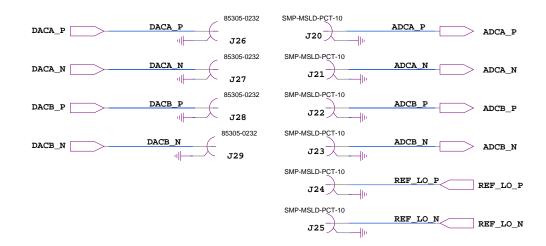


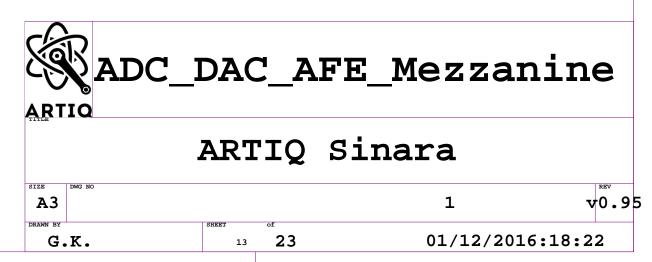


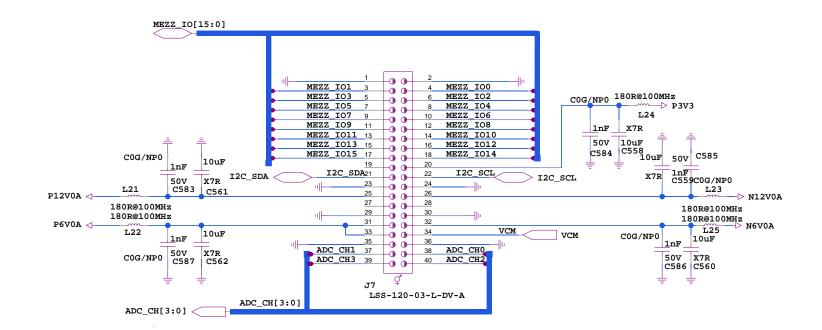


- +12VDC @ 200 mA, max 1 mV p-p noise in 20 Hz-20 MHz bandwidth
- -12VDC @ 50 mA, max 1 mV p-p noise in 20 Hz-20 MHz bandwidth
- +6VDC @ 1.5 A, max 1 mV p-p noise in 20 Hz-20 MHz bandwidth
- -6VDC rail @ 100 mA, max 1 mV p-p noise in 20 Hz-20 MHz bandwidth
- +3.3VDC @ 1 A, max 10 mV p-p noise in 20 Hz-20 MHz

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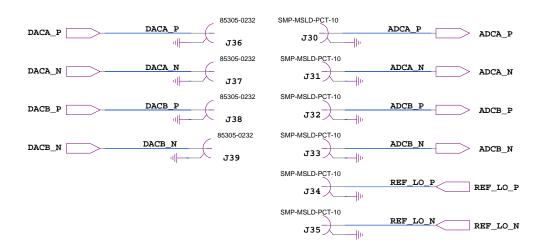


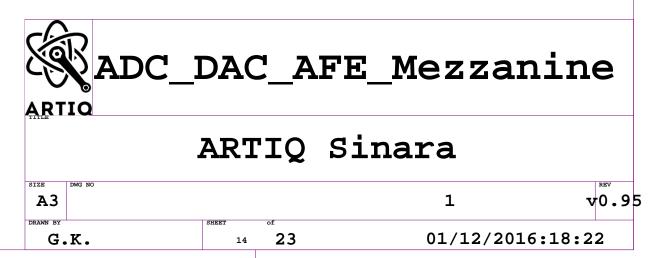


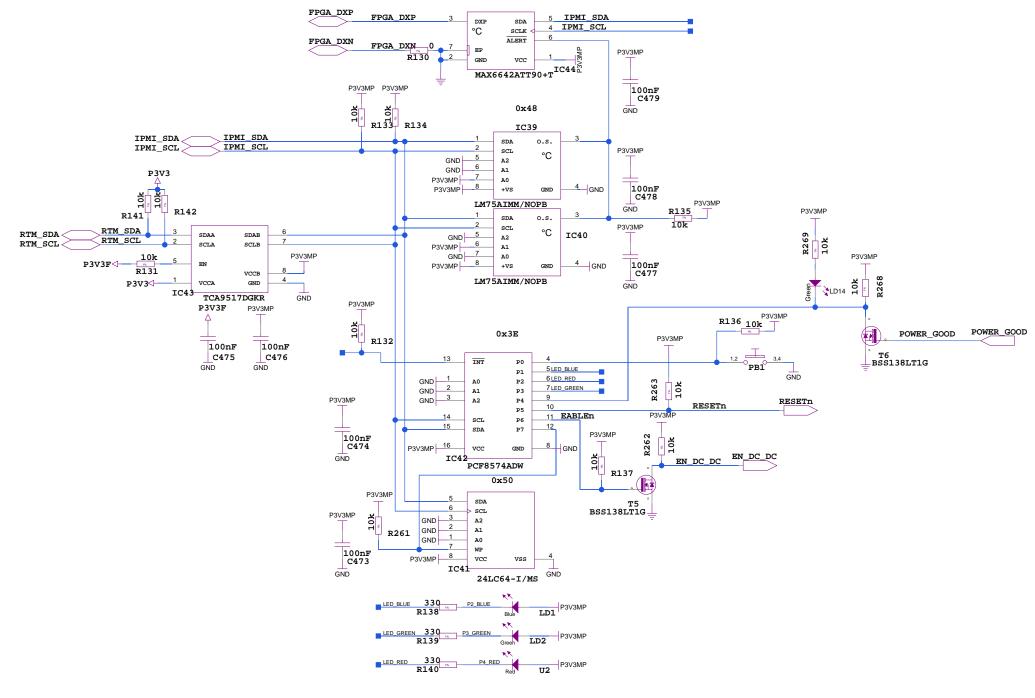


- +12VDC @ 200 mA, max 1 mV p-p noise in 20 Hz-20 MHz bandwidth
- -12VDC @ 50 mA, max 1 mV p-p noise in 20 Hz-20 MHz bandwidth
- +6VDC @ 1.5 A, max 1 mV p-p noise in 20 Hz-20 MHz bandwidth -6VDC rail @ 100 mA, max 1 mV p-p noise in 20 Hz-20 MHz bandwidth
- +3.3VDC @ 1 A, max 10 mV p-p noise in 20 Hz-20 MHz

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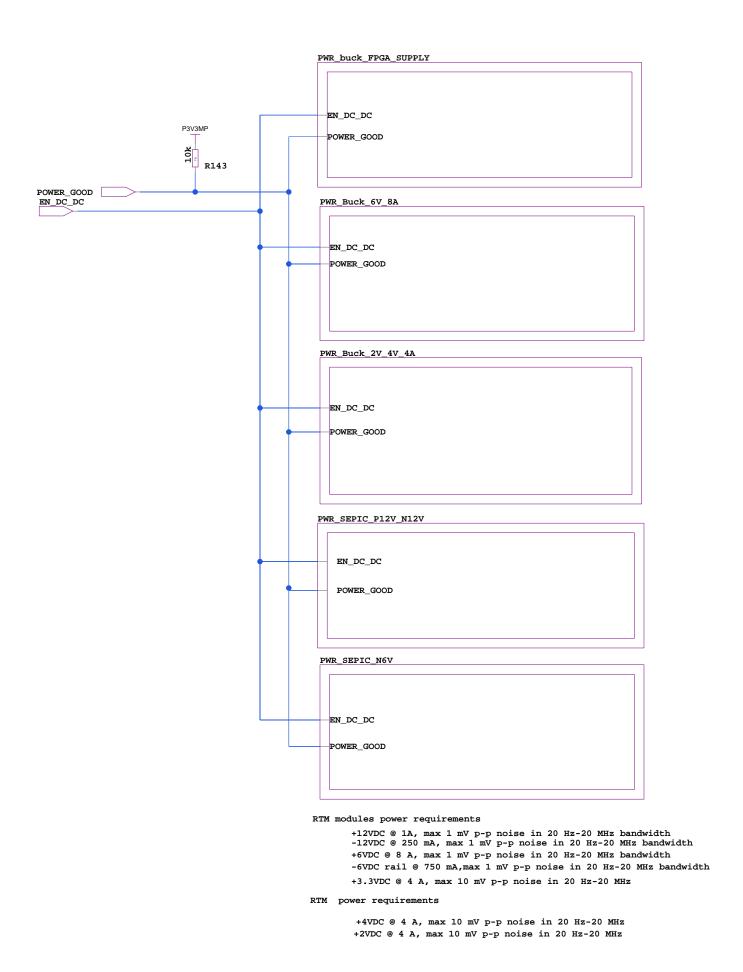
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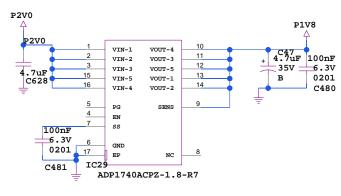
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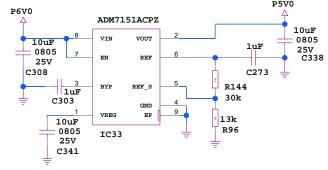
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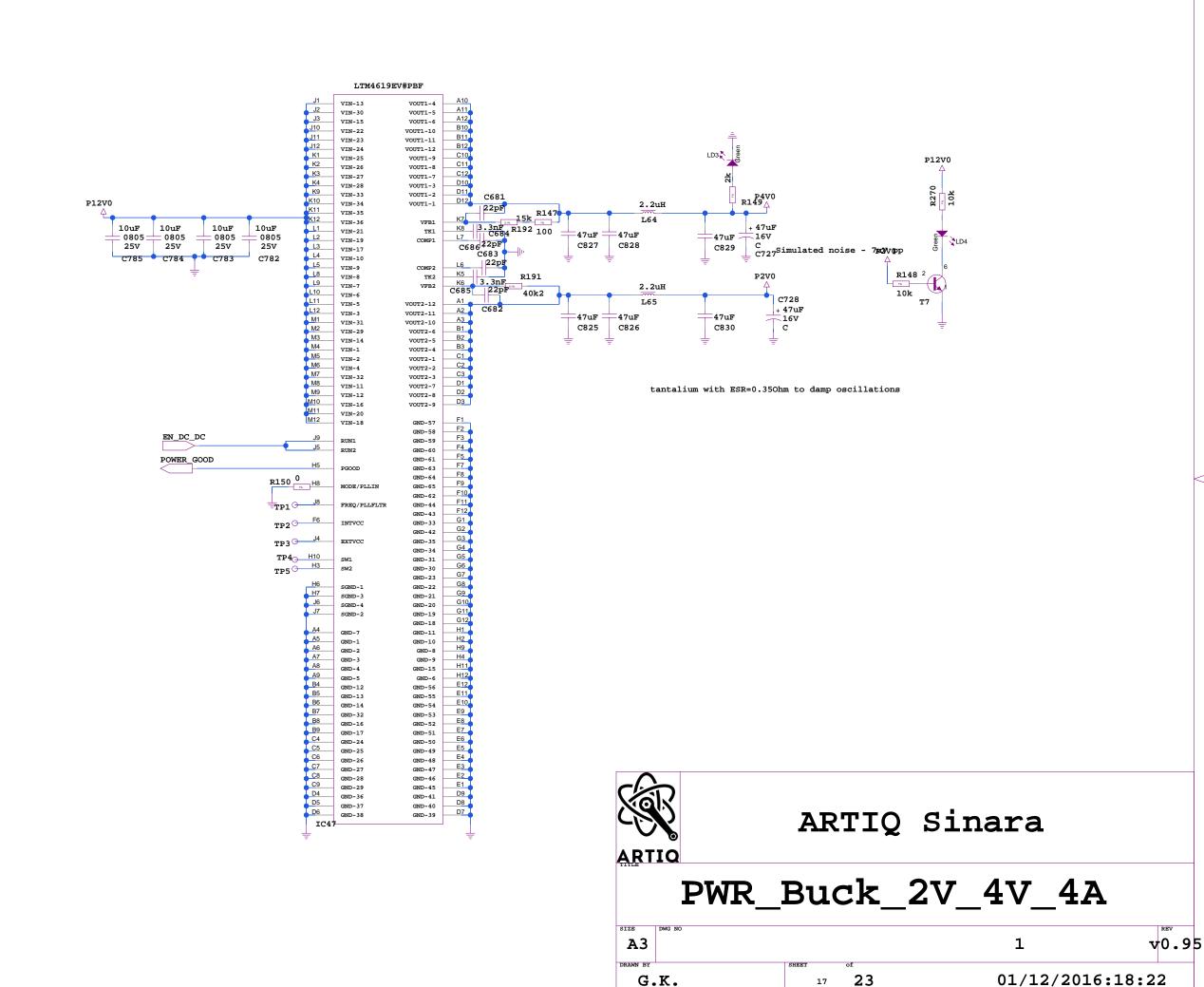
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### ARTIQ Sinara

## RTM\_POWER\_SUPPLY

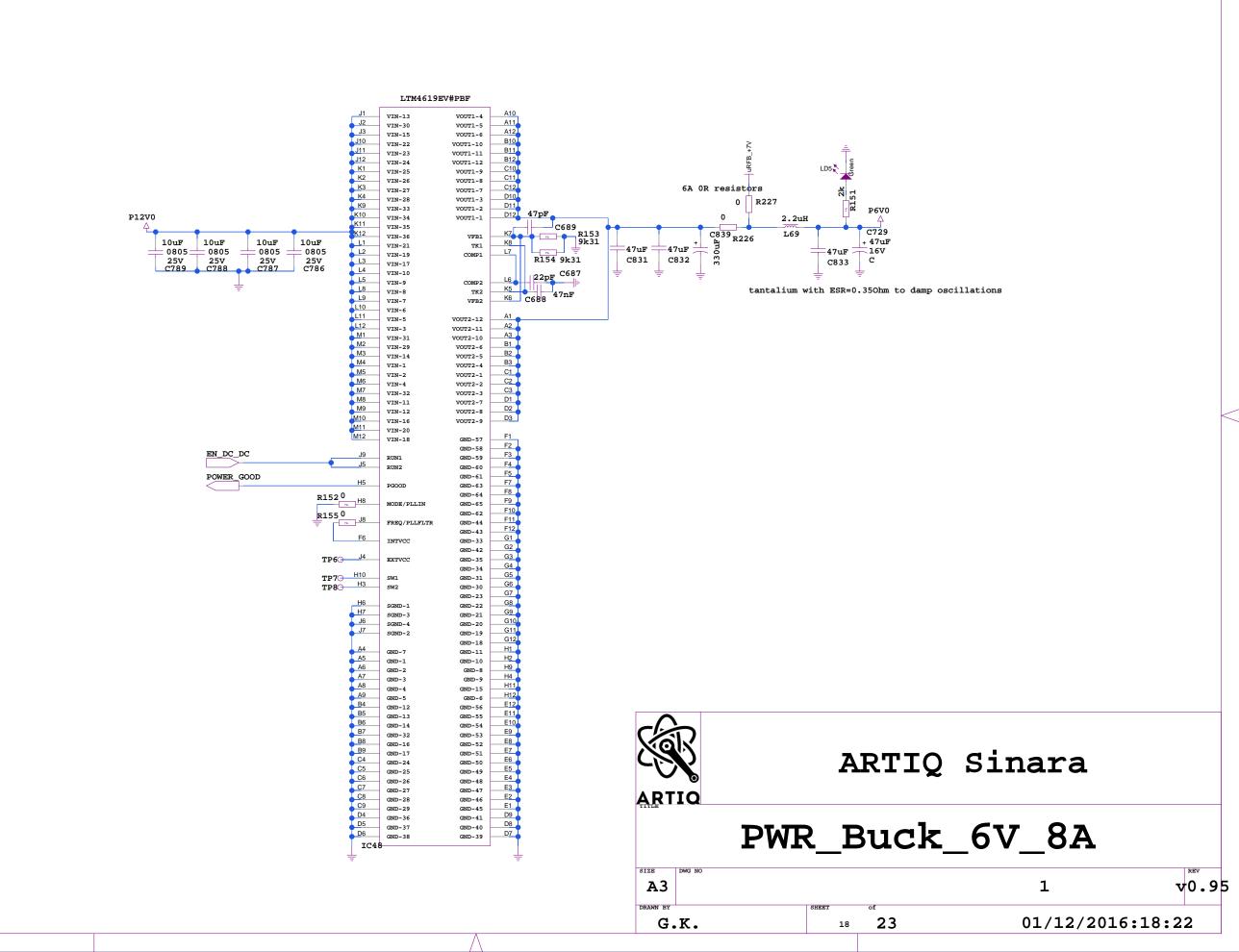
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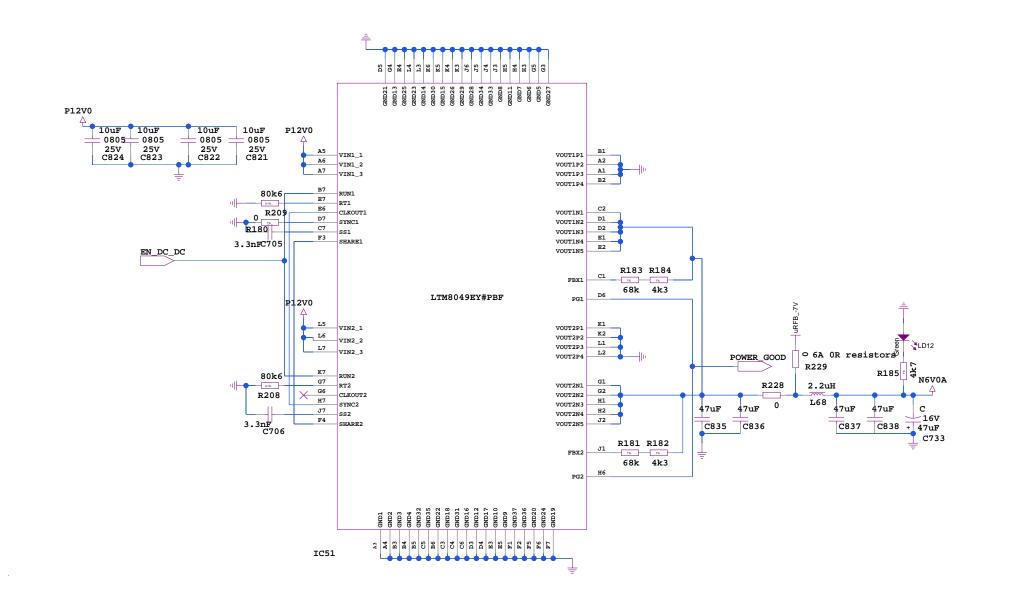
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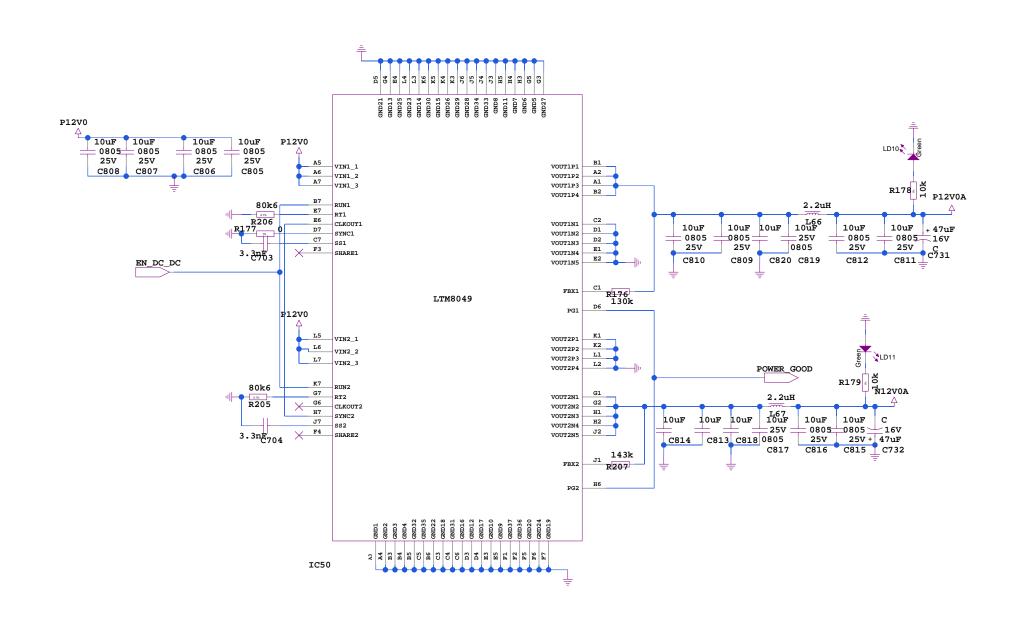


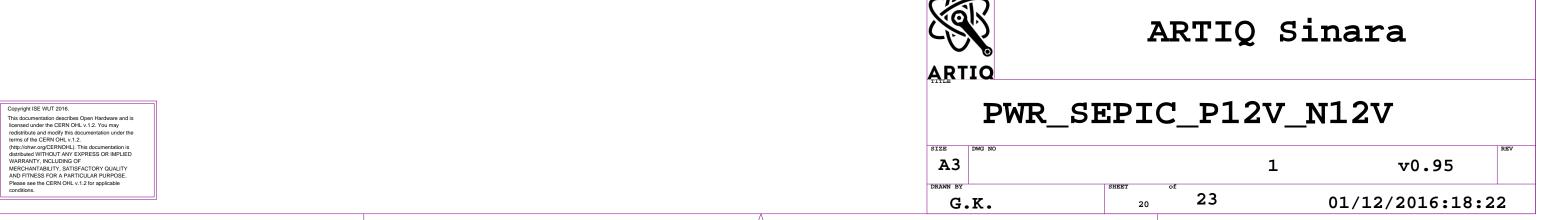
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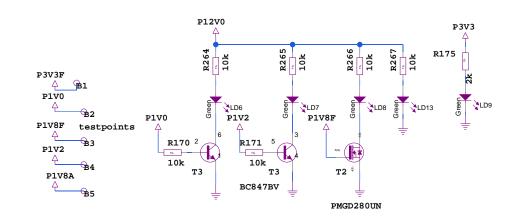
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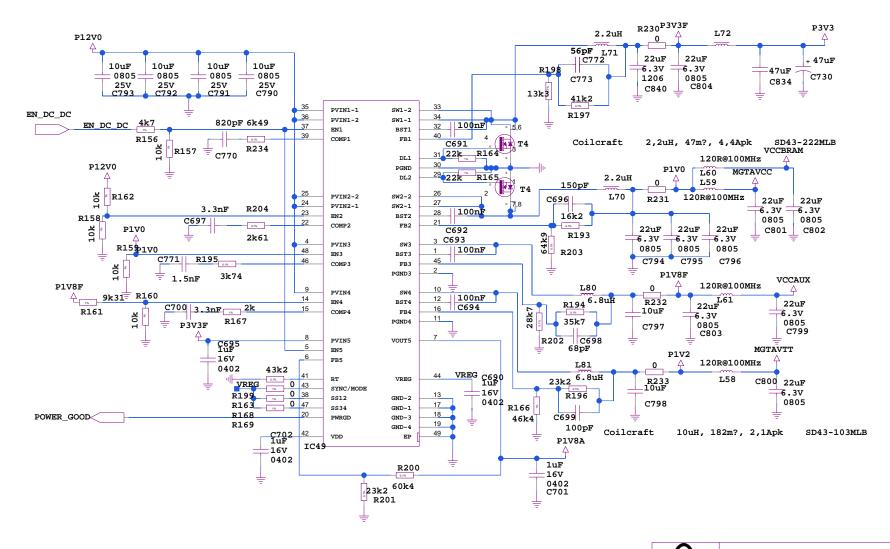














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#### ARTIQ Sinara

## PWR\_buck\_FPGA\_SUPPLY

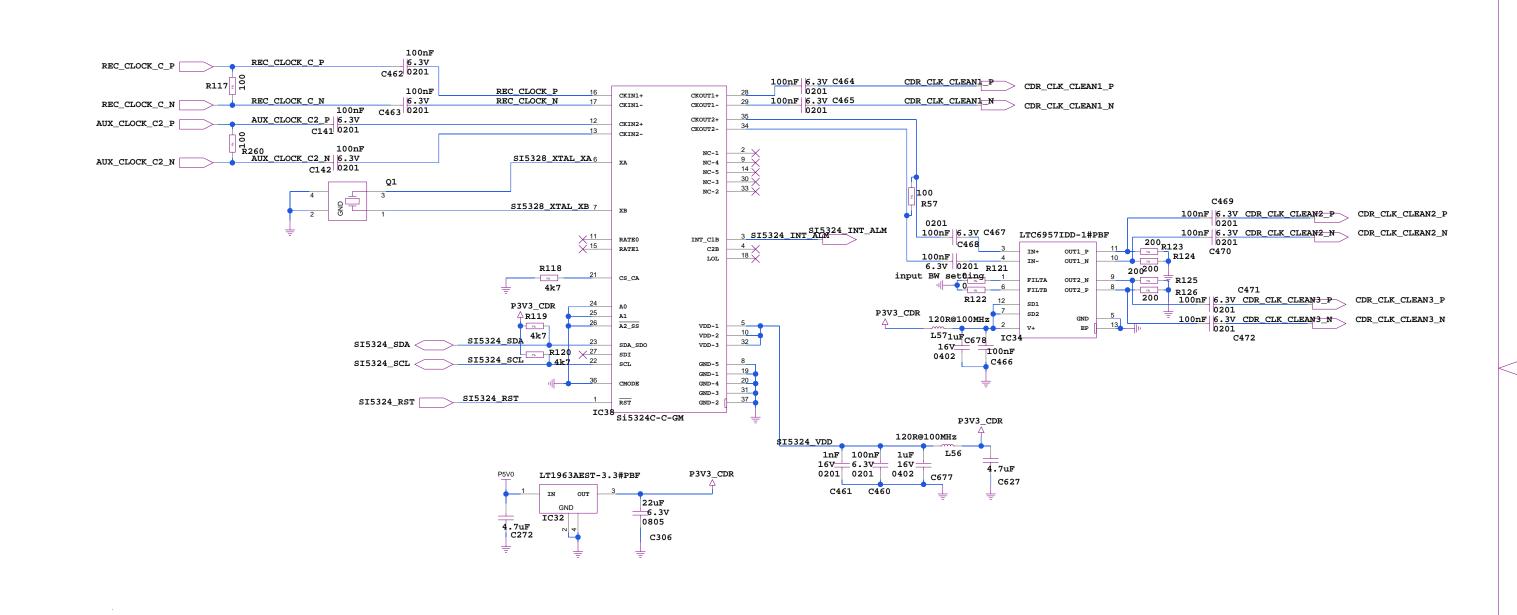
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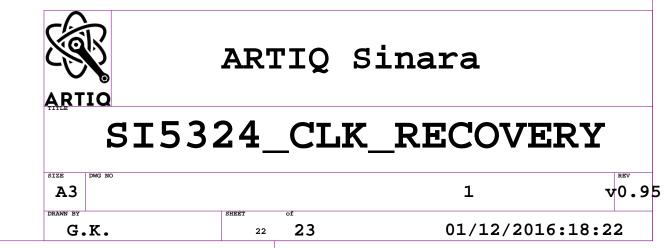
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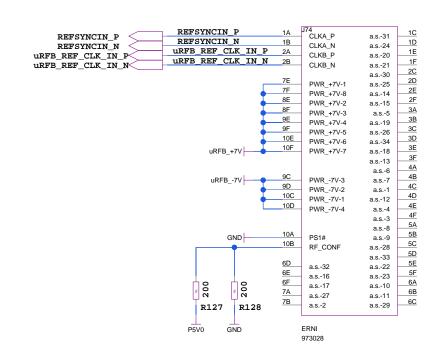
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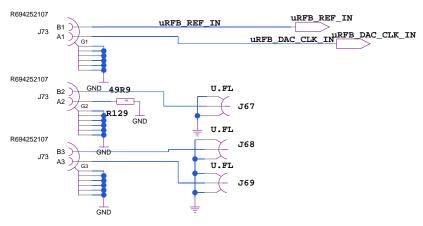




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