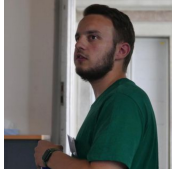


Riccardo Romanello

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About me

I completed my PhD at University of Udine at the end of 2024 and I successfully defended (cum laude) my thesis entitled *On the role of Graphs in Quantum Computation* in March 17th, 2025. Currently I hold a fellowship from Ca' Foscari University of Venice. My research activity is driven by what theoretical computer science can do to address task related to quantum computing. The main tool I adopt to do so in theoretical graph theory, with a particular emphasis on quantum circuit compilation across all its stages. I have explored various aspects of this process, including CNOT synthesis, qubit mapping, qubit routing, and general unitary synthesis. My investigations in theoretical computer science enhance my capability to tackle diverse challenges in both classical and quantum domains. Additionally, I am member of the Quantum Computing Lab at the University of Udine.

Contact Information

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Address: Viale Tricesimo, 164, 33100 Udine, Italy

Education

University of Udine, Udine, Italy

Ph.D. Student, 11/2021-11/2024

Topic: Investigating the role of graphs in quantum computation. How they can be used in such paradigm and how graph-theoretical techniques can help quantum.

MSc in Computer Science, 2018-2021

Score: 110/110 with honors. *Thesis:* Quantum Automata. How are automata defined in the quantum settings. Introduction of a novel model of quantum automata *a la Heisenberg*.

BSc in Computer Science, 2015-2018

Score: 110/110 with honors. *Thesis:* Models for Quantum Computing. A very first approach to theoretical Quantum models: from Quantum Turing Machines to Grover's algorithm.

I.T.I.S Max Planck, Treviso, Italy

Technical Diploma (High school), 2010-2015

Score: 100/100 with honors.

Experience

Academic

Technical University of Munich, Munich, Germany

Visiting Scientist, 03/2023-06/2023

Three-month internship at the Technical University of Munich, working in Professor Wille's Quantum laboratory. We investigated techniques for the compilation of Quantum Circuits.

Industrial

Blue Reply, Silea, Italy

Consultant, 10/2018-10/2021

Software for financial services.

Skills

Languages: Italian (native), English (competent).

Software: C/C++, Python, Java, Haskell.

Teaching Services

Teaching Assistant: *Elements of Mathematics and Linear Algebra:* Introduction to the basic notions of mathematics, probability and linear algebra, 24 hours. October 2022-February 2023, BSc Computer Science at the University of Udine.

Teaching Assistant: *Foundations of Computer Science:* automata theory, computability theory, complexity theory, 20 hours. March 2024-June 2024, BSc Computer Science at the University of Udine.

Research Activities

Journals Reviewer: Quantum, International Journal of Foundations of Computer Science, IEEE Transactions on Neural Networks and Learning Systems, Intelligenza Artificiale.

Conferences Reviewer:

- IEEE QSW, IEEE International Conference on Quantum Software, 2023.
- AIQxQIA, International Workshop on AI for Quantum and Quantum for AI, 2023 and 2024.
- CILC, Italian Conference on Computational Logic, 2023 and 2024.
- GandALF, Games, Automata, Logics, and Formal Verification, 2023.
- ICTCS, Italian Conference on Theoretical Computer Science, 2023 and 2024.

Program Committee member:

- AIQxQIA, International Workshop on AI for Quantum and Quantum for AI, 2023 and 2024.

Local Committee member:

- EQAI, European Summer School on Quantum AI, from 2022 to 2024.
- AIxIA, International Conference of the Italian Association for Artificial Intelligence, 2023.
- CILC, Italian Conference on Computational Logic, 2023 and 2024.

Thesis Co-Supervisor:

[5] Diego Borsoi, MSc Computer Science at University of Udine. *Translating unitaries into the Measurement Based Quantum Computing model.*

[4] Alex Della Schiava, MSc Artificial Intelligence and Cybersecurity at the University of Udine. *Graph encoding in Quantum Computing.*

[3] Davide Della Giustina, MSc Artificial Intelligence and Cybersecurity at the University of Udine. *Classical Computations On Quantum Architectures - Compiling Answer Set Programs.*

[2] Dusan Sutulovic, MSc Artificial Intelligence and Cybersecurity at the University of Udine. *Playing with CNOT: a Reinforcement Learning Approach to Linear Reversible Circuit Synthesis*.

[1] Francesco De Cataldo, BSc Computer Science at University of Udine. *An algorithm for the T-Count of Clifford+T circuits*.

Selected Publications

[7] *Classical computation over quantum architectures: From graph encoding to declarative languages compilation*. Della Schiava A et al. Journal of Logic and Computation (2024).

[6] *Incremental NFA Minimization*. Bianchini C et al. Theoretical Computer Science (2024).

[5] *Quantum Encoding of Dynamic Directed Graphs*. Della Giustina D et al. Journal of Logical and Algebraic Methods in Programming (2024).

[4] *Speeding up Answer Set Programming by Quantum Computing*. Romanello R et al. ACM Workshop on Quantum Search and Information Retrieval, QUASAR (2024).

[3] *Compressing Neural Networks via Formal Methods*. Ressi D et al. Neural Networks (2024).

[2] *Synthesis of CNOT minimal quantum circuits with topological constraints through ASP*. Piazza C and Romanello R. International Workshop on AI for Quantum and Quantum for AI, AIQxQIA (2023).

[1] *Mirrors and Memory In Quantum Automata*. Piazza C and Romanello R. International Conference on Quantitative Evaluation of Systems, QEST (2022).

Awards

[3] First place at the Hackathon 'Quantum Machine Learning for Smart Cities' organized by University of Cagliari.

[2] *Graph-Theoretical Arguments in Support of a Quantum Declarative Manifesto* presented at CILC23 won the best student paper award.

[1] I was awarded best BSc computer science student in 2021 by University of Udine.

Publications

Journal papers

[5] *Classical computation over quantum architectures: From graph encoding to declarative languages compilation*. Della Schiava A et al. Journal of Logic and Computation (2024).

[4] *Incremental NFA Minimization*. Bianchini C et al. Theoretical Computer Science (2024).

[3] *Quantum Encoding of Dynamic Directed Graphs*. Della Giustina D et al. Journal of Logical and Algebraic Methods in Programming (2024).

[2] *Compressing Neural Networks via Formal Methods*. Ressi D et al. Neural Networks (2024).

[1] *AI-enhanced blockchain technology: A review of advancements and opportunities*. Ressi D et al. Journal of Network and Computer Applications (2024).

Conference papers

[9] *Softer is Better: Tweaking Quantum Dropout to Enhance Quantum Neural Network Trainability*. Romanello R et al. Inter-

national Conference on Quantum Communications, Networking, and Computing, QCNC (2025).

[8] *Speeding up Answer Set Programming by Quantum Computing*. Romanello R et al. ACM Workshop on Quantum Search and Information Retrieval, QUASAR (2024).

[7] *Synthesis of CNOT minimal quantum circuits with topological constraints through ASP*. Piazza C and Romanello R. International Workshop on AI for Quantum and Quantum for AI, AIQxQIA (2023).

[6] *Graph-Theoretical Arguments in Support of a Quantum Declarative Manifesto*. Della Schiava A et al. Italian Conference on Computational Logic, CILC (2023).

[5] *An ASP Approach for the Synthesis of CNOT Minimal Quantum Circuits*. Piazza C et al. Italian Conference on Computational Logic, CILC (2023).

[4] *Neural Networks Reduction Via Lumping*. Ressi D et al. International Conference of the Italian Association for Artificial Intelligence, AIxIA (2022).

[3] *Directed Graph Encoding in Quantum Computing Supporting Edge-Failures*. Della Giustina D et al. International Conference on Reversible Computation, RC (2022).

[2] *Incremental NFA Minimization*. Bianchini C et al. 23rd Italian Conference on Theoretical Computer Science, ICTCS (2022).

[1] *Mirrors and Memory In Quantum Automata*. Piazza C and Romanello R. International Conference on Quantitative Evaluation of Systems, QEST (2022).

Talks

Conferences and Workshops

[8] **QUASAR24**, June 2024, Pisa.

Speeding up Answer Set Programming by Quantum Computing.

[7] **AIxIA23**, November 2023, Udine.

Synthesis of CNOT minimal quantum circuits with topological constraints through ASP.

[6] **CILC23**, July 2023, Udine.

An ASP Approach for the Synthesis of CNOT Minimal Quantum Circuits.

[5] **CILC23**, July 2023, Udine.

Graph-Theoretical Arguments in Support of a Quantum Declarative Manifesto.

[4] **QEST22**, September 2022, Warsaw.

Mirrors and Memory in Quantum Automata.

[3] **ICTCS22**, September 2022, Rome.

Incremental NFA Minimization.

[2] **RC22**, July 2022, Urbino.

Directed Graph Encoding in Quantum Computing Supporting Edge-Failures.

[1] **CILC22**, June 2022, Bologna.

Heisenberg in Quantum Automata.

Software

[1] Soft Dropout for VQC