Ricco Ferraro’s system

Text

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About my macbook pro:

CPU:

-Intel I9 8-core. ISA

-64bit

-Clock 2.4Ghz

-**ISA:** Everything up to [AVX512](https://en.wikichip.org/wiki/x86/avx-512) ([SMM](https://en.wikichip.org/w/index.php?title=x86/smm&action=edit&redlink=1), [FPU](https://en.wikichip.org/w/index.php?title=x86/fpu&action=edit&redlink=1), [NX](https://en.wikichip.org/w/index.php?title=x86/nx&action=edit&redlink=1), [MMX](https://en.wikichip.org/w/index.php?title=x86/mmx&action=edit&redlink=1), [SSE](https://en.wikichip.org/w/index.php?title=x86/sse&action=edit&redlink=1), [SSE2](https://en.wikichip.org/w/index.php?title=x86/sse2&action=edit&redlink=1), [Template:X86](https://en.wikichip.org/w/index.php?title=Template:X86&action=edit&redlink=1), [Template:X86](https://en.wikichip.org/w/index.php?title=Template:X86&action=edit&redlink=1), [SSE4.1](https://en.wikichip.org/w/index.php?title=x86/sse4.1&action=edit&redlink=1), [SSE4.2](https://en.wikichip.org/w/index.php?title=x86/sse4.2&action=edit&redlink=1), [AES](https://en.wikichip.org/w/index.php?title=x86/aes&action=edit&redlink=1), [AVX](https://en.wikichip.org/w/index.php?title=x86/avx&action=edit&redlink=1), [FMA3](https://en.wikichip.org/w/index.php?title=x86/fma3&action=edit&redlink=1), [AVX2](https://en.wikichip.org/w/index.php?title=x86/avx2&action=edit&redlink=1), and [AVX-512](https://en.wikichip.org/wiki/x86/avx-512))

AVX 512 allows 8bit multiplies with 32bit accumulations with a single instruction.

Graphical user interface, website

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-256 KB L2 cache (per core)

-16MB L1 Cache (shared)

GPU

Graphical user interface, text

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Graphical user interface, application

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-Cores: 1536 “pipelines”

-Memory 4GB

-Bandwidth = 192 GB/s

Memory:

-32GB (2X 16GB) DDR4 2.66 GHz

Graphical user interface, text, application

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Harddrive:

-1TB SSD

-PCIe

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