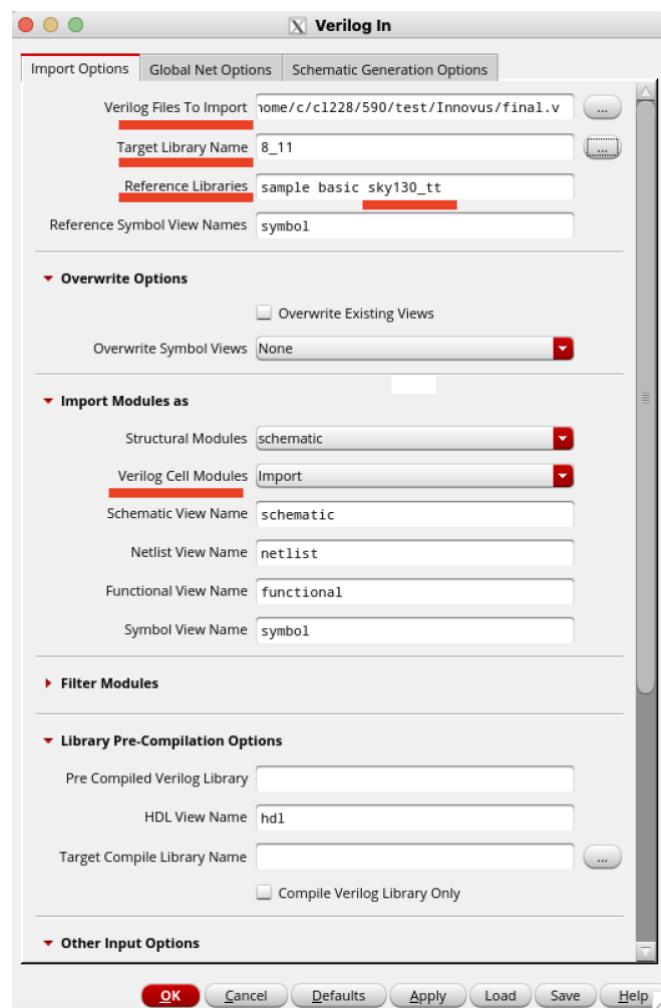
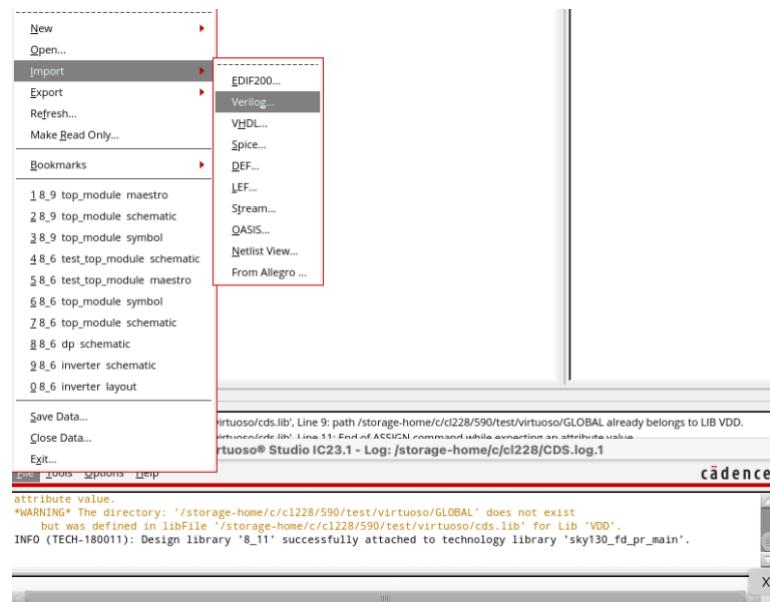


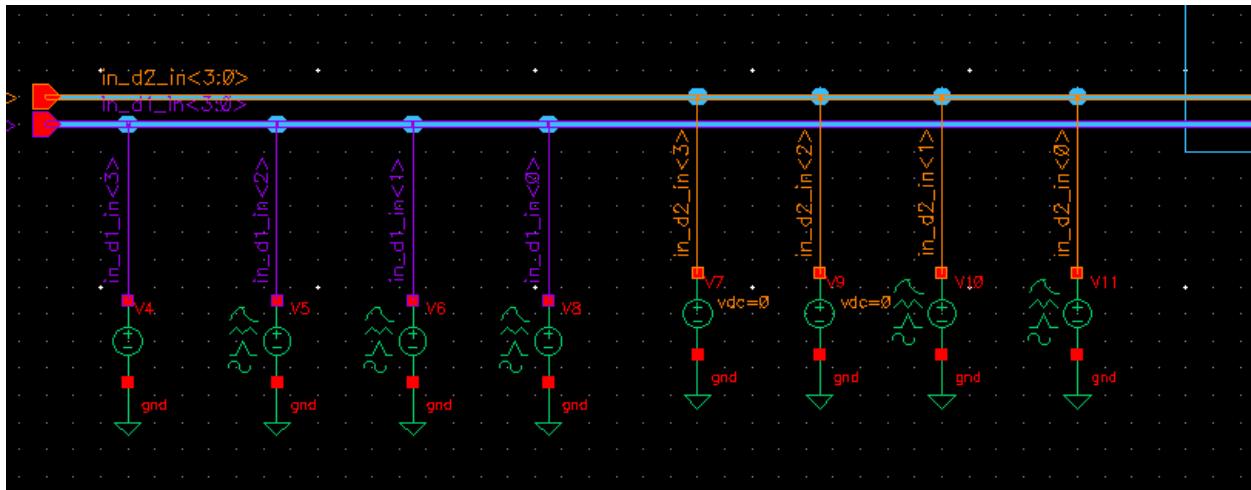
1. Import verilog file to virtuoso



Set global nets



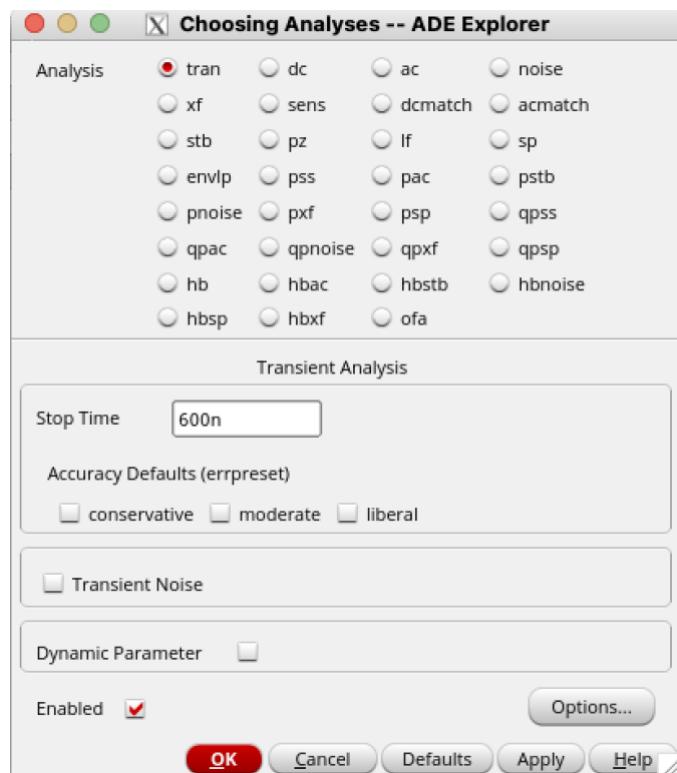
Then added input source in schematic



Run ADE explore and change simulator to spectre



Set tran analysis



Choose setup->stimuli

Assign global nets VDD! and VSS! to stimuli sources.

