

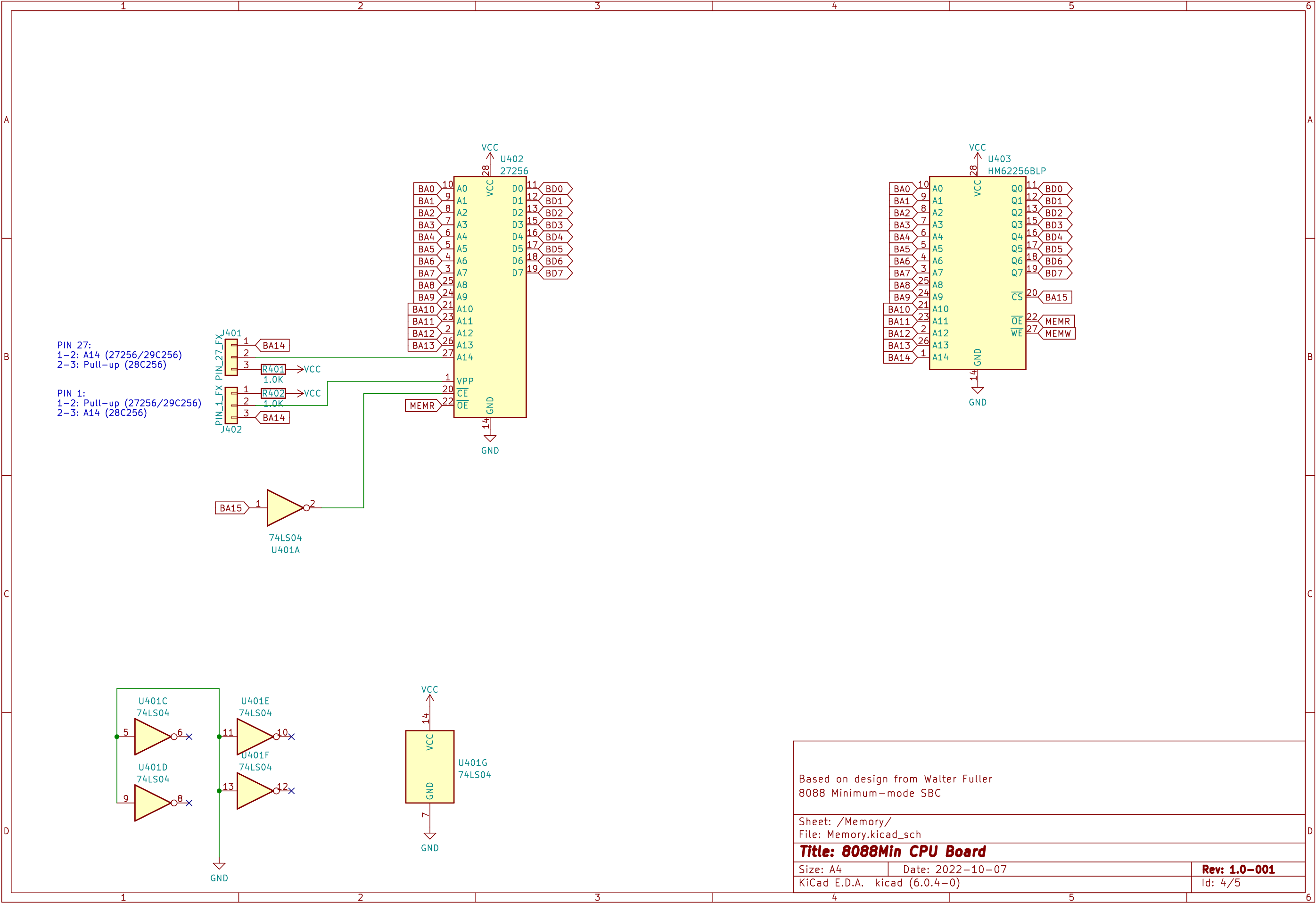
Based on design from Walter Fuller
8088 Minimum-mode SBC

Sheet: /PwrClock/
File: PwrClock.kicad_sch

Title: 8088Min CPU Board

Size: B Date: 2022-10-07
KiCad E.D.A. kicad (6.0.4-0)

Rev: 1.0-001
Id: 2/5



Based on design from Walter Fuller
8088 Minimum-mode SBC

Sheet: /Memory/
File: Memory.kicad_sch

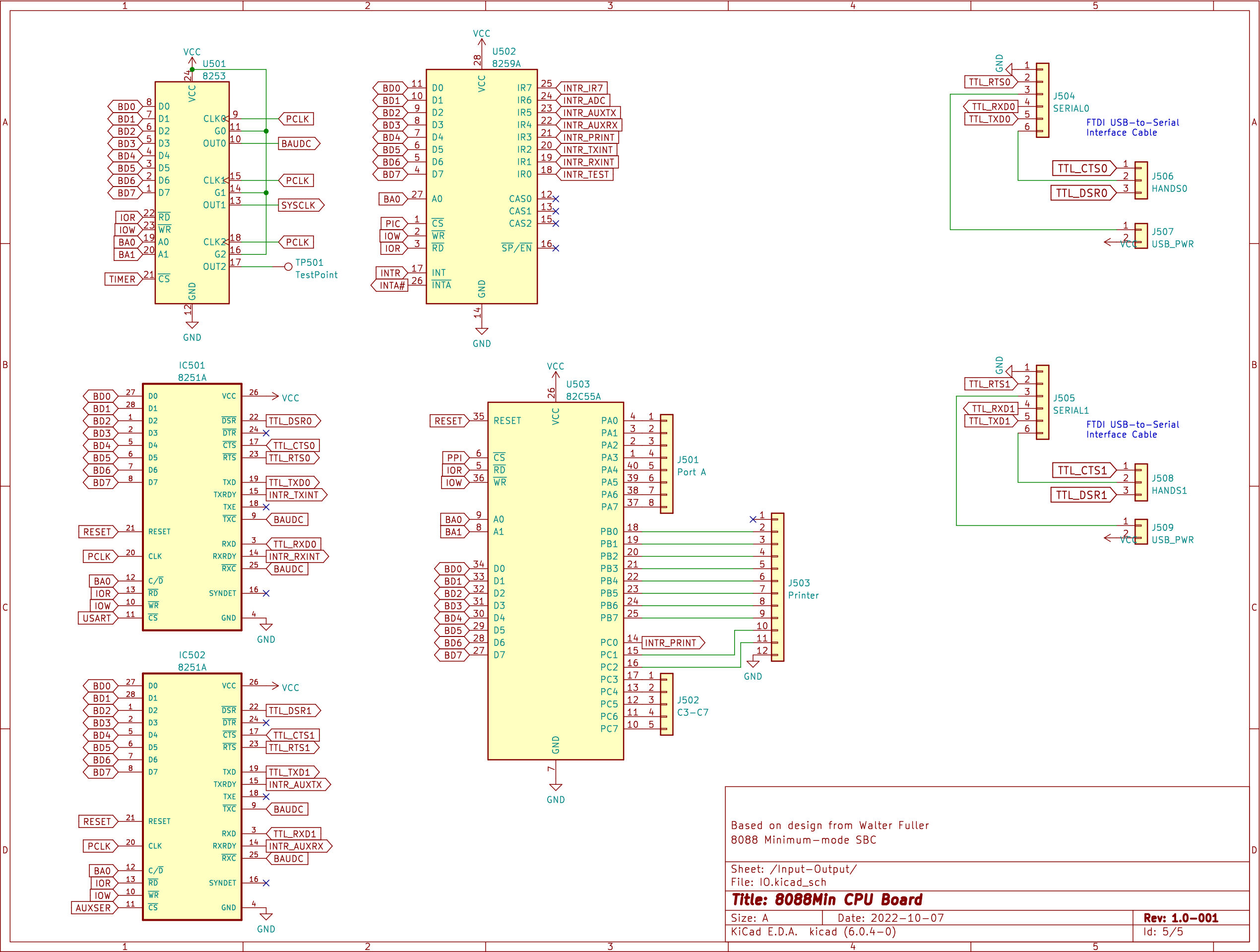
Title: 8088Min CPU Board

Size: A4 Date: 2022-10-07

KiCad E.D.A. kicad (6.0.4-0)

Rev: 1.0-001

Id: 4/5



Based on design from Walter Fuller
8088 Minimum-mode SBC

Sheet: /Input-Output/
File: IO.kicad_sch

Title: 8088Min CPU Board

Size: A Date: 2022-10-07
KiCad E.D.A. kicad (6.0.4-0)

Rev: 1.0-001
Id: 5/5