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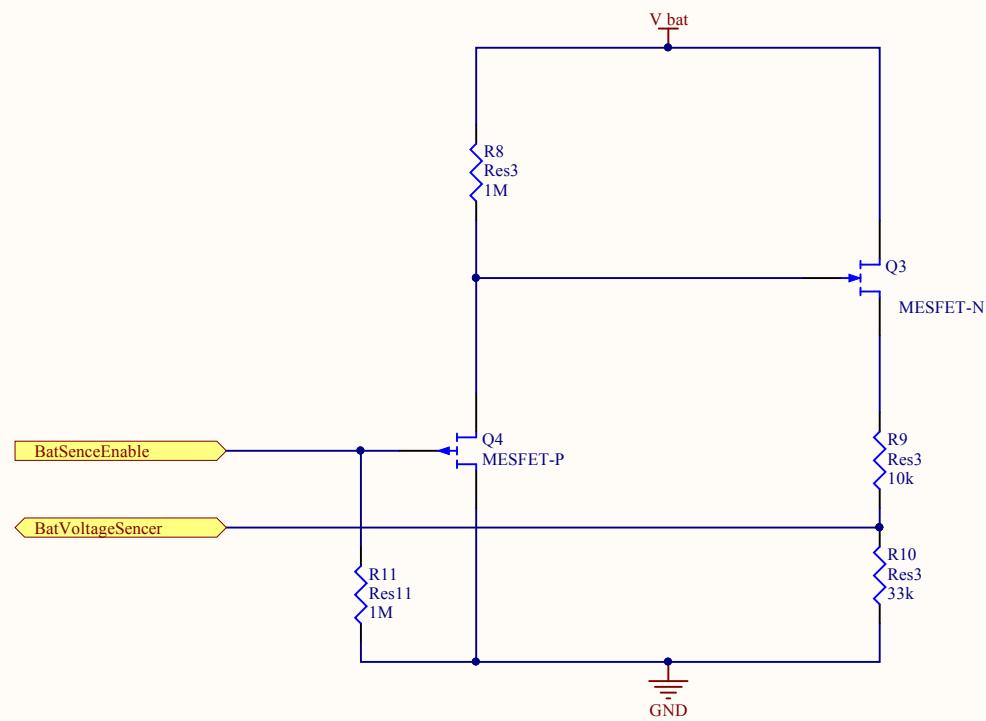
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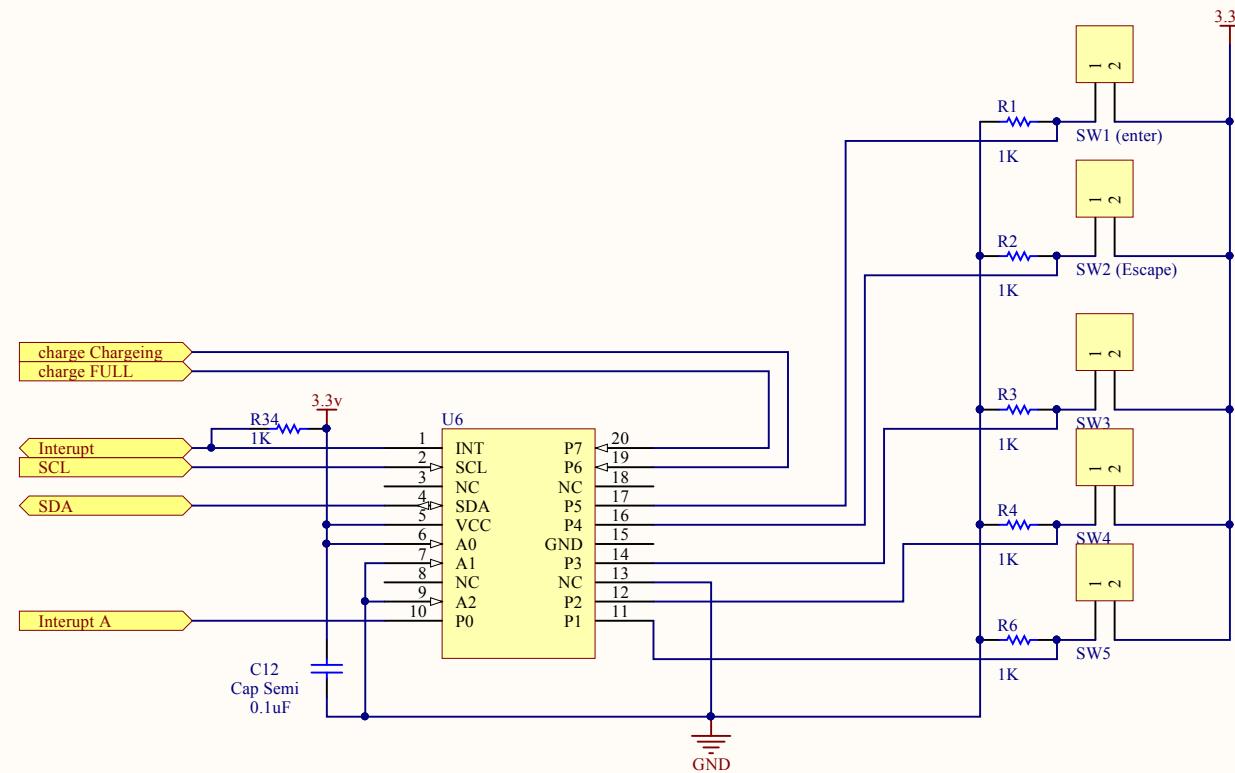
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Date: 28/09/2015

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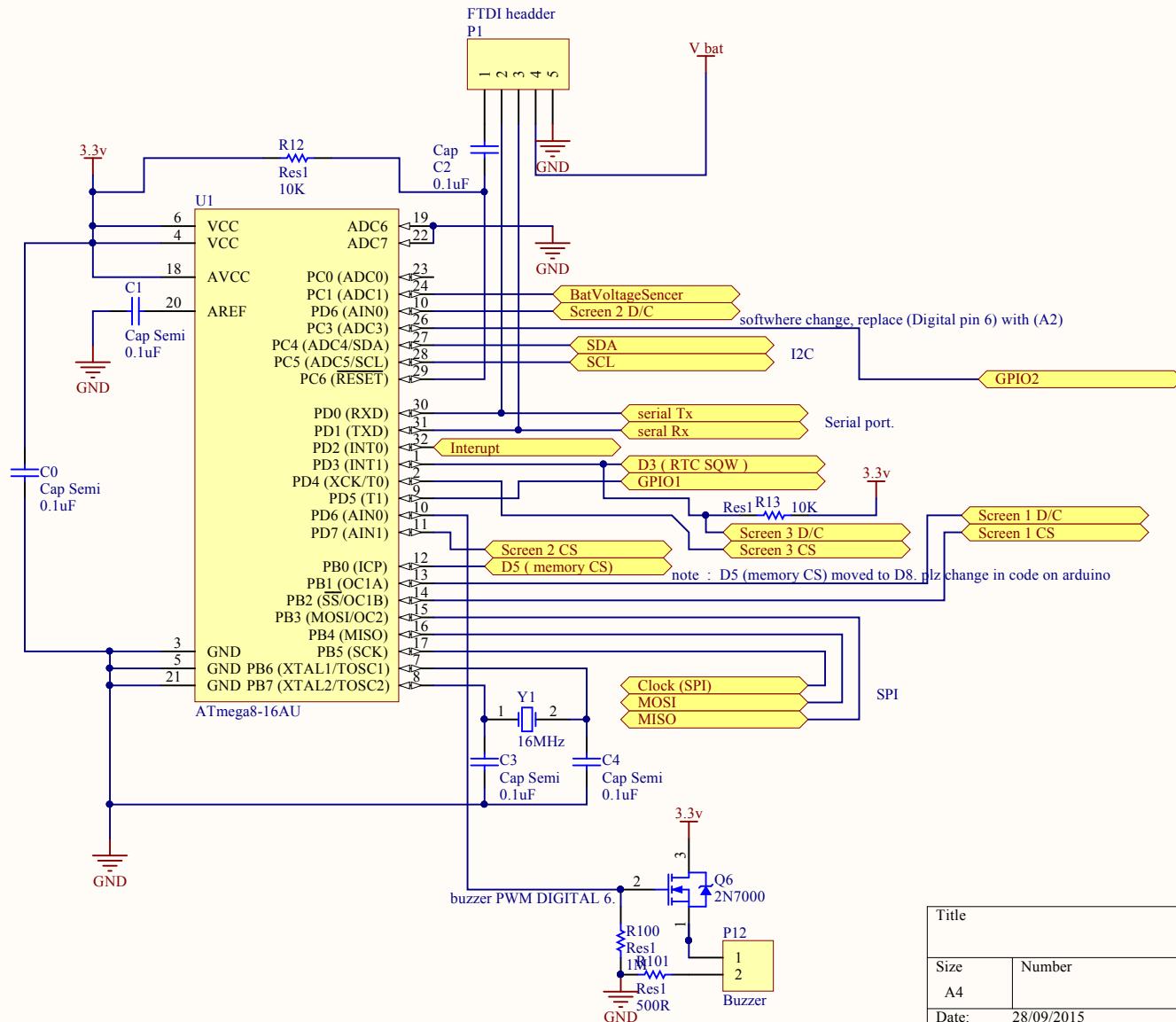
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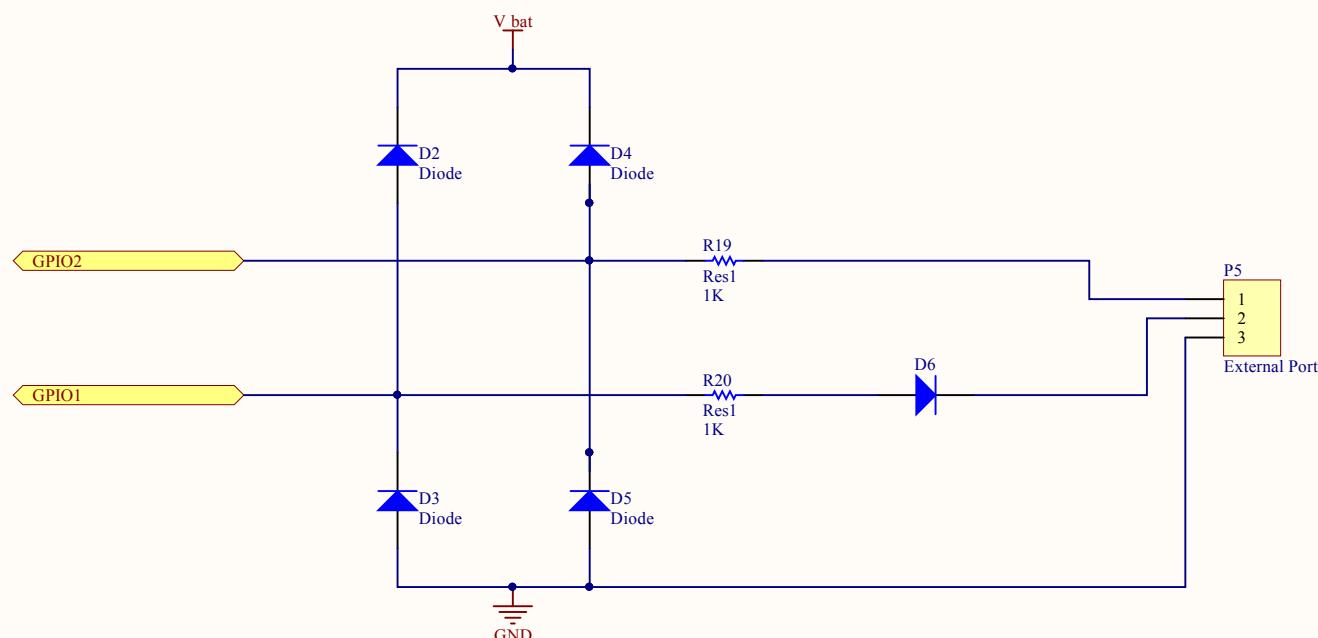
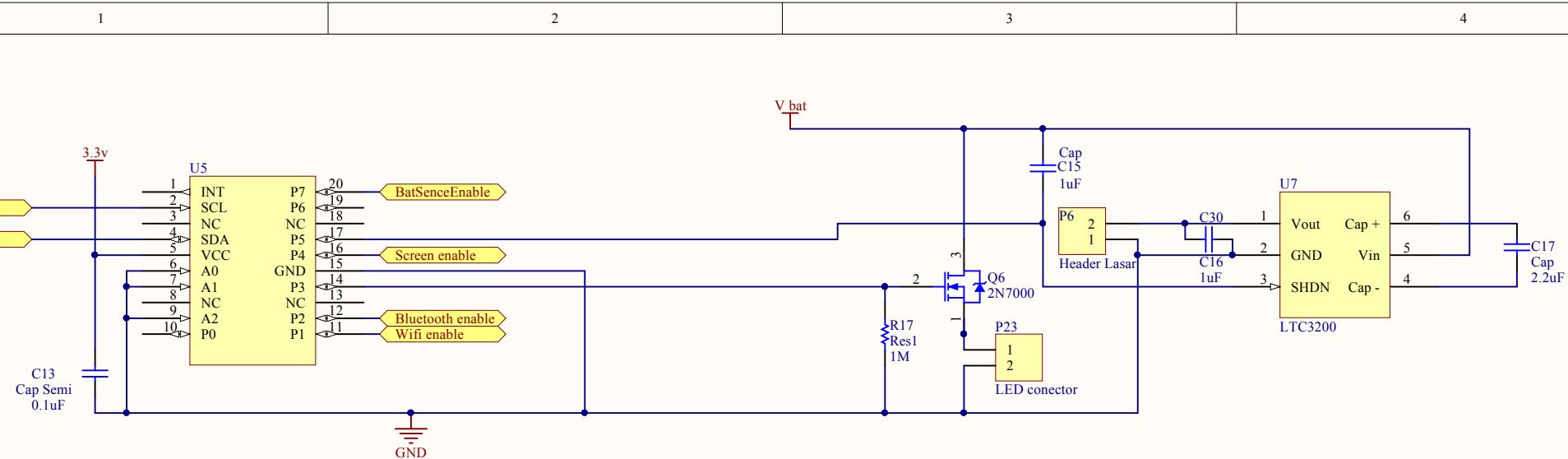
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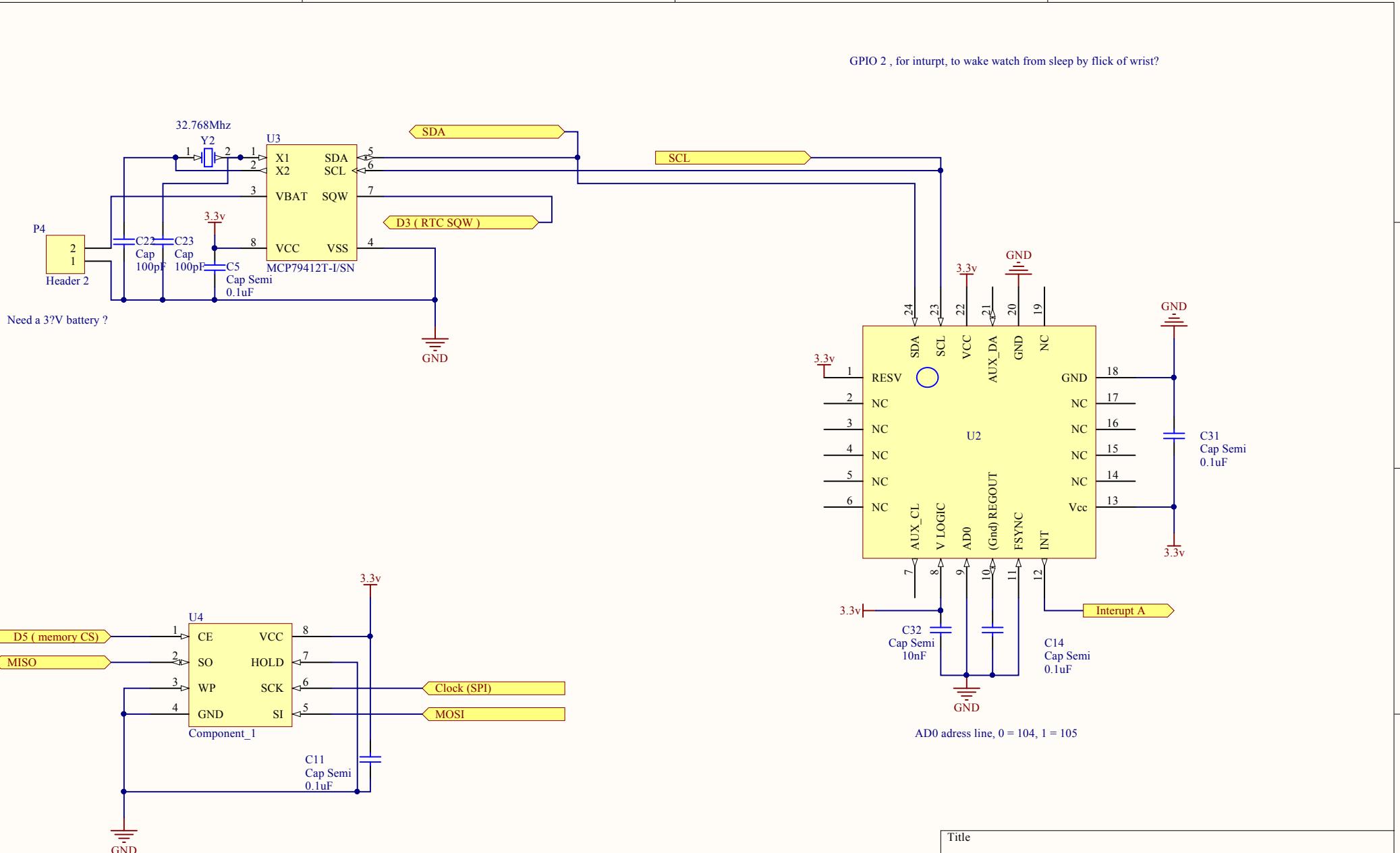
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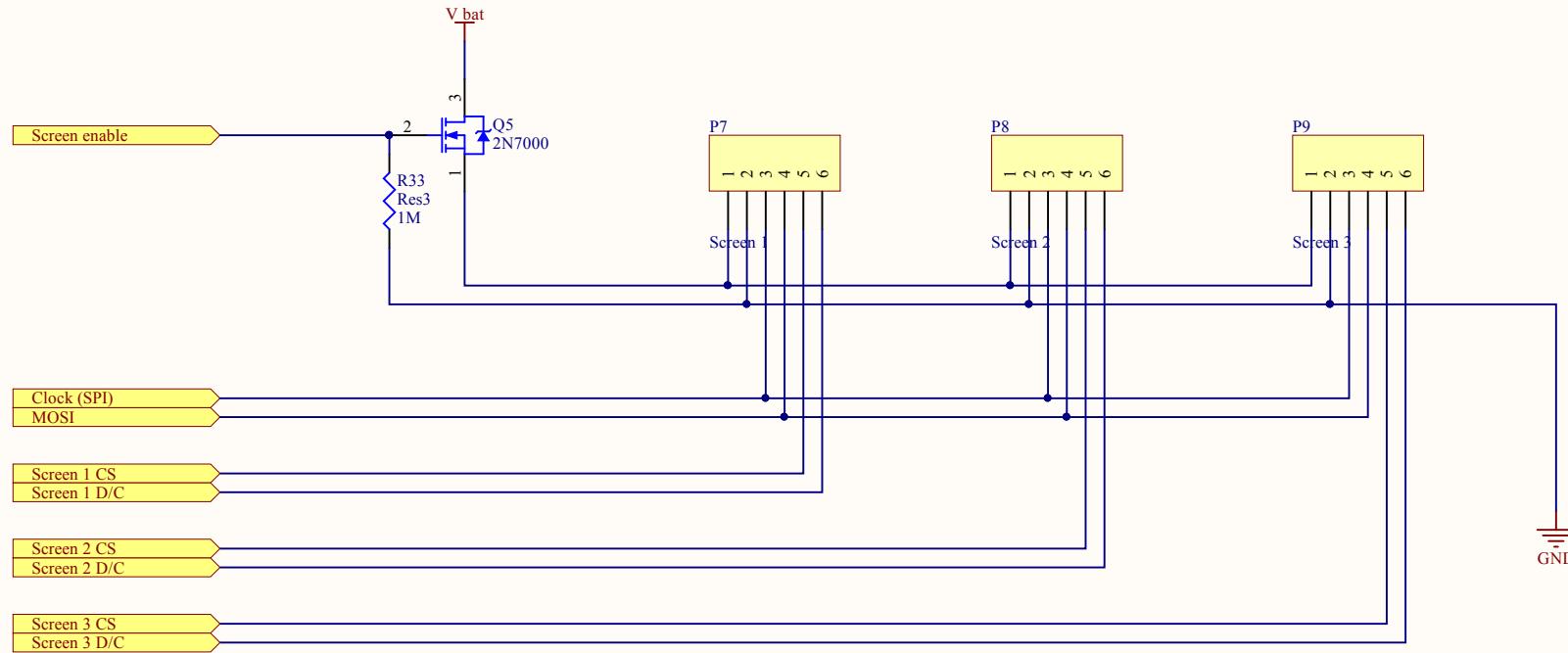
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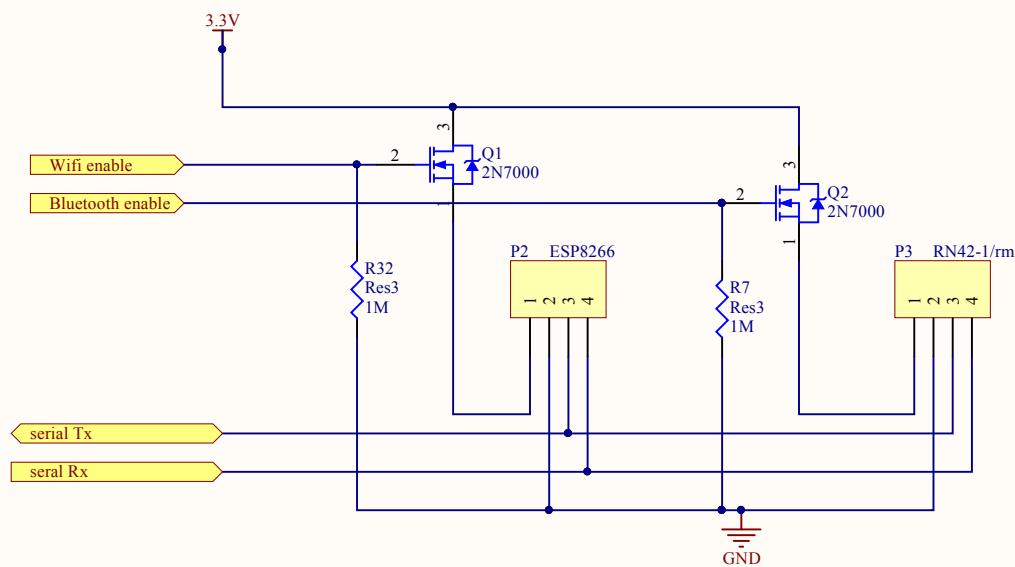


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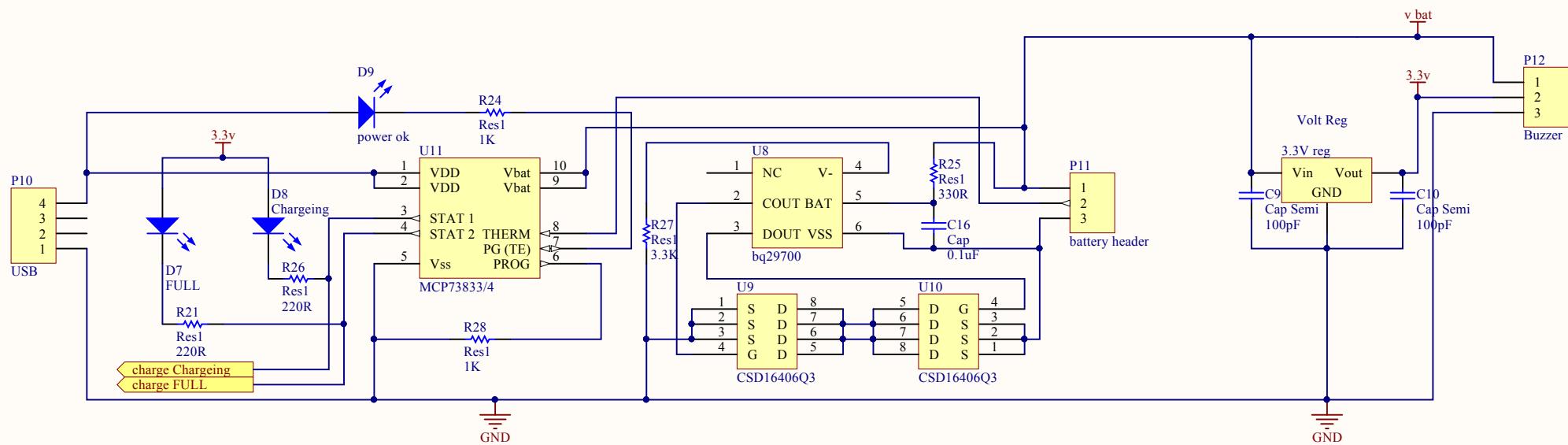
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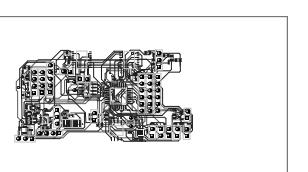
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F	THE INFORMATION CONTAINED HEREIN IS A TRADE SECRET OF RAYMARINE AND MAY NOT BE REPRODUCED WITHOUT THE PRIOR WRITTEN PERMISSION OF RAYMARINE. TOLERANCES UNLESS OTHERWISE STATED X = +/-0.50 OTHERWISE STATED	GENERAL NOTES PRINTED IN ACCORDANCE WITH IPC-A-610 CLASS 2. THIS ASSEMBLY SHALL BE ROHS COMPLIANT.	GENERAL NOTES PRINTED IN ACCORDANCE WITH IPC-A-610 CLASS 2.	GENERAL NOTES PRINTED IN ACCORDANCE WITH IPC-A-610 CLASS 2.	GENERAL NOTES PRINTED IN ACCORDANCE WITH IPC-A-610 CLASS 2.	GENERAL NOTES PRINTED IN ACCORDANCE WITH IPC-A-610 CLASS 2.	GENERAL NOTES PRINTED IN ACCORDANCE WITH IPC-A-610 CLASS 2.	GENERAL NOTES PRINTED IN ACCORDANCE WITH IPC-A-610 CLASS 2.	GENERAL NOTES PRINTED IN ACCORDANCE WITH IPC-A-610 CLASS 2.
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THE INFORMATION CONTAINED HEREIN SHALL NOT BE REPRODUCED WITHOUT THE PRIOR WRITTEN PERMISSION OF RAYMARINE LTD.		NO. OF LAYERS:		GENERAL NOTES																																																
MATERIAL: FR4 TO IPC410109 OR ABOVE WITH WITH A MINIMUM Tg RATING OF 170°C BOARD THICKNESS: OVER PLATING SURFACE FINISH: ENIG TO IPC-6502 CLASS 2 SOLDER RESIST: IPC-SM446 CLASS T		MINIMUM Tg RATING OF 170°C ALL PCB'S TO BE ELECTRICALY TESTED TO IPC-6222, 4. PCB NUMBER, ISSUE AND TITLE TO BE INCLUDED IN COPPER ON LAYER 1, TEXT TO BE IN UPPER CASE AND 5. PANEL WASTE AREA TO INCLUDE COPPER HATCHING.		1. THIS PCB SHALL BE MANUFACTURED IN ACCORDANCE WITH IPC-6012 CLASS 2. 2. MANUFACTURER LOGO AND UL MARK (SAV-V) TO BE ADDED TO SOLDER RESIST ARTWORK IN AREA SHOWN. 3. ALL PCB'S TO BE ELECTRICALY TESTED TO IPC-6222, 4. PCB NUMBER, ISSUE AND TITLE TO BE INCLUDED IN COPPER ON LAYER 1, TEXT TO BE IN UPPER CASE AND 5. PANEL WASTE AREA TO INCLUDE COPPER HATCHING.																																																
X	✓x 0.60	SHARP INTERNAL CORNERS TO HAVE MINIMUM RADIUS 0.30		6. WHERE APPPLICABLE IMPEDANCE TEST COUPONS SHALL BE PROVIDED; 7. REFER TO PURCHASE ORDER FOR DEVIATIONS FROM THIS DRAWING.																																																
XXX	✓x 0.06	CONTROLED IMPEDANCE BUILD:		8. SOLDER MASK: NO BLOCKING OF PINS WITHOUT WRITTEN PERMISSION OF RAYMARINE 9. SURFACE MICROVIA'S TO BE FILLED TO IPC-SPECIFICATIONS																																																
TOLERANCES UNLESS OTHERWISE STATED																																																				
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