RICHARD ROBINNSON

VLSI DESIGN ENGINEER



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https://richardrobinnsonlj.github.io/richard-portfolio/

EDUCATION

MASTER DEGREE

Sri Sivasubramaniya Nadar College of Engineering

BACHELOR OF ENGINEERING(ECE)

Loyola-ICAM College of Engineering and Technology

CGPA-8.44

HSC

St. Joseph School, Tindivanam

79.6%

SSLC

St.Joseph School, Tindivanam

91.2%

SKILLS

PROGRAMMING LANGUAGE

- Embedded C
- Verilog

HARDWARE

- Arduino
- STM32
- NodeMCU
- FPGA(De10-Nano)

TOOLS

- Cadence
- ADS

PROFILE

My interest lies in analog circuit design and VLSI systems, and I am pursuing my master's in VLSI from SSN College, following the ECE undergraduate degree from LICET. Such academic and project experience makes it possible for me to design high-performance, optimized semiconductor solutions in consonance with the commitment to innovation at Texas Instruments.

CAREER SUMMARY

INTERNSHIPS

- Completed a 2-month IoT internship at Bolt Academy during my second year, which gave me hands-on experience with IoT kits and basic Python programming. I worked on a project related to IoT, deepening my understanding of its concepts and applications.
- Completed a 14-day online **VLSI internship** at **Tessolve**, where I learned Verilog and implemented an **ALU design** on an **FPGA board**.
- I also completed a 6-month internship at NPG Websmart, where I worked with boards like NodeMCU, Arduino, and STM32, as well as sensors and protocols such as the memory card module, MQTT protocol, USART, SPI, and I2C.

CERTIFICATIONS

- Embedded C course in Udemy
- Verilog and Digital design course in Maven Silicon

PROJECTS

- Submitted a paper on **LNA Design** for frequency range between **60GHz to 90GHz** in **WISPNET**.
- Submitted a paper on **LNA Design** on **90nm CMOS** for frequency range between **7GHz to 10GHz** in **WISPNET**.
- Finalist on Chrysalis event conducted by LIBA for submitting a
 project on Accident Detection With location sharing and
 "Accident prevention because of alcohol consumption" my team
 showed a demo of the project in proteus software where I did the
 connection and coding.
- Automatic Fire Extinguisher Bot that extinguish fire automatically during intracollege competion and i did all the hardware connections and coding.
- **Zera Food Recycler** that converts food waste into manure during my 12th grade and got district second.
- Water Purifier which purifies water from AC and water the plant during my 10th grade and got district first.
- Participated in Intel FPGA contest and publish a paper on **Automatic Agriculture Bot** and got selected for the second phase which is the implementation.