

# Actively Secure Half-Gates with Minimum Overhead under Duplex Networks

**Hongrui Cui**

Shanghai Jiao Tong University



**Kang Yang**

State Key Laboratory of Cryptology



**Xiao Wang**

Northwestern University



**Yu Yu**

Shanghai Jiao Tong University

Shanghai Qi Zhi Institute



**上海期智研究院**

SHANGHAI QI ZHI INSTITUTE

# Background on Constant Round 2PC

■ Steady improvement in the semi-honest world

Textbook [Yao86]	P&P [BMR90]	GRR3 [NPS99]	GRR2 [PSSW90]	Free-XOR [KS08]	FleXOR [KMR14]	Half-Gates [ZRE15]	Three-Halves [RR21]
XOR: $8\kappa$ AND: $8\kappa$	XOR: $4\kappa$ AND: $4\kappa$	XOR: $3\kappa$ AND: $3\kappa$	XOR: $2\kappa$ AND: $2\kappa$	XOR: 0 AND: $3\kappa$	$\{0, 1, 2\}_\kappa$	$2\kappa$	$1.5\kappa + 5$

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Cut-and-Choose [LP07,NO09,HKE13,NST17,...]
$O(\rho\kappa)$ or $O(\frac{\rho\kappa}{\log C})$

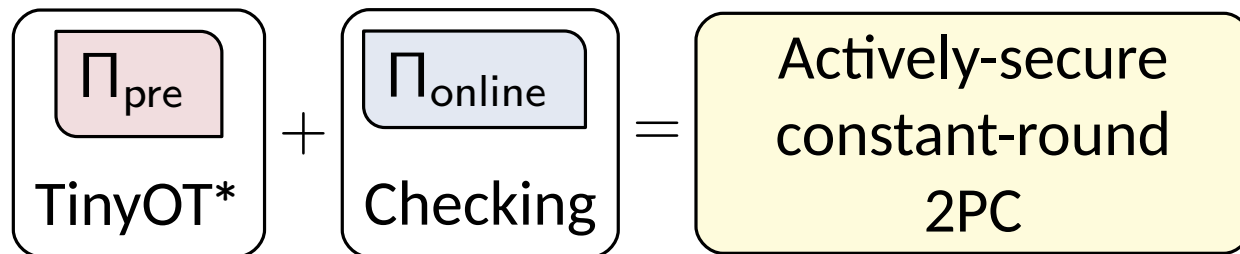
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[Yao86]	[BMR90]	[NPS99]	[PSSW90]	[KS08]	[KMR14]	[ZRE15]	[RR21]
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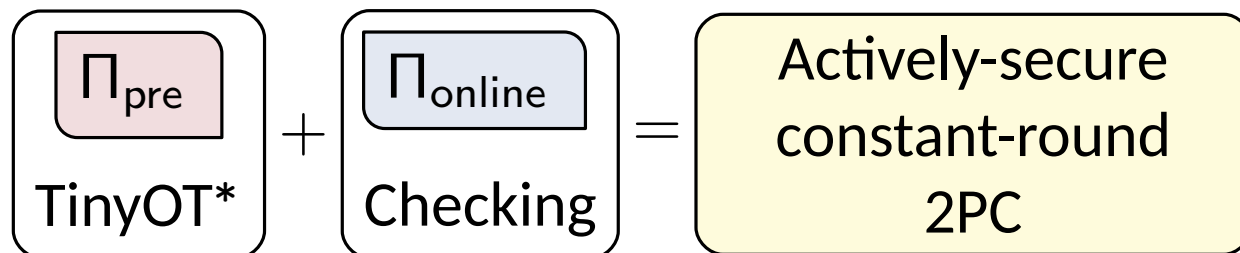
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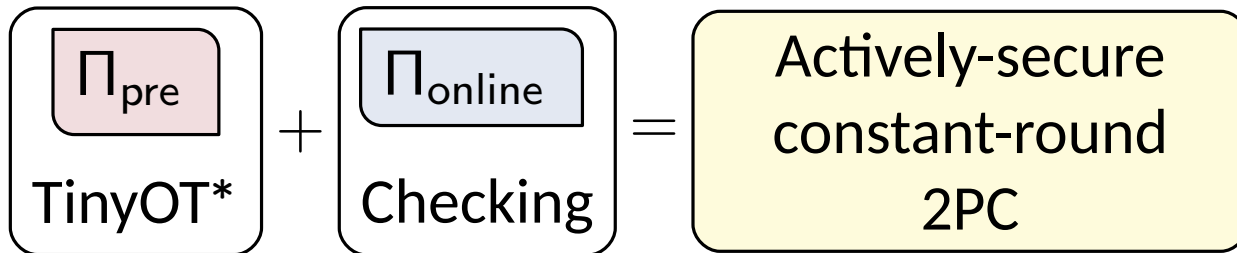
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## ■ What about the malicious world?

Cut-and-Choose [LP07,NO09,HKE13,NST17,...]	Authenticated Garbling [WRK17,KRRW18]	PCGs [BCG+19, YWL+20, CRR21,...]	AG from PCG [DILO22]
$O(\rho\kappa)$ or $O(\frac{\rho\kappa}{\log C})$	$\Pi_{\text{pre}}: 13\kappa + 8\rho$ $\Pi_{\text{online}}: 2\kappa + 1$		$\mathcal{F}_{\text{VOLE-hyb.}} 2\kappa + 8\rho$ $\mathcal{F}_{\text{DAMT-hyb.}} 2\kappa + 4\rho$

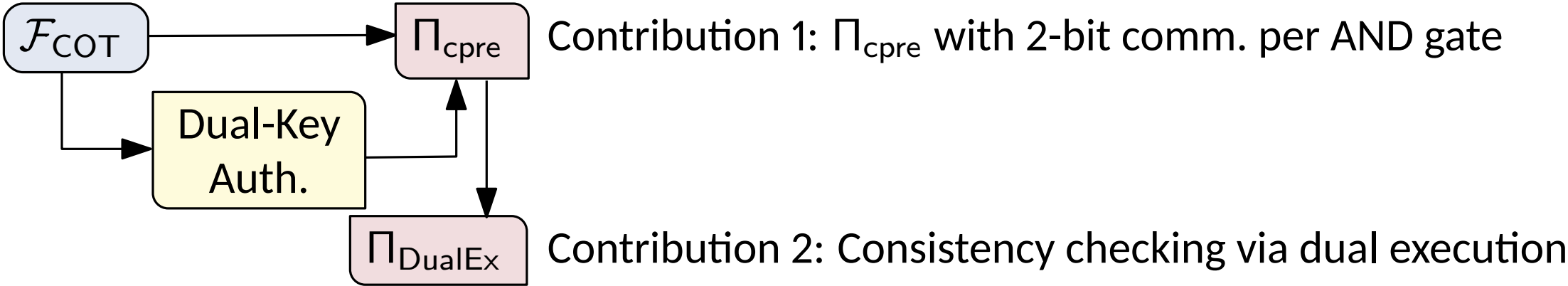


## Can we close the gap?

# Our Contributions

- Authenticated garbling with one-way comm. as small as semi-honest half-gates

2PC	Rounds		Communication per AND gate	
	Prep.	Online	one-way (bits)	two-way (bits)
Half-gates	1	2	$2\kappa$	$2\kappa$
HSS-PCG	8	2	$8\kappa + 11$ (4.04 $\times$ )	$16\kappa + 22$ (8.09 $\times$ )
KRRW-PCG	4	4	$5\kappa + 7$ (2.53 $\times$ )	$8\kappa + 14$ (4.05 $\times$ )
DILO	7	2	$2\kappa + 8\rho + 1$ (2.25 $\times$ )	$2\kappa + 8\rho + 5$ (2.27 $\times$ )
This work	8	3	$2\kappa + 5$ ( $\approx 1\times$ )	$4\kappa + 10$ (2.04 $\times$ )
This work+DILO	8	2	$2\kappa + 3\rho + 2$ (1.48 $\times$ )	$2\kappa + 3\rho + 4$ ( $\approx 1.48\times$ )



# Semi-Honest Garbled Circuit

$$\Lambda_k := \lambda_k \oplus (\Lambda_i \oplus \lambda_i) \cdot (\Lambda_j \oplus \lambda_j)$$

i	j	k	$i \text{---} \text{AND} \text{---} j$	$\Lambda_i$	$\Lambda_j$	ciphertext
0	0	0		0	0	$H(L_{i,0}, L_{j,0}) \oplus L_{k,0} \oplus (\lambda_i \cdot \lambda_j \oplus \lambda_k) \Delta_A$
0	1	0	Encrypt	0	1	$H(L_{i,0}, L_{j,1}) \oplus L_{k,0} \oplus (\lambda_i \cdot \bar{\lambda}_j \oplus \lambda_k) \Delta_A$
1	0	0	& Sort	1	0	$H(L_{i,1}, L_{j,0}) \oplus L_{k,0} \oplus (\bar{\lambda}_i \cdot \lambda_j \oplus \lambda_k) \Delta_A$
1	1	1	→	1	1	$H(L_{i,1}, L_{j,1}) \oplus L_{k,0} \oplus (\bar{\lambda}_i \cdot \bar{\lambda}_j \oplus \lambda_k) \Delta_A$

$$c_k := \text{lsb}(L_{k,0})$$



Sample  $\{\lambda_w, L_{w,0}\}$   
and  $\Delta_A$

- $L_{w,0} \oplus L_{w,1} = \Delta_A$
- $\text{lsb}(\Delta_A) = 1$

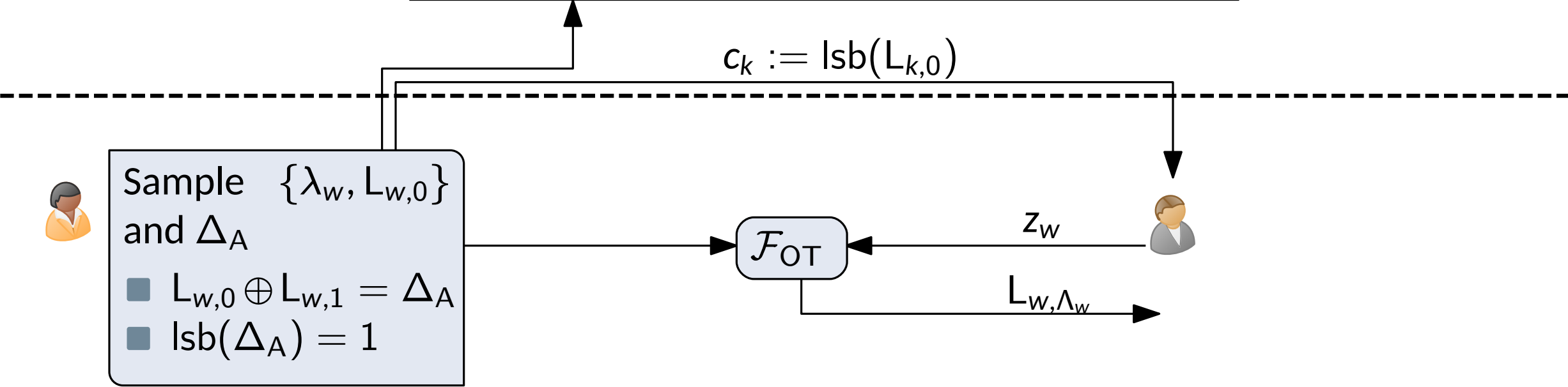




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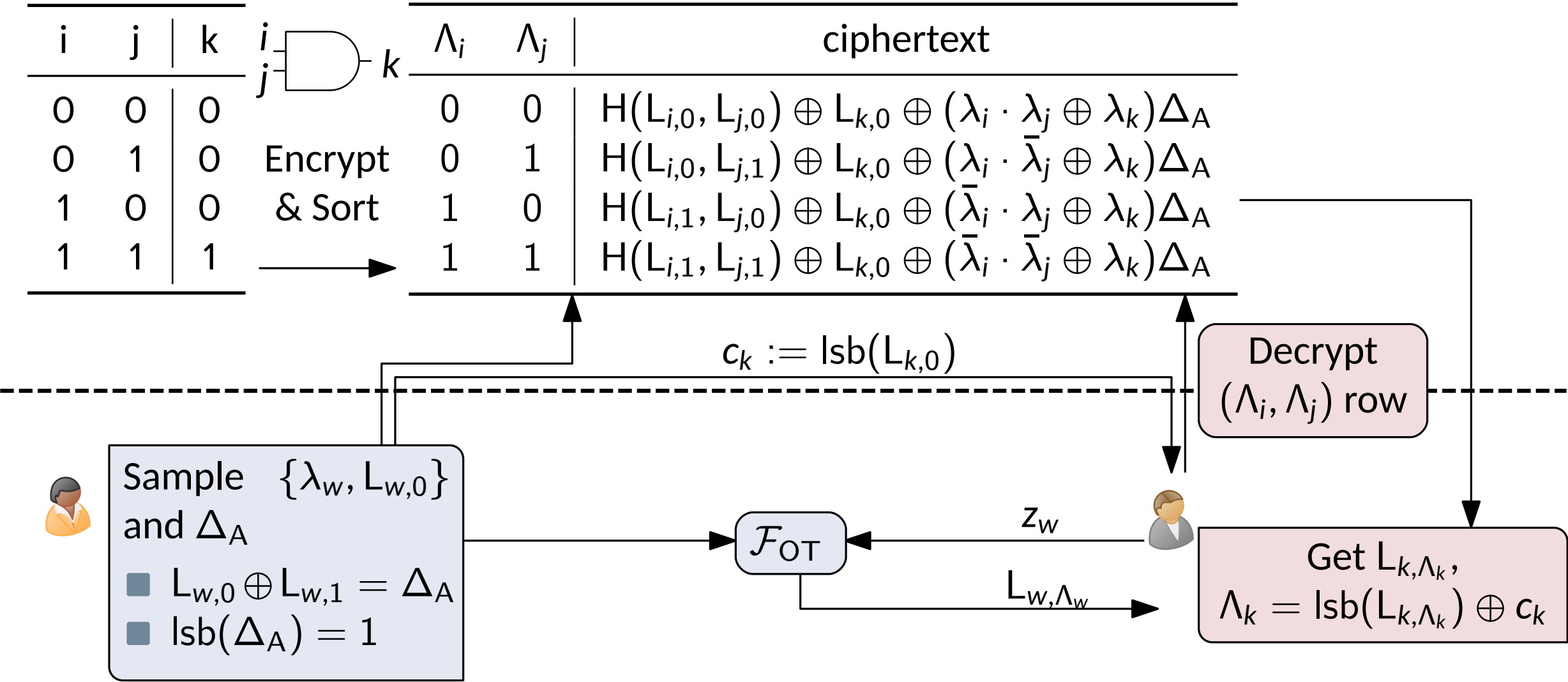
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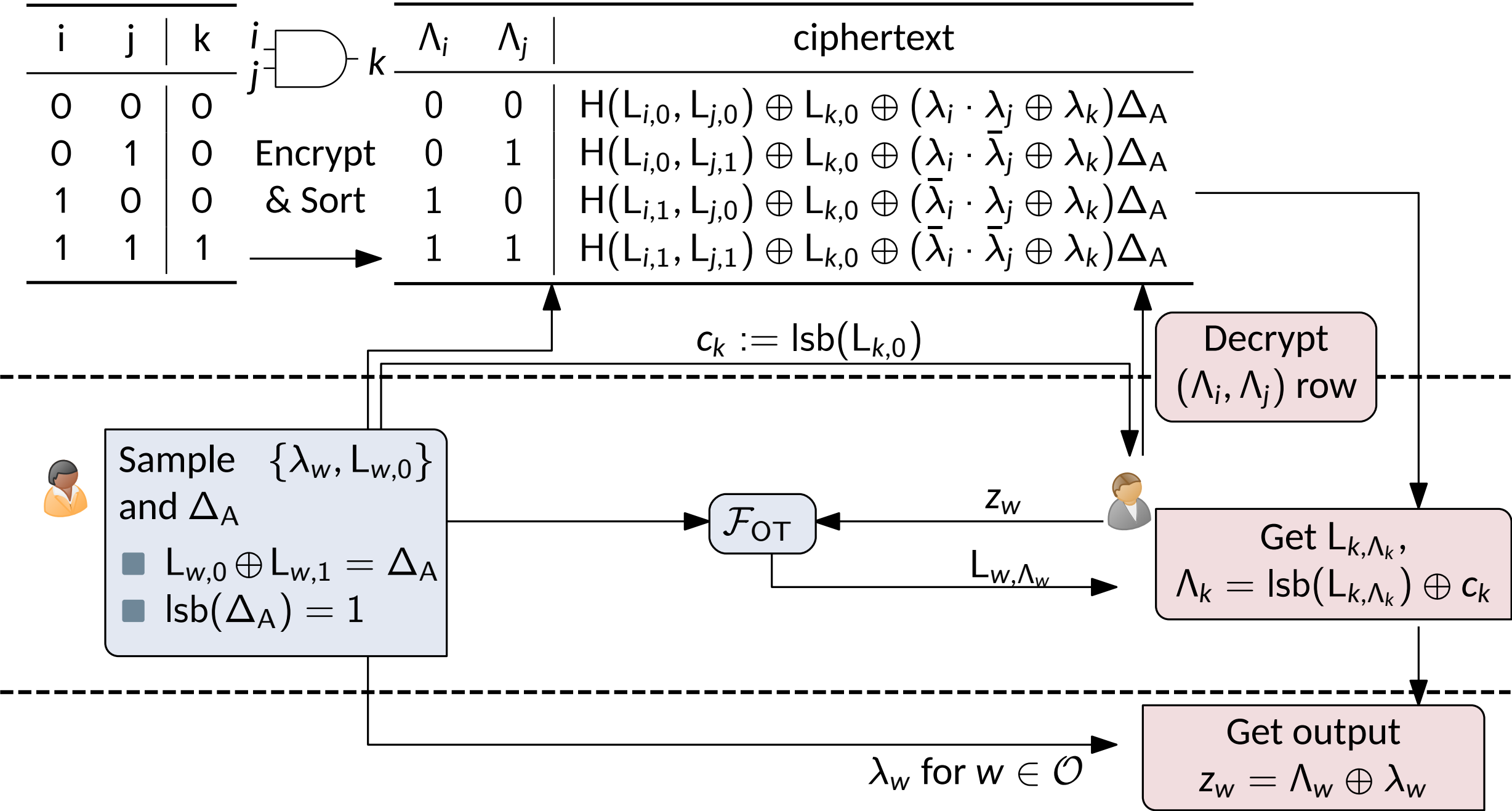
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# Authenticated Garbling = Distributed Garbling + Checking



controls garbling so it can

- selective-failure on  $\Lambda := z \oplus \lambda \Rightarrow$  Secret share  $\lambda := a \oplus b$
- garble different logic  $\Rightarrow$  Add IT-MAC, equality check, etc.

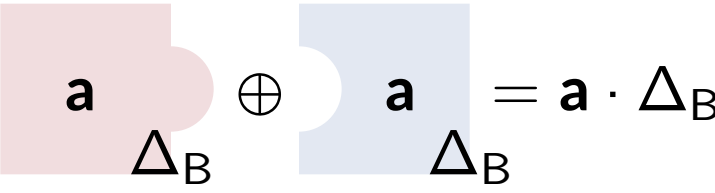
$\Lambda_i$	$\Lambda_j$	Masked $L_{k,\Lambda_k}$
0	0	$L_{k,0} \oplus (\lambda_i \cdot \lambda_j \oplus \lambda_k) \Delta_A$
0	1	$L_{k,0} \oplus (\lambda_i \cdot \bar{\lambda}_j \oplus \lambda_k) \Delta_A$
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- We need preprocessing information to complete garbling





  
 $a, \hat{a}, \Delta_A$   


samples

$\mathcal{F}_{\text{pre}} \quad [a], [\hat{a}], [b], [\hat{b}]$

$\Delta_A, \Delta_B$

$\hat{a}_k \oplus \hat{b}_k = \lambda_i \cdot \lambda_j \text{ for } (i, j, k, \wedge)$

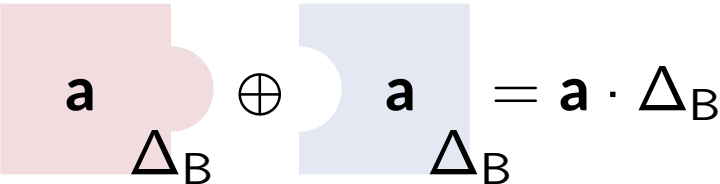
  
 $b, \hat{b}, \Delta_B$   


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$$a_{\Delta_B} \oplus a_{\Delta_B} = a \cdot \Delta_B$$

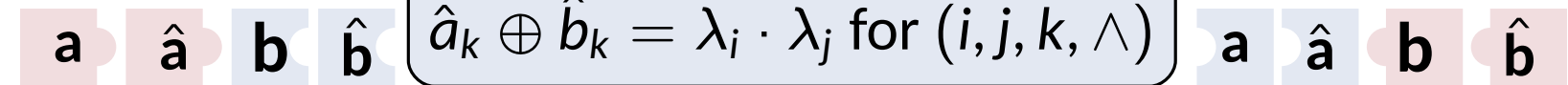

 $a, \hat{a}, \Delta_A$

$\mathcal{F}_{\text{pre}}$ 

samples  
 $[a], [\hat{a}], [b], [\hat{b}]$   
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$\hat{a}_k \oplus \hat{b}_k = \lambda_i \cdot \lambda_j \text{ for } (i, j, k, \wedge)$


 $b, \hat{b}, \Delta_B$



$\Lambda_i$	$\Lambda_j$	Alice's GC	Bob's GC
0	0	$L_{k,0} \oplus K[\Lambda_{00}]$	$M[\Lambda_{00}]$
0	1	$L_{k,0} \oplus K[\Lambda_{01}]$	$M[\Lambda_{01}]$
1	0	$L_{k,0} \oplus K[\Lambda_{10}]$	$M[\Lambda_{10}]$
1	1	$L_{k,0} \oplus K[\Lambda_{11}]$	$M[\Lambda_{11}]$

$$\Lambda_k \cdot \Delta_A := \lambda_k \cdot \Delta_A \oplus (\Lambda_i \oplus \lambda_i) \cdot (\Lambda_j \oplus \lambda_j) \cdot \Delta_A$$

$$= \lambda_k \cdot \Delta_A \oplus \dots \oplus (\hat{a}_k \oplus \hat{b}_k) \cdot \Delta_A$$

Free-XOR GC  $\Rightarrow$   
 $|\Delta_A| = \kappa \approx 128$

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$$a \oplus a = a \cdot \Delta_B$$

$a, \hat{a}, \Delta_A$

$\mathcal{F}_{\text{pre}}$   
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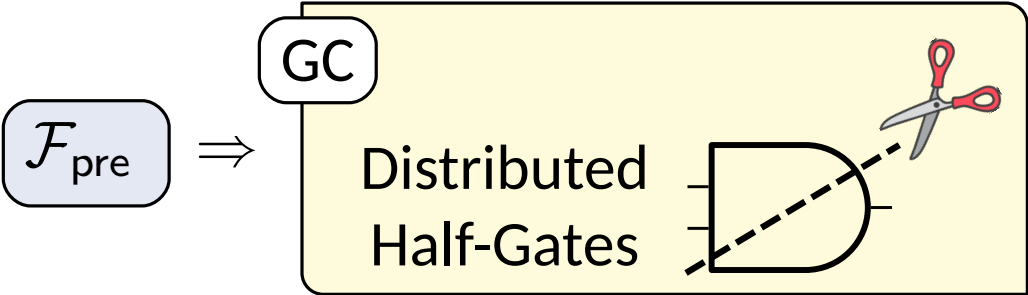
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0	0	$L_{k,0} \oplus M[\Lambda_{00}]$	$K[\Lambda_{00}]$
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1	0	$L_{k,0} \oplus M[\Lambda_{10}]$	$K[\Lambda_{10}]$
1	1	$L_{k,0} \oplus M[\Lambda_{11}]$	$K[\Lambda_{11}]$

IT-MAC Soundness  $\Rightarrow$   
 $|\Delta_B| = \rho \approx 40$

# KRRW18: Distributed Half-Gates Garbling + Equality Checking

- Distributed half-gates garbling is fully compatible with  $\mathcal{F}_{\text{pre}}$



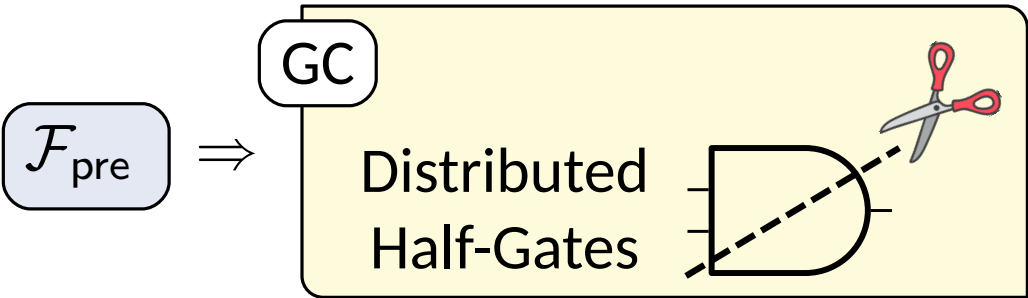
$$\begin{aligned} \Lambda_k \cdot \Delta_A &:= \lambda_k \cdot \Delta_A \oplus (\Lambda_i \oplus \lambda_i) \cdot (\Lambda_j \oplus \lambda_j) \cdot \Delta_A \\ &= \underbrace{(\lambda_k \oplus \lambda_i \lambda_j) \cdot \Delta_A}_{\text{already shared}} \oplus \underbrace{\Lambda_i \lambda_j \cdot \Delta_A}_{G_{k,0}} \oplus \underbrace{\Lambda_j (\Lambda_i \oplus \lambda_i) \cdot \Delta_A}_{G_{k,1}} \end{aligned}$$

$4\kappa$ bits/AND WRK17	$\Rightarrow$	$2\kappa + 1$ bits/AND KRRW18
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4κ bits/AND  
WRK17

⇒

2κ + 1 bits/AND  
KRRW18

- **b**-mask removes selective failure, now only need to check correct AND correlation

$\Lambda_k \oplus \lambda_k = (\Lambda_i \oplus \lambda_i) \cdot (\Lambda_j \oplus \lambda_j)$ 
 $\Leftrightarrow e_k := \Lambda_k \oplus \lambda_k \oplus \Lambda_i \Lambda_j \oplus \Lambda_i \lambda_j \oplus \lambda_i \Lambda_j \oplus \lambda_i \lambda_j = 0$

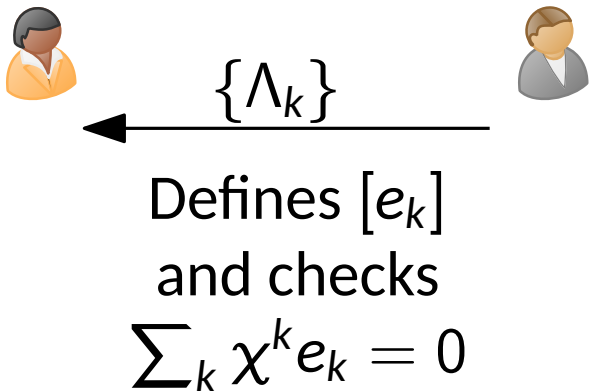
Check:

- Evaluator sends  $\{\Lambda_w\}$  for all AND gates
- The checking equation reduces to equality check
- Use random linear combination to reduce comm.

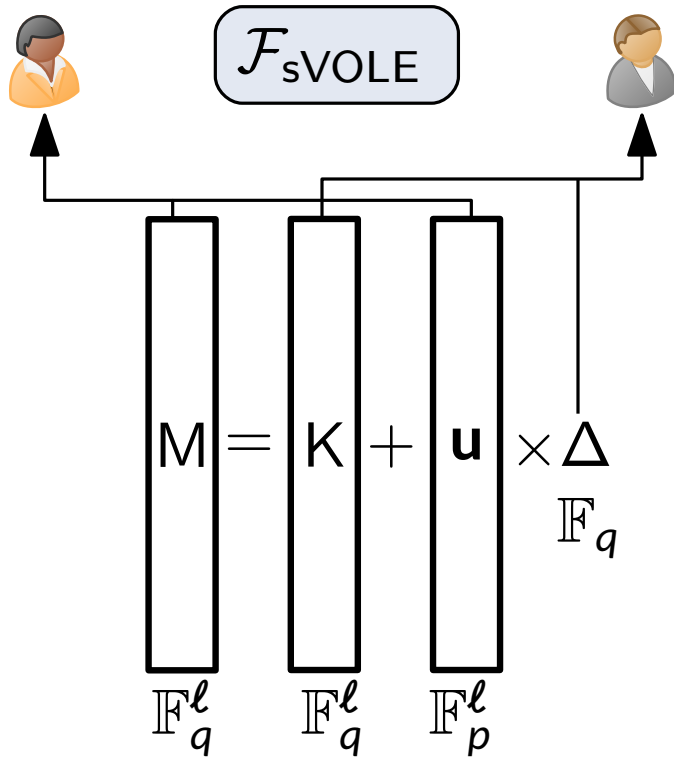
4ρ bits/AND  
WRK17

⇒

0 bits/AND  
KRRW18

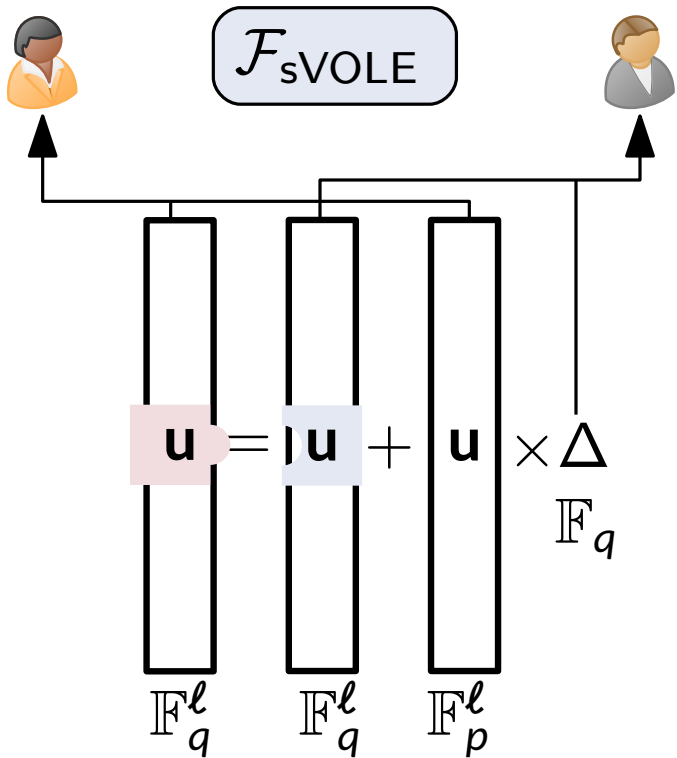


# Efficient COT/sVOLE and Designated Verifier Zero Knowledge



- Efficient protocol for  $\mathcal{F}_{\text{COT}}$ ,  $\mathcal{F}_{\text{sVOLE}}$  with sublinear comm. and linear comp. from LPN [YWL+20, CRR21, ...]
- We refer the  $\mathbb{F}_p = \mathbb{F}_2$  variant of  $\mathcal{F}_{\text{sVOLE}}$  as  $\mathcal{F}_{\text{COT}}$

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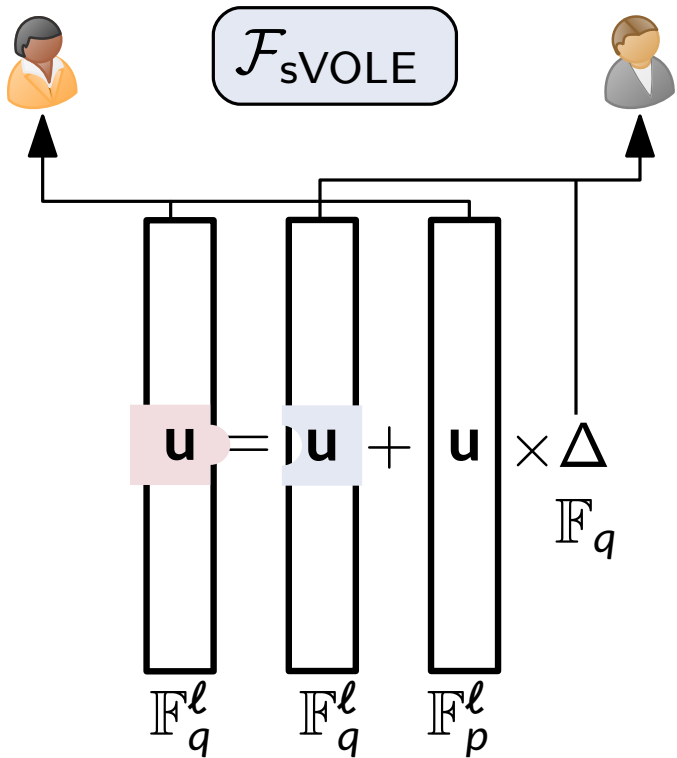
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Derandomization operation: Fix

$\delta := \mathbf{x} \oplus \mathbf{u}$

$\mathbf{x} := \mathbf{u} \oplus \delta \cdot \Delta$

# Efficient COT/sVOLE and Designated Verifier Zero Knowledge



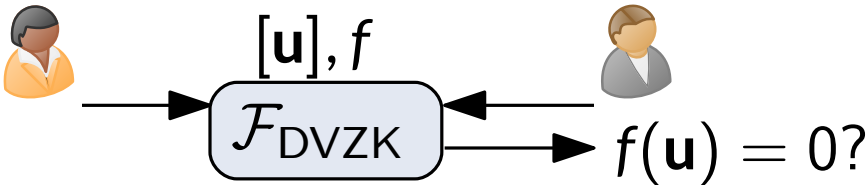
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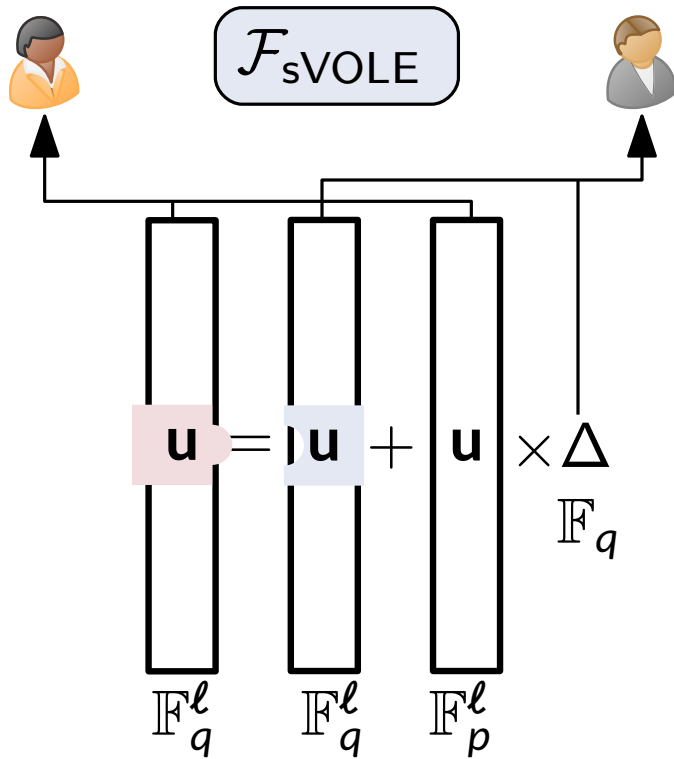
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$\mathbf{x} := \mathbf{u}$ 
 $\mathbf{x} := \mathbf{u} \oplus \delta \cdot \Delta$

- Efficient proof for deg- $d$  relations on  $\mathbf{u}$  [DIO21, YSWW21, ...]

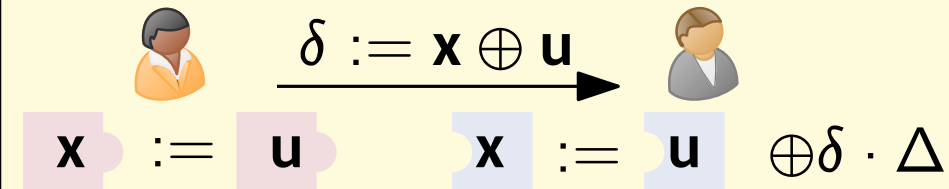


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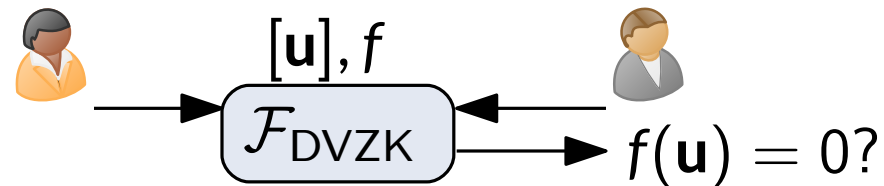


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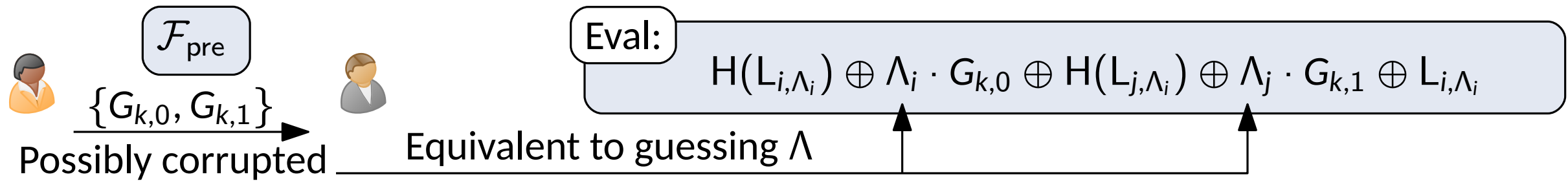


- Efficient proof for deg- $d$  relations on  $u$  [DIO21, YSWW21, ...]



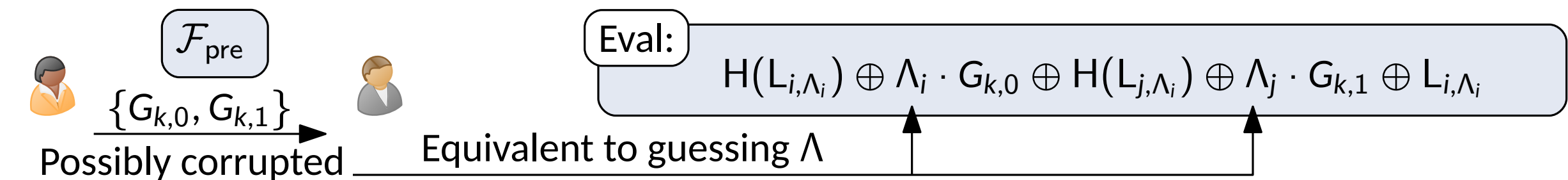
- In DILO, those PCG correlations are called “simple correlations”
- Unfortunately, we still don’t have an efficient direct  $\mathcal{F}_{\text{pre}}$  PCG construction
- The closest is the  $\mathcal{F}_{\text{DAMT}}$  correlation generated from Ring-LPN, but with  $\rho$ -time overhead

# Prior Art: DILO



- Garbler can only guess once
- If  $\mathbf{b}$  is uniformly random, then guessing leaks no information
- If #Guess is too large, then abort happens overwhelmingly, if #Guess is too little, then we don't require much entropy from  $\mathbf{b}$

# Prior Art: DILO



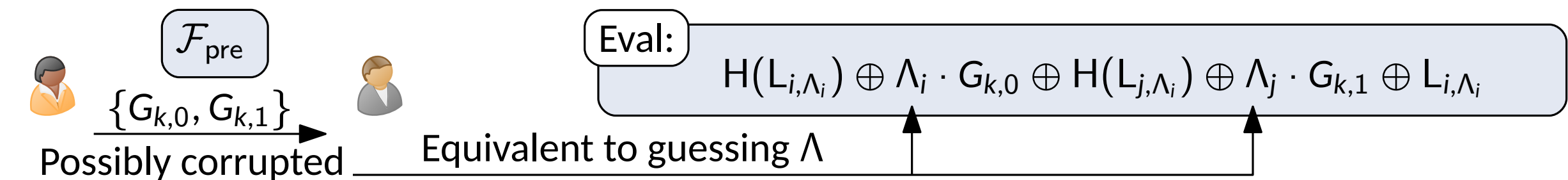
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## DILO Observation 1

It suffices for  $\mathbf{b}$  to be  $\rho$ -wise independent

- #Guess  $\leq \rho$ : Abort is input-independent
- #Guess  $> \rho$ : Abort is overwhelming

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## DILO Observation 2

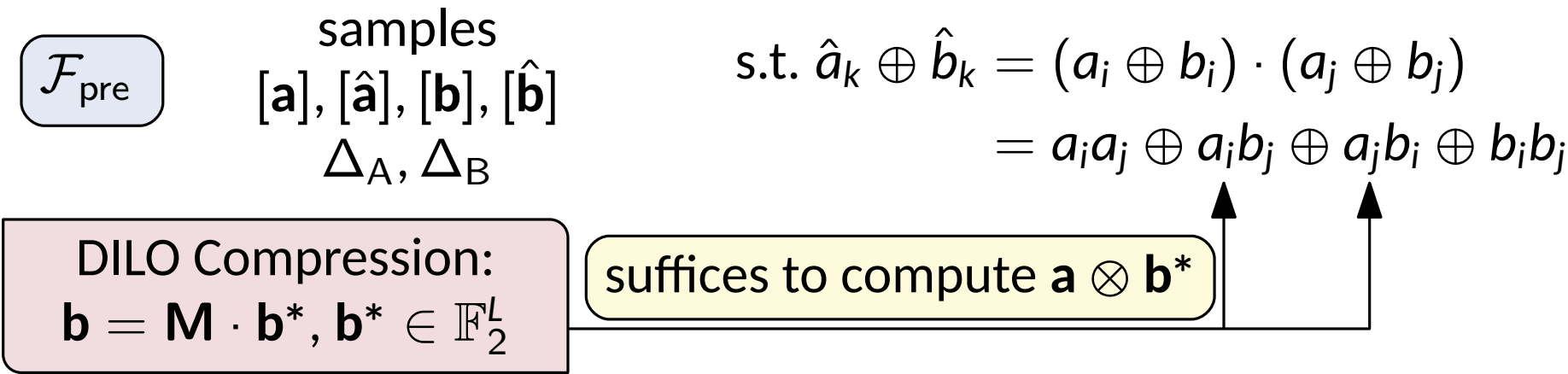
We can construct  $\rho$ -wise independent  $\mathbf{b}$  by linear expansion

$$\mathbf{b} = \mathbf{M} \times \mathbf{b}^*$$

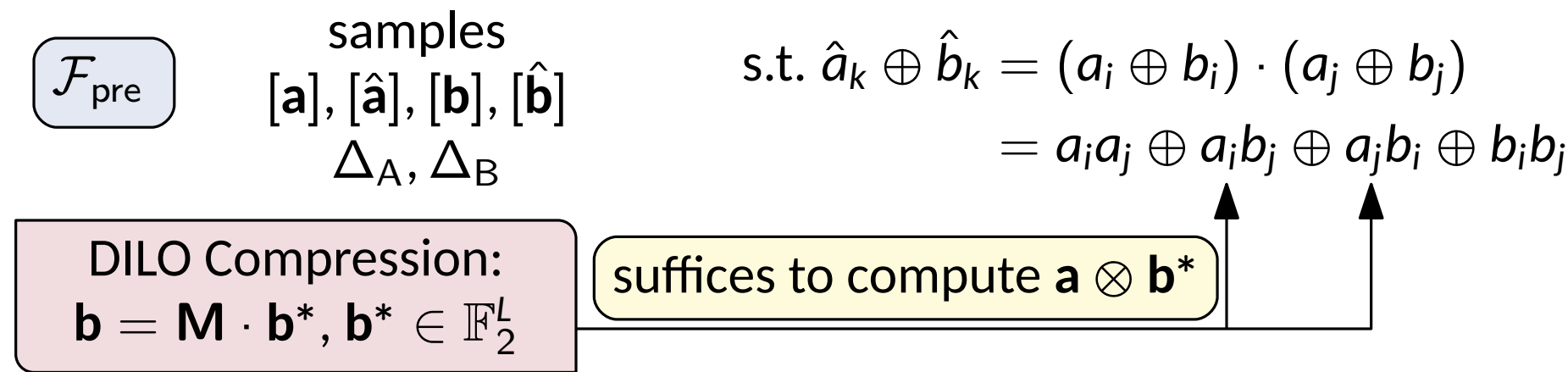
- For  $L = O(\rho \cdot \log(\frac{n}{\rho}))$ , a uniformly random  $\mathbf{M}$  suffices
- We can encode  $\mathbf{b}^*$  in  $\mathcal{F}_{\text{COT}}$  global keys



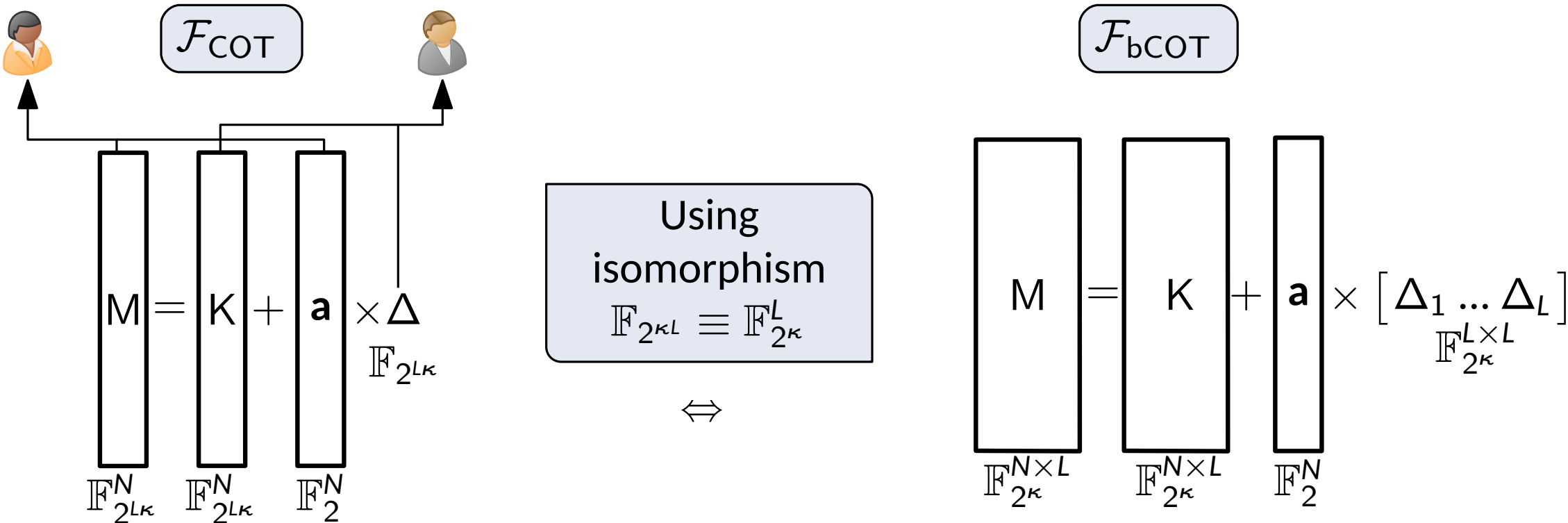
# DILO Implementation of $\mathcal{F}_{\text{cpre}}$ : Encoding $\mathbf{b}^*$ as Global Keys



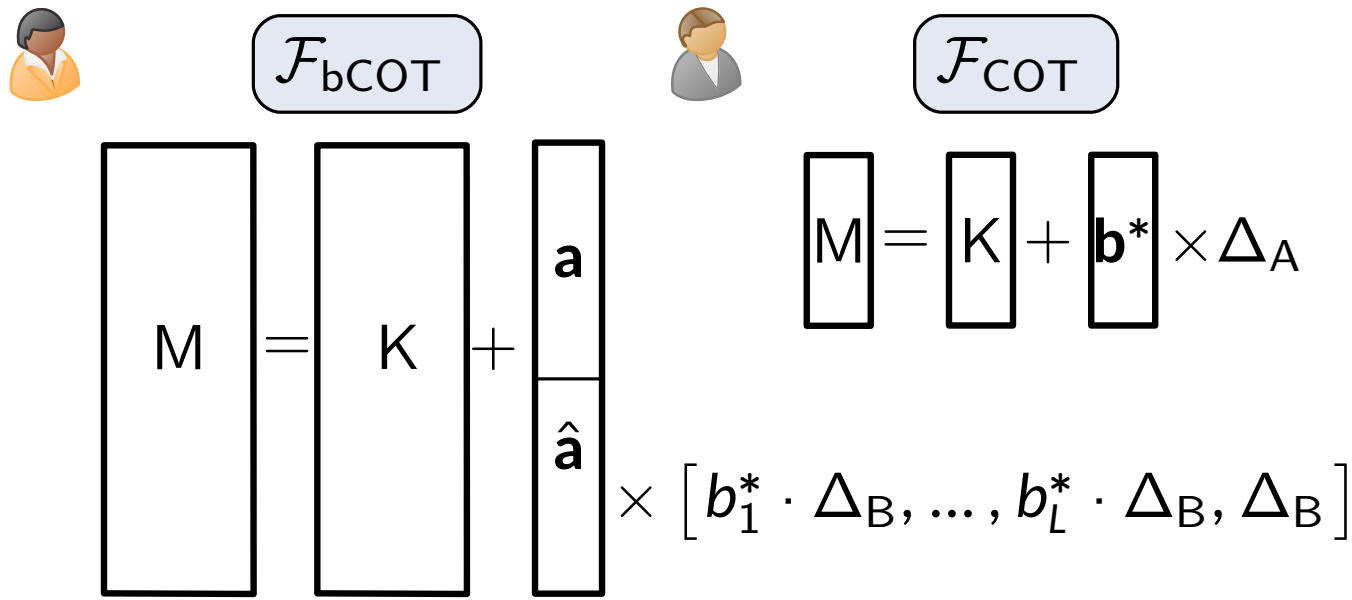
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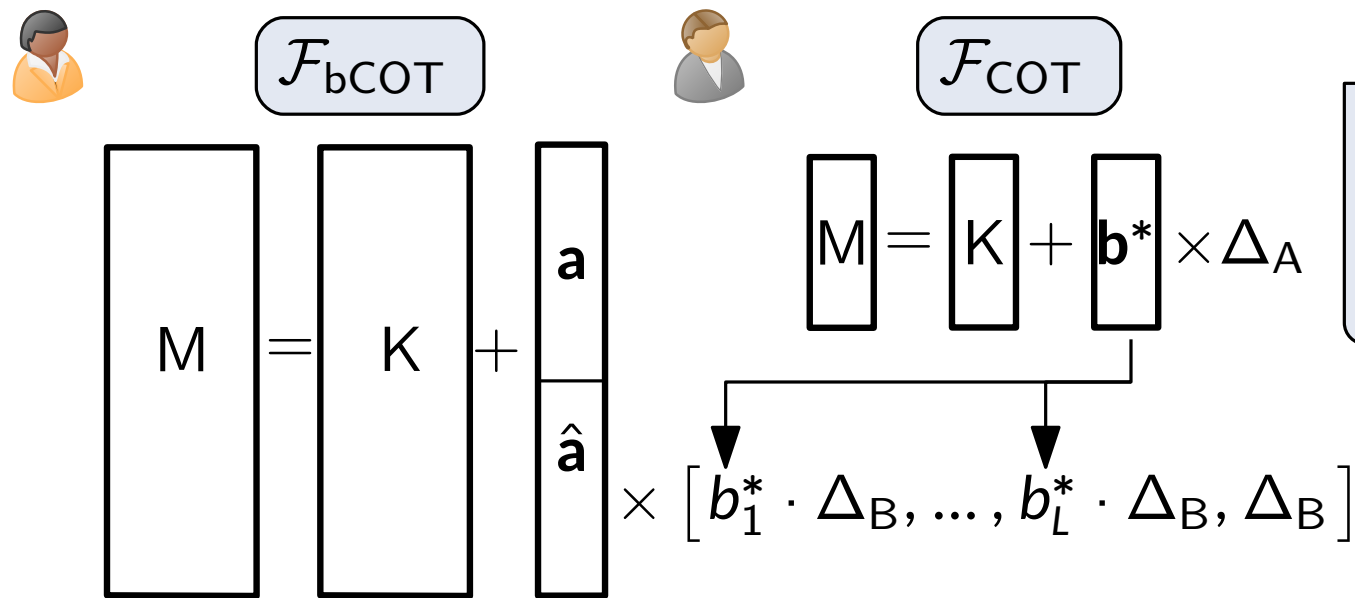
- COT can be extended to block COT, preserving PCG efficiency



# DILO Implementation of $\mathcal{F}_{\text{cpre}}$ : Computing $\hat{b}_k$

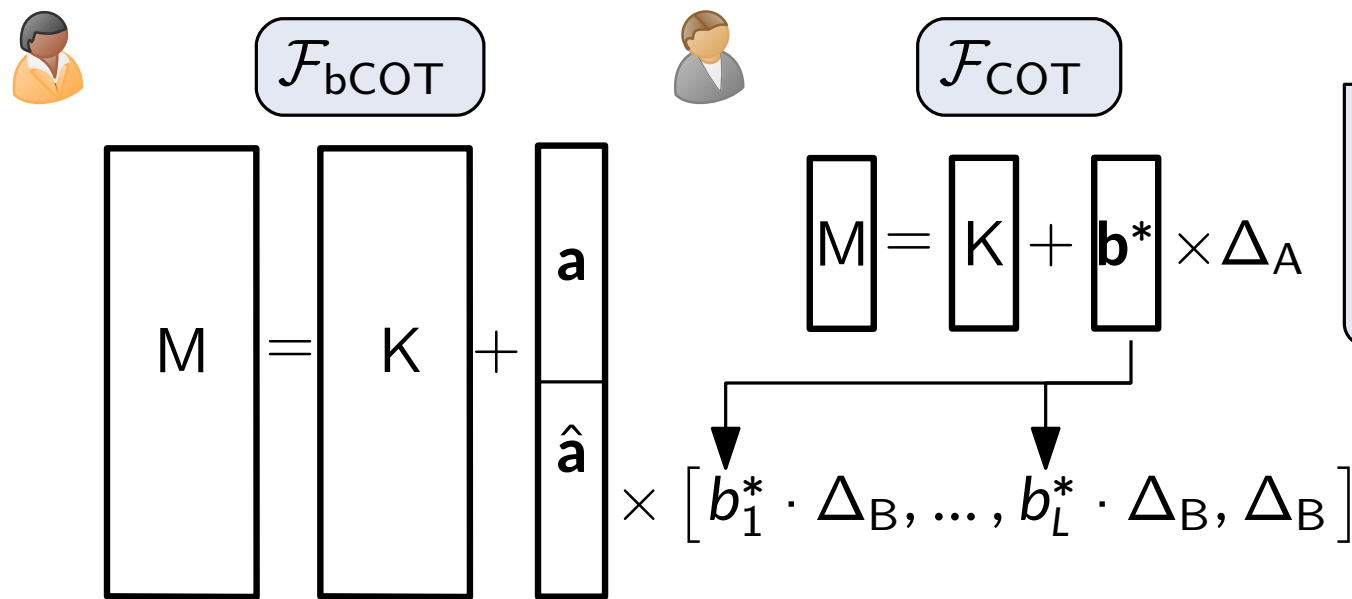


# DILO Implementation of $\mathcal{F}_{\text{cpre}}$ : Computing $\hat{b}_k$

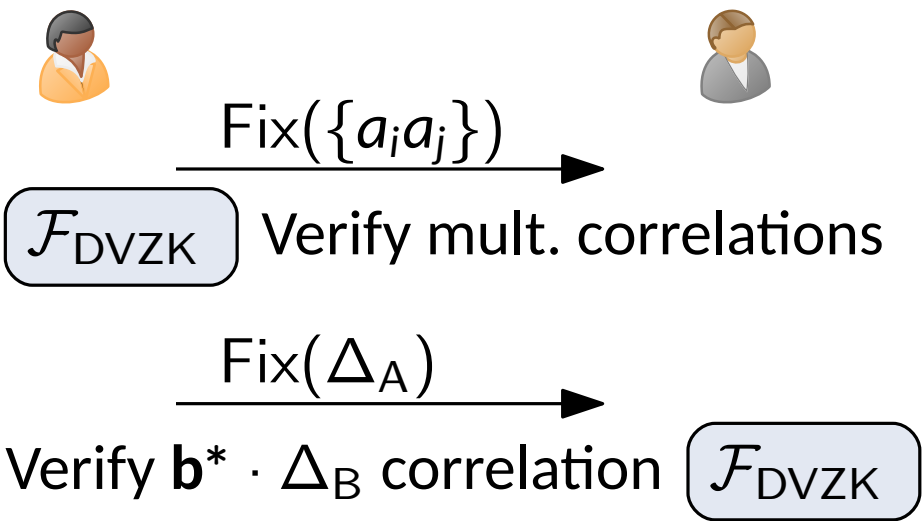


- $[a]_{b_j^* \Delta_B} \equiv [ab_j]_{\Delta_B}$
- By linearity on IT-MAC, we can get  $[a_i b_j]_{\Delta_B}$  for any  $i, j$

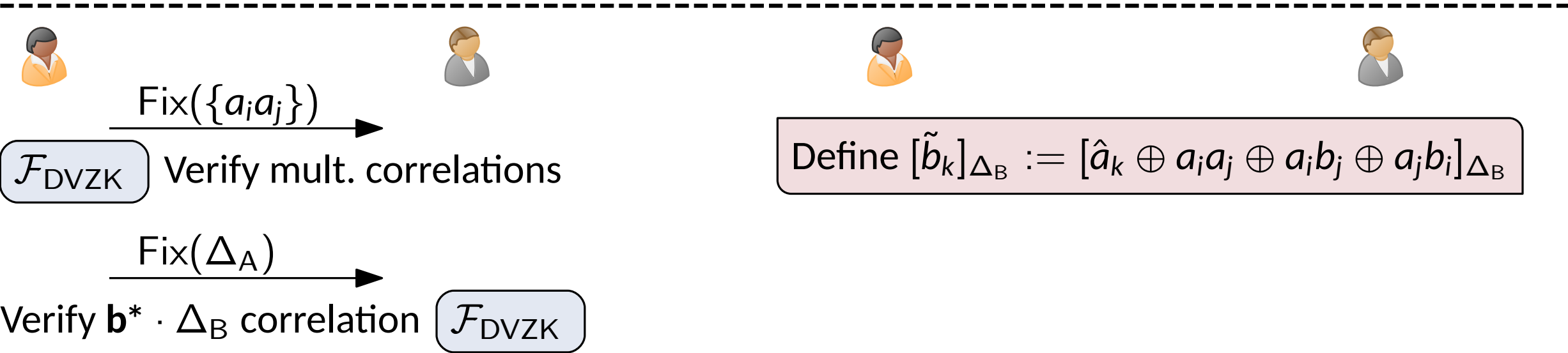
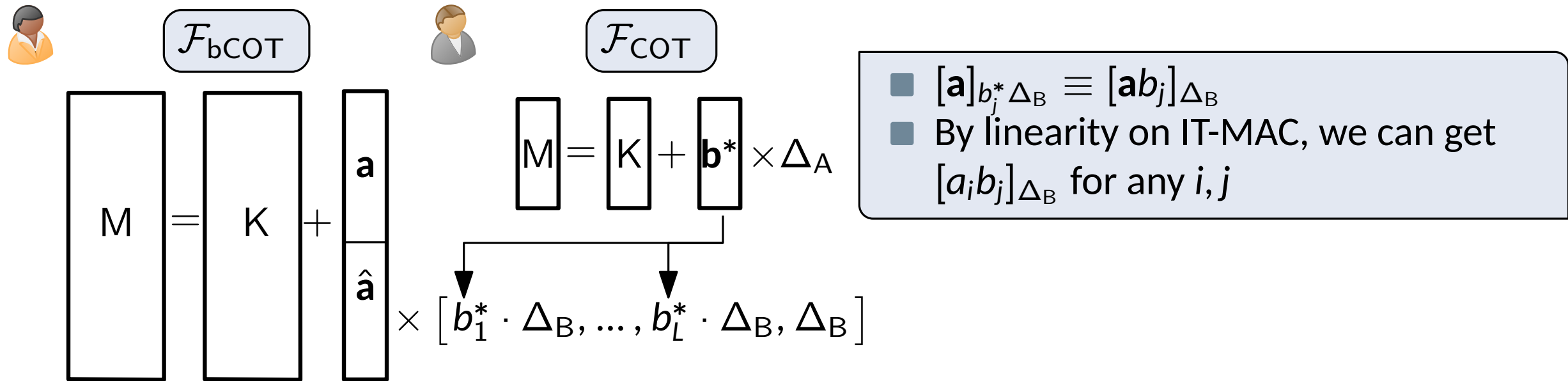
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# DILO Implementation of $\mathcal{F}_{\text{cpre}}$ : Computing $\hat{b}_k$



# DILO Implementation of $\mathcal{F}_{\text{cpre}}$ : Computing $\hat{b}_k$



$\mathcal{F}_{\text{bcOT}}$

$$M = K + \begin{bmatrix} a \\ \hat{a} \end{bmatrix} \times [b_1^* \cdot \Delta_B, \dots, b_L^* \cdot \Delta_B, \Delta_B]$$



$\mathcal{F}_{\text{COT}}$

$$M = K + \mathbf{b}^* \times \Delta_A$$

$$\times [b_1^* \cdot \Delta_B, \dots, b_L^* \cdot \Delta_B, \Delta_B]$$

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$\text{Fix}(\{a_i a_j\})$

$\mathcal{F}_{\text{DVZK}}$

Verify mult. correlations

$\text{Fix}(\Delta_A)$

Verify  $\mathbf{b}^* \cdot \Delta_B$  correlation

$\mathcal{F}_{\text{DVZK}}$



Define  $[\tilde{b}_k]_{\Delta_B} := [\hat{a}_k \oplus a_i a_j \oplus a_i b_j \oplus a_j b_i]_{\Delta_B}$

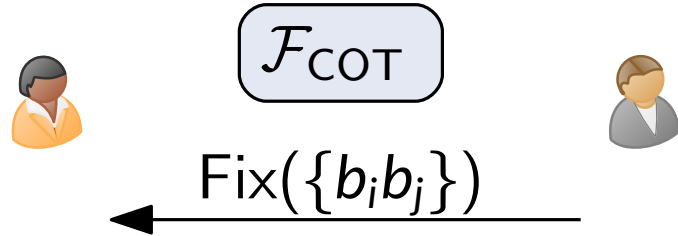
$$m_{k,1} := \tilde{b}_k$$

$$\tilde{b}_k := (\tilde{b}_k \oplus \tilde{b}_k) \cdot \Delta_B^{-1}$$

$$\hat{b}_k = b_i b_j \oplus \tilde{b}_k$$

# DILO Implementation of $\mathcal{F}_{\text{cpre}}$ : Authenticating $\hat{b}_k$ (Under $\Delta_A$ )

- It suffices to compute  $\tilde{b}_k$  since  $[\hat{b}_k]_{\Delta_A} = [\tilde{b}_k]_{\Delta_A} \oplus [b_i b_j]_{\Delta_A}$



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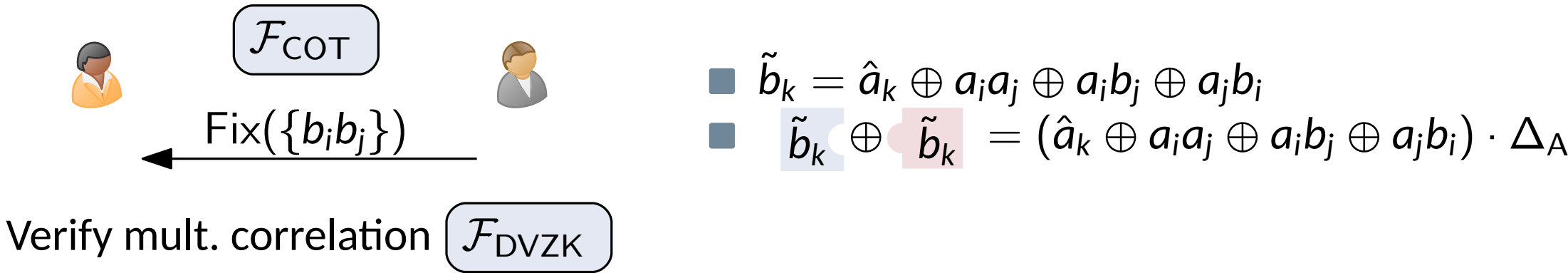
- $\tilde{b}_k \oplus \tilde{b}_k = (\hat{a}_k \oplus a_i a_j \oplus a_i b_j \oplus a_j b_i) \cdot \Delta_A$

Verify mult. correlation  $\mathcal{F}_{\text{DVZK}}$

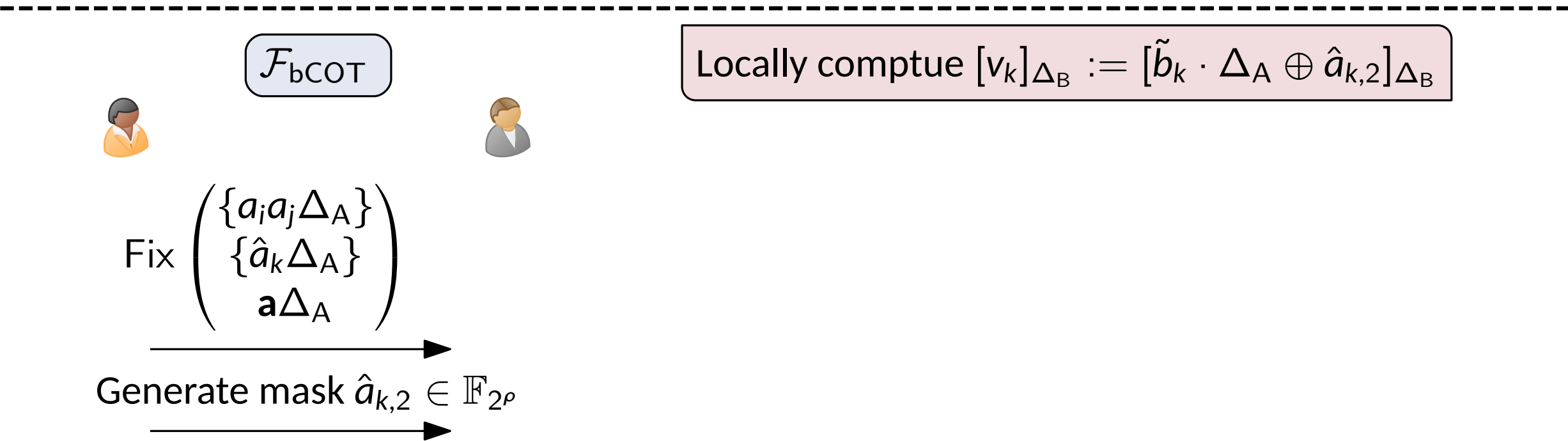


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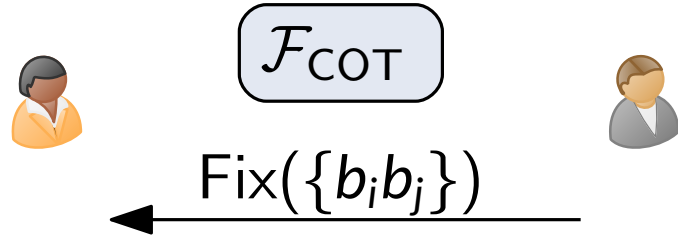


- $\tilde{b}_k = \hat{a}_k \oplus a_i a_j \oplus a_i b_j \oplus a_j b_i$
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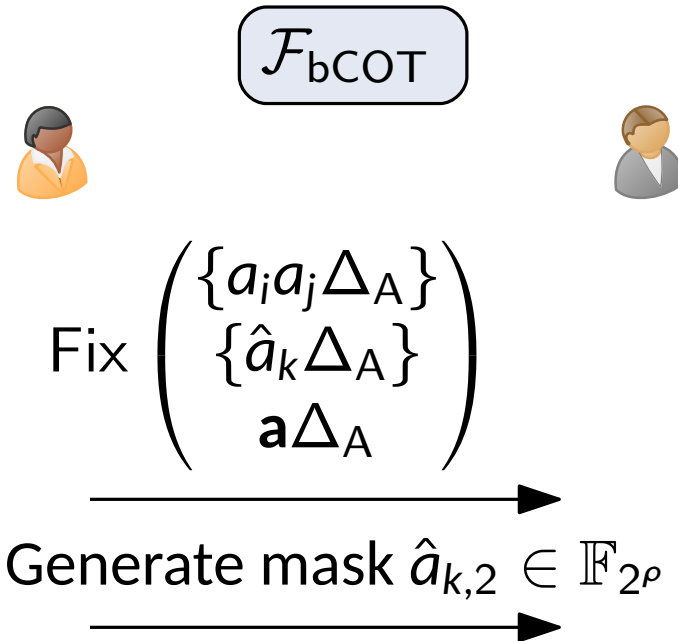
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Verify mult. correlation  $\mathcal{F}_{\text{DVZK}}$



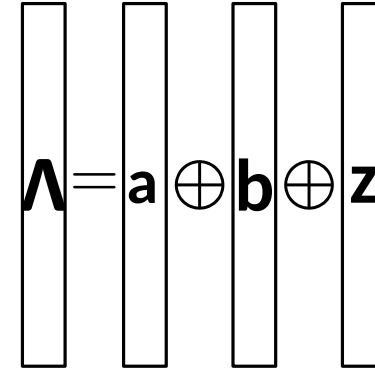
Locally compute  $[v_k]_{\Delta_B} := [\tilde{b}_k \cdot \Delta_A \oplus \hat{a}_{k,2}]_{\Delta_B}$

- Diagram showing a protocol step where a party (represented by an icon) sends  $m_{k,2} := \tilde{b}_k$  to another party (represented by an icon).
- $\tilde{b}_k := (\tilde{b}_k \oplus \tilde{b}_k) \cdot \Delta_B^{-1}$
  - $\hat{b}_k = \tilde{b}_k \oplus b_i b_j$

# The Online Protocol

## KRRW Check:

- Evaluator sends  $\{\Lambda_w\}$  for all AND gates
- The checking equation reduces to equality check
- Use random linear combination to reduce comm.


$$\Lambda = a \oplus b \oplus z$$

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**X**

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## DILO-WRK Check

$$\begin{aligned} \Lambda_k \cdot \Delta_B &:= \lambda_k \cdot \Delta_B \oplus (\Lambda_i \oplus \lambda_i) \cdot (\Lambda_j \oplus \lambda_j) \cdot \Delta_B \\ &= \lambda_k \cdot \Delta_B \oplus \Lambda_i \Lambda_j \cdot \Delta_B \oplus \Lambda_i \lambda_j \cdot \Delta_B \oplus \Lambda_j \lambda_i \cdot \Delta_B \oplus (\hat{a}_k \oplus \hat{b}_k) \cdot \Delta_B \end{aligned}$$

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$$\Lambda_k \cdot \Delta_B := \lambda_k \cdot \Delta_B \oplus (\Lambda_i \oplus \lambda_i) \cdot (\Lambda_j \oplus \lambda_j) \cdot \Delta_B$$

$$\Lambda_i(a_j \oplus b_j)\Delta_B = \Lambda_i b_j \Delta_B \oplus \Lambda_i K[a_j] \oplus \Lambda_i M[a_j]$$

$$= \lambda_k \cdot \Delta_B \oplus \Lambda_i \Lambda_j \cdot \Delta_B \oplus \Lambda_i \lambda_j \cdot \Delta_B \oplus \Lambda_j \lambda_i \cdot \Delta_B \oplus (\hat{a}_k \oplus \hat{b}_k) \cdot \Delta_B$$

# The Online Protocol

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$$\mathbf{b} = \mathbf{M} \times \mathbf{b}^*$$

## DILO-WRK Check

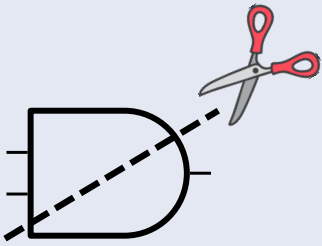
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## GC

Distributed  
Half-Gates



$2\kappa$  bits/AND

## AuthGC

$\Lambda_i$	$\Lambda_j$	Alice's AuthGC	Bob's AuthGC
0	0	$L_{k,0} \oplus M[\Lambda_{00}]$	$K[\Lambda_{00}]$
0	1	$L_{k,0} \oplus M[\Lambda_{01}]$	$K[\Lambda_{01}]$
1	0	$L_{k,0} \oplus M[\Lambda_{10}]$	$K[\Lambda_{10}]$
1	1	$L_{k,0} \oplus M[\Lambda_{11}]$	$K[\Lambda_{11}]$



$3\rho$  bits/AND

$= 2\kappa + 3\rho$  bits/AND

# Optimizing the Compressed Preprocessing Protocol

The overhead of DILO is  
 $5\rho + 2$  bits per AND gate



1 bit

$\text{Fix}(\{b_i b_j\})$

$\rho + 1$  bits

$\text{Fix}(\{a_i a_j\})$

$m_{k,1} := M[\tilde{b}_k]$

$4\rho$  bits

$\text{Fix} \begin{pmatrix} \{a_i a_j \Delta_A\} \\ \{\hat{a}_k \Delta_A\} \\ \mathbf{a} \Delta_A \end{pmatrix}$

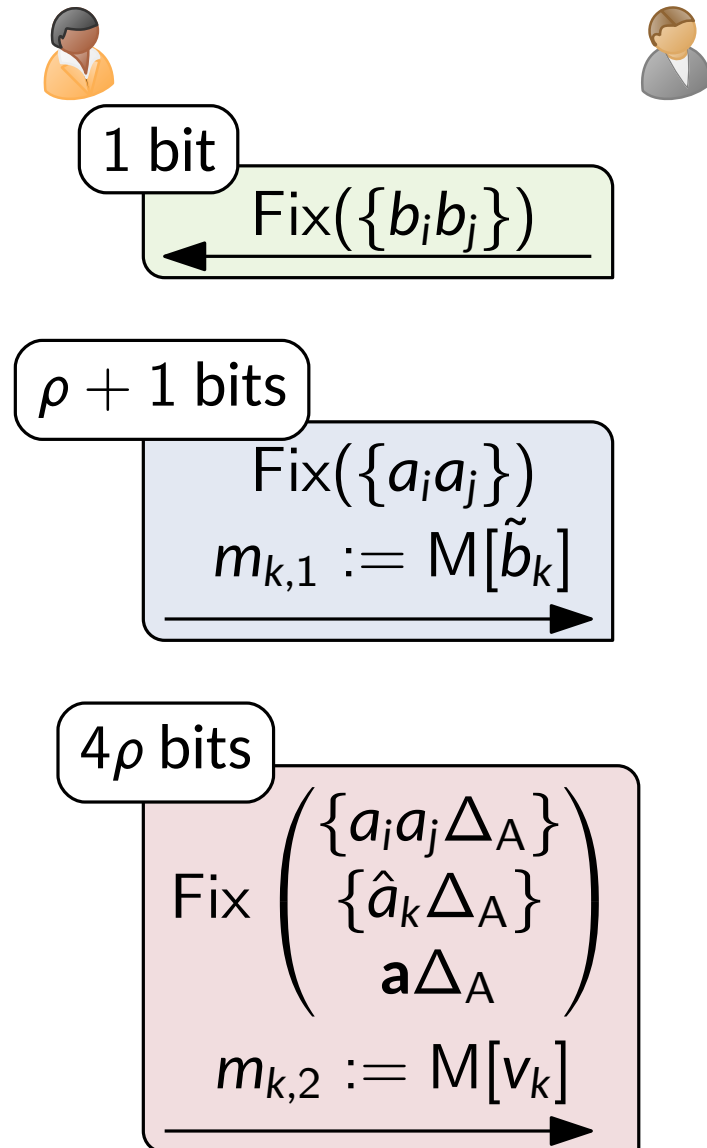
$m_{k,2} := M[v_k]$



# Optimizing the Compressed Preprocessing Protocol

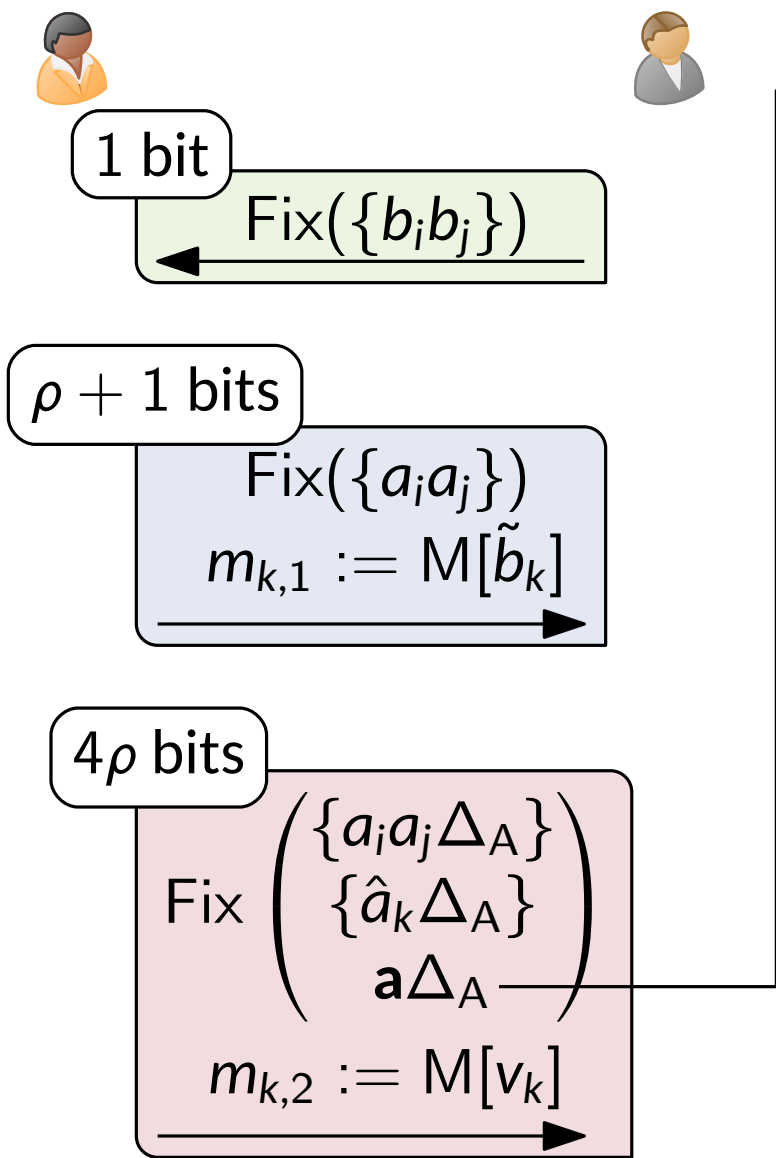
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- Why not call  $\text{Fix}(\tilde{b}_k)$  directly?
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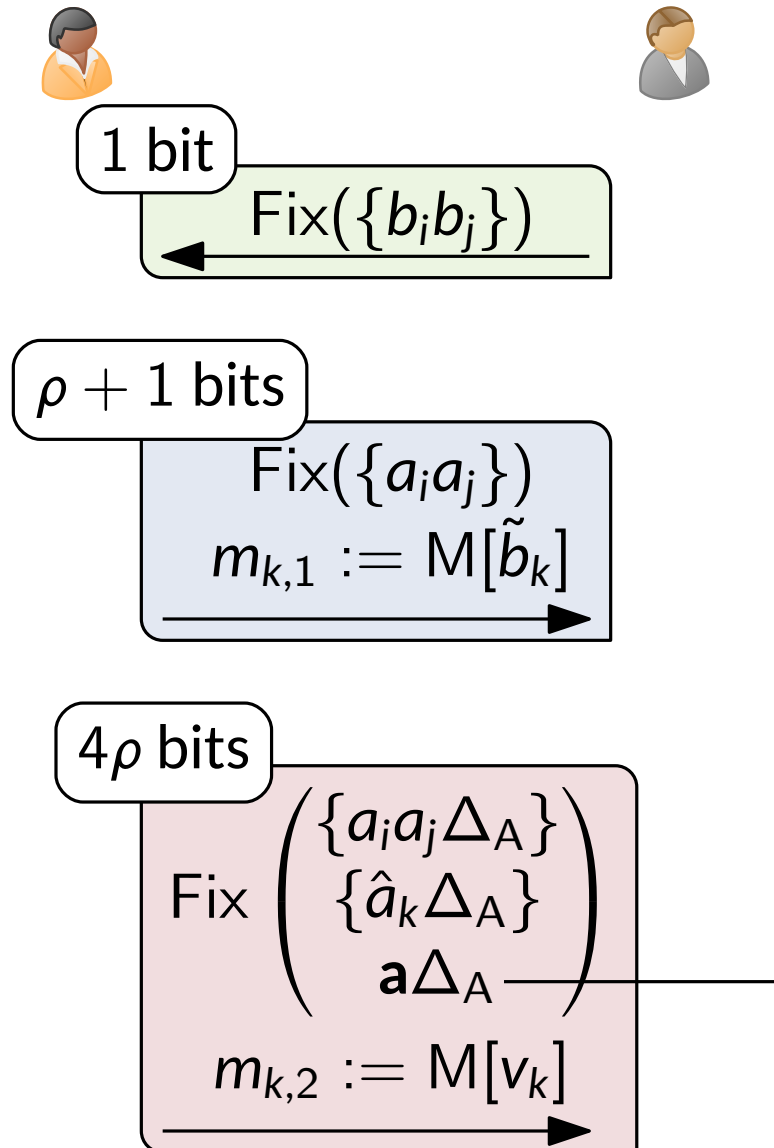


- Why not call  $\text{Fix}(\tilde{b}_k)$  directly?
- We need to detect against dishonest  $\text{Fix}()$  input
- $[\mathbf{a} \Delta_A]_{\Delta_B} \equiv [\mathbf{a}]_{\Delta_A \cdot \Delta_B}$
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- We denote it as  $\langle \mathbf{a} \rangle$

Dual Key Authentication

# Optimizing the Compressed Preprocessing Protocol





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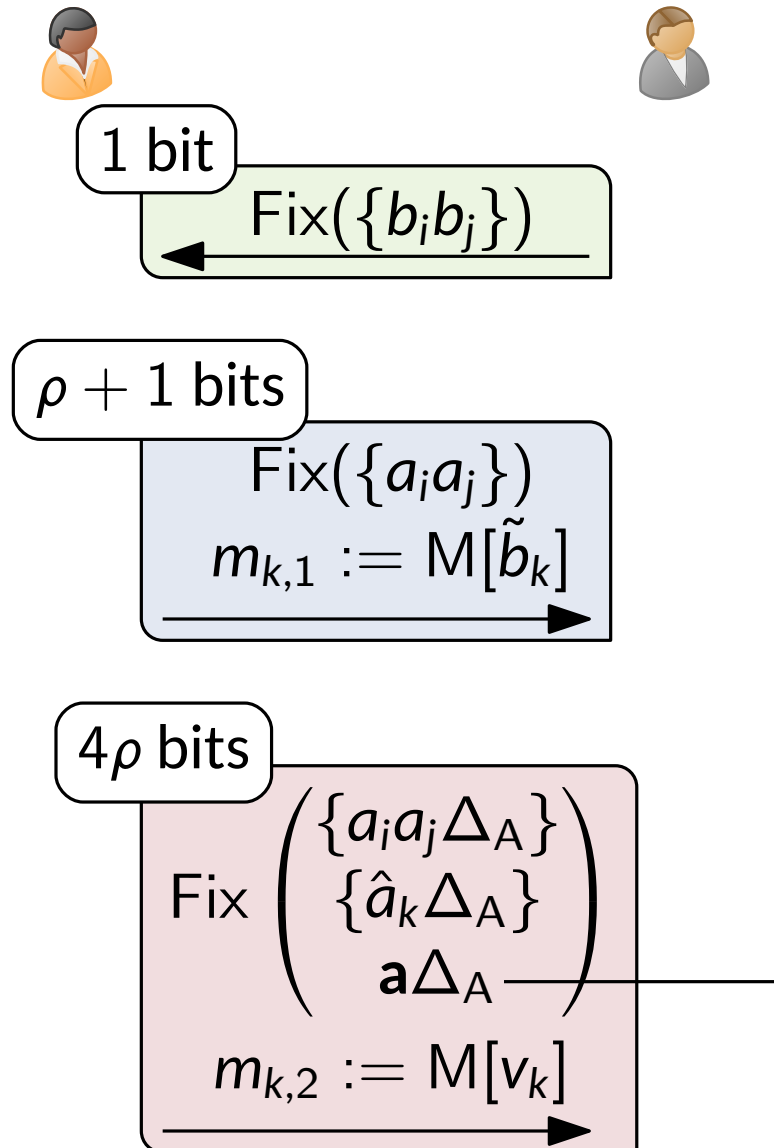
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- Suppose we generate  $\langle \tilde{b}_k \rangle$  and  $\langle r \rangle, [r]_B$  (mask for )
-  can open  $y := \sum_k \chi^k \cdot \tilde{b}_k \oplus r$  and convince 
-  calls  $\text{Fix}(\tilde{b}_k)$  and checks  $\sum_k \chi^k [\tilde{b}_k] \oplus [r] \oplus y = 0$

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If so we can reduce  $4\rho$  bits to 1 bit

Our goal is to generate  $\langle \tilde{b}_k \rangle := \langle \hat{a}_k \rangle \oplus \langle a_i a_j \rangle \oplus \langle a_i b_j \rangle \oplus \langle a_j b_i \rangle$

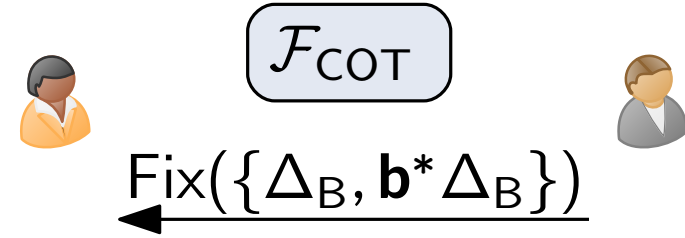
# Optimizing the Compressed Preprocessing Protocol, Continued

- $\langle \tilde{b}_k \rangle := \langle \hat{a}_k \rangle \oplus \langle a_i a_j \rangle \oplus \langle a_i b_j \rangle \oplus \langle a_j b_i \rangle$

- $D_A[\hat{a}_k] \oplus D_B[\hat{a}_k] = \hat{a}_k \Delta_A \Delta_B$

- $D_A[a_i b_j] \oplus D_B[a_i b_j] = a_i b_j \Delta_A \Delta_B$

The compression technique allows encoding  $\mathbf{b}$  in  $\mathcal{F}_{\text{bCOT}}$  global keys



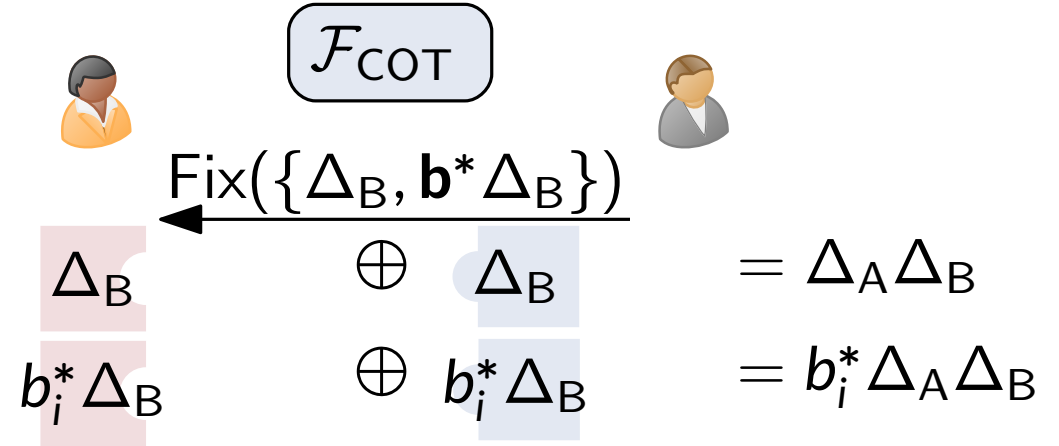
# Optimizing the Compressed Preprocessing Protocol, Continued

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# Optimizing the Compressed Preprocessing Protocol, Continued

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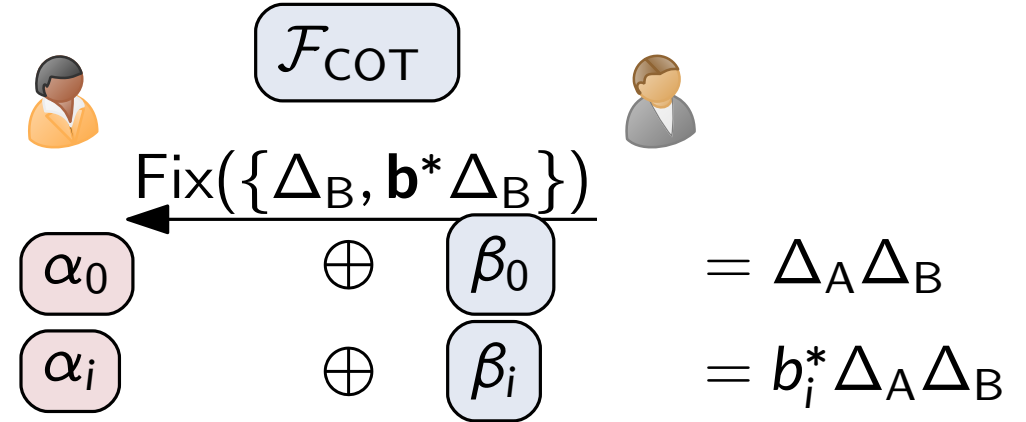
$\mathcal{F}_{\text{bCOT}}^{L+1}$

$$\boxed{a} = \boxed{a} + \boxed{a} \times [\beta_1, \dots, \beta_L, \Delta_B]$$

$\mathcal{F}_{\text{bCOT}}^2$

$$\boxed{\hat{a}} = \boxed{\hat{a}} + \boxed{\hat{a}} \times [\beta_0, \Delta_B]$$

The compression technique allows encoding  $\mathbf{b}$  in  $\mathcal{F}_{\text{bCOT}}$  global keys



# Optimizing the Compressed Preprocessing Protocol, Continued

- $\langle \tilde{b}_k \rangle := \langle \hat{a}_k \rangle \oplus \langle a_i a_j \rangle \oplus \langle a_i b_j \rangle \oplus \langle a_j b_i \rangle$

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$\mathcal{F}_{\text{bCOT}}^{L+1}$

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The compression technique allows encoding  $\mathbf{b}$  in  $\mathcal{F}_{\text{bCOT}}$  global keys

$\mathcal{F}_{\text{COT}}$



$\text{Fix}(\{\Delta_B, \mathbf{b}^* \Delta_B\})$

$\alpha_0$

$\oplus$

$\beta_0$

$$= \Delta_A \Delta_B$$

$\alpha_i$

$\oplus$

$\beta_i$

$$= b_i^* \Delta_A \Delta_B$$

$\Leftrightarrow$

$\alpha_0 \cdot \Delta_A^{-1}$

$\oplus$

$\beta_0 \cdot \Delta_A^{-1}$

$$= \Delta_B$$

$\alpha_i \cdot \Delta_A^{-1}$

$\oplus$

$\beta_i \cdot \Delta_A^{-1}$

$$= b_i^* \Delta_B$$

By  $\text{Fix}(\Delta'_A)$

$K[\beta_0]$

$\oplus$

$\beta_0 \cdot \Delta'_A$

$$= M[\beta_0]$$

$K[\beta_i]$

$\oplus$

$\beta_i \cdot \Delta'_A$

$$= M[\beta_i]$$

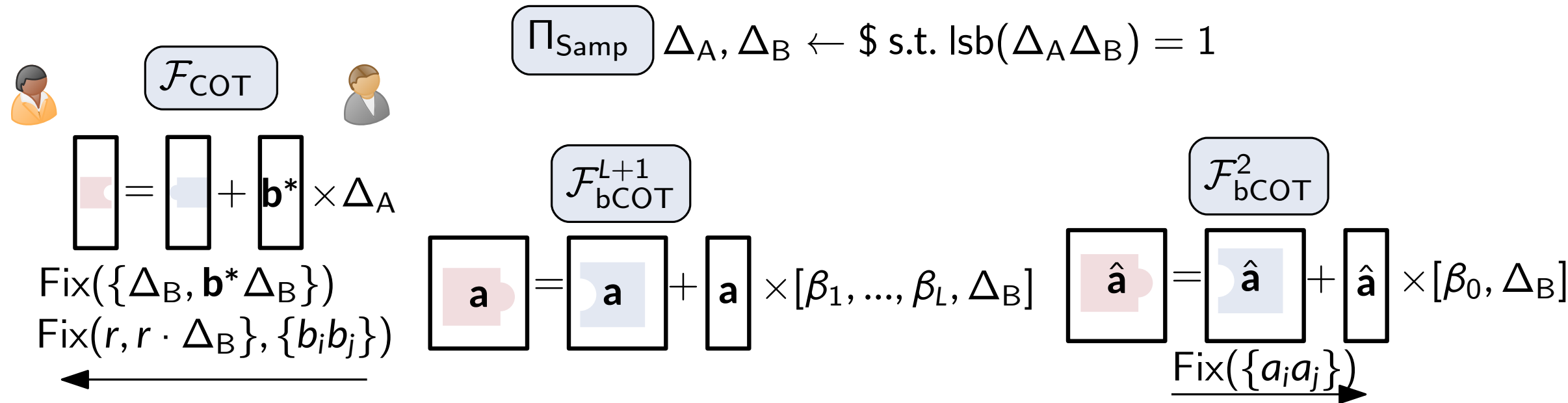
[DIO22] gives a modular way of proving equality under independent keys



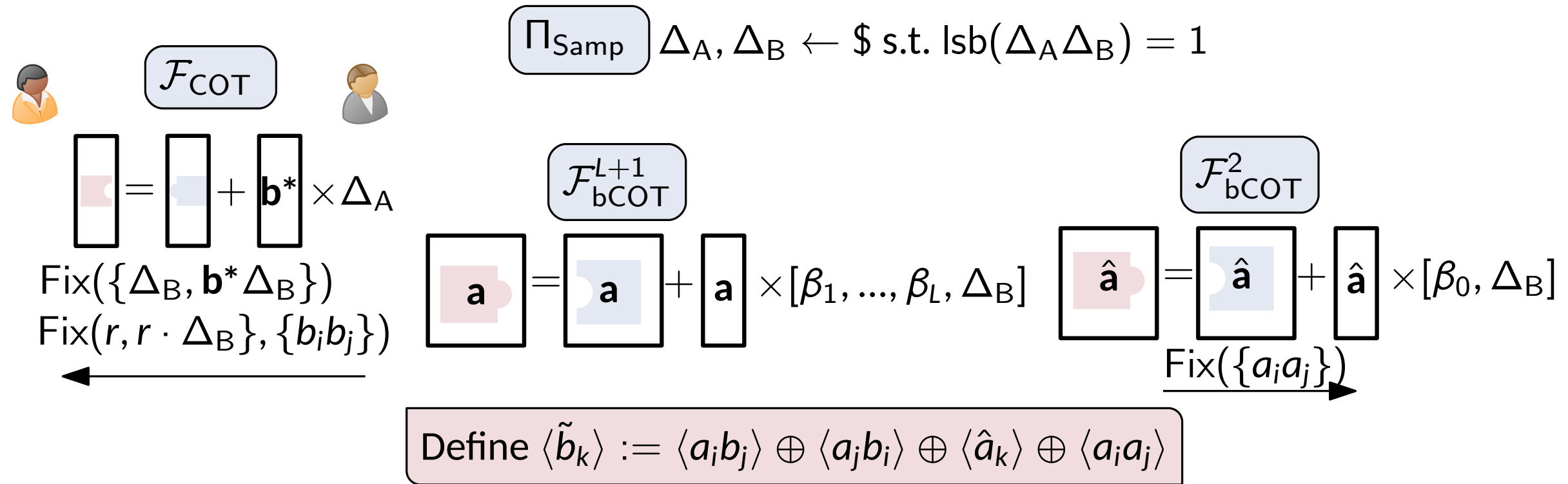
# Optimizing the Compressed Preprocessing Protocol, Completed

$$\Pi_{\text{Samp}} \Delta_A, \Delta_B \leftarrow \$ \text{ s.t. } \text{lsb}(\Delta_A \Delta_B) = 1$$

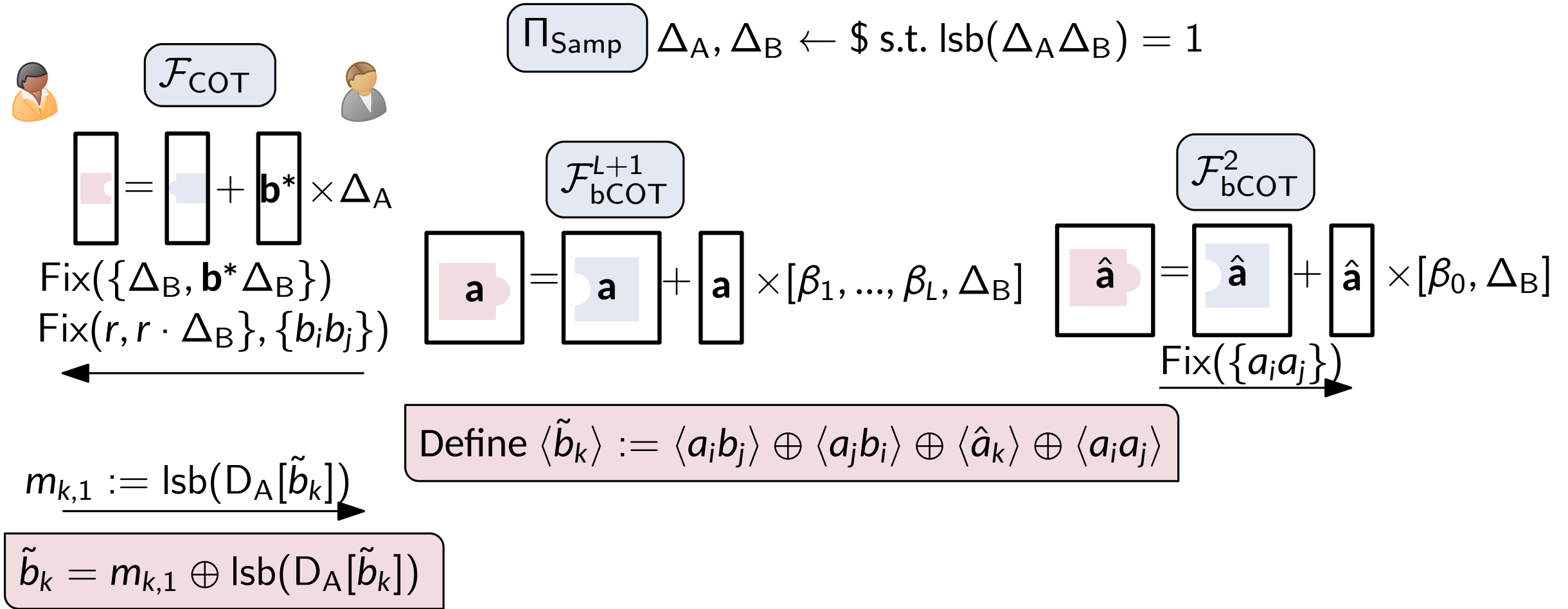
# Optimizing the Compressed Preprocessing Protocol, Completed



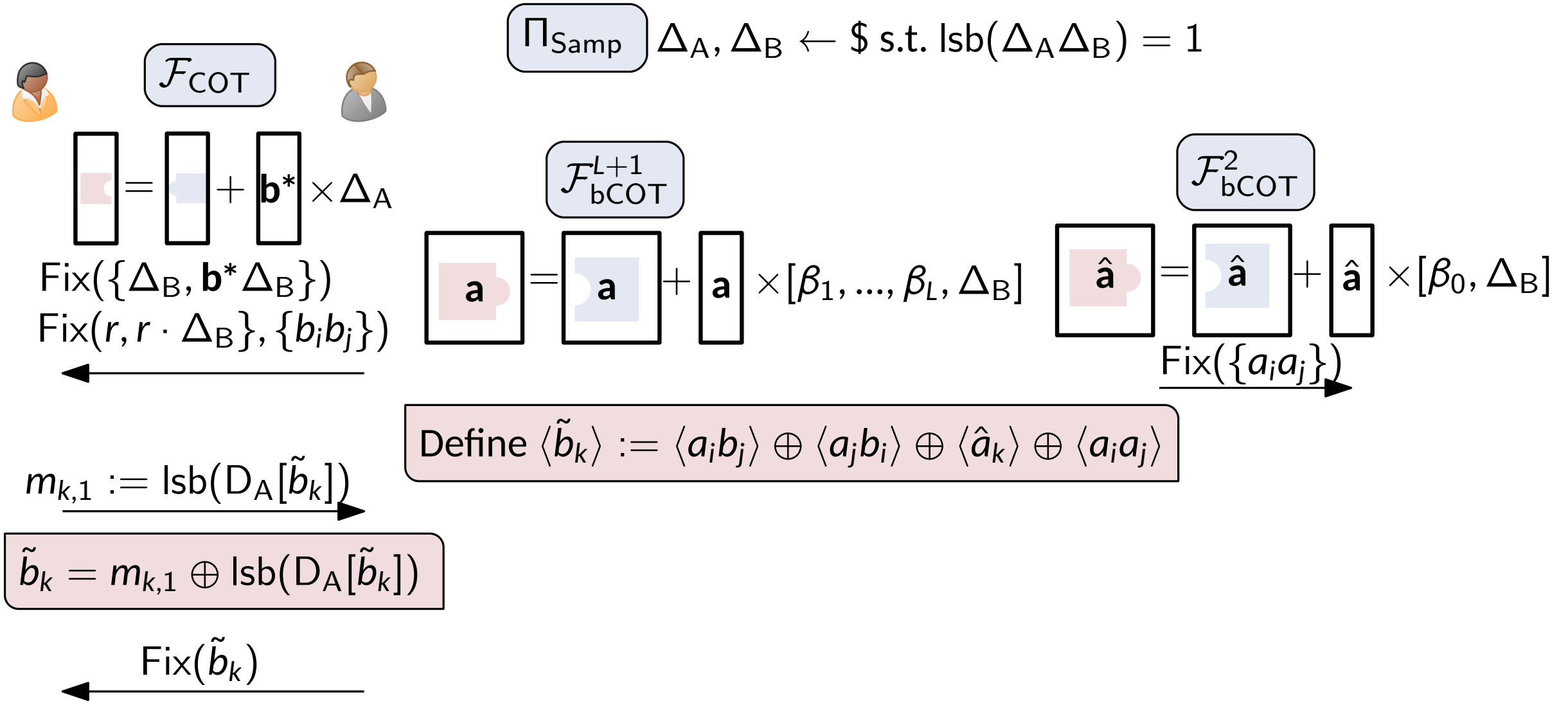
# Optimizing the Compressed Preprocessing Protocol, Completed



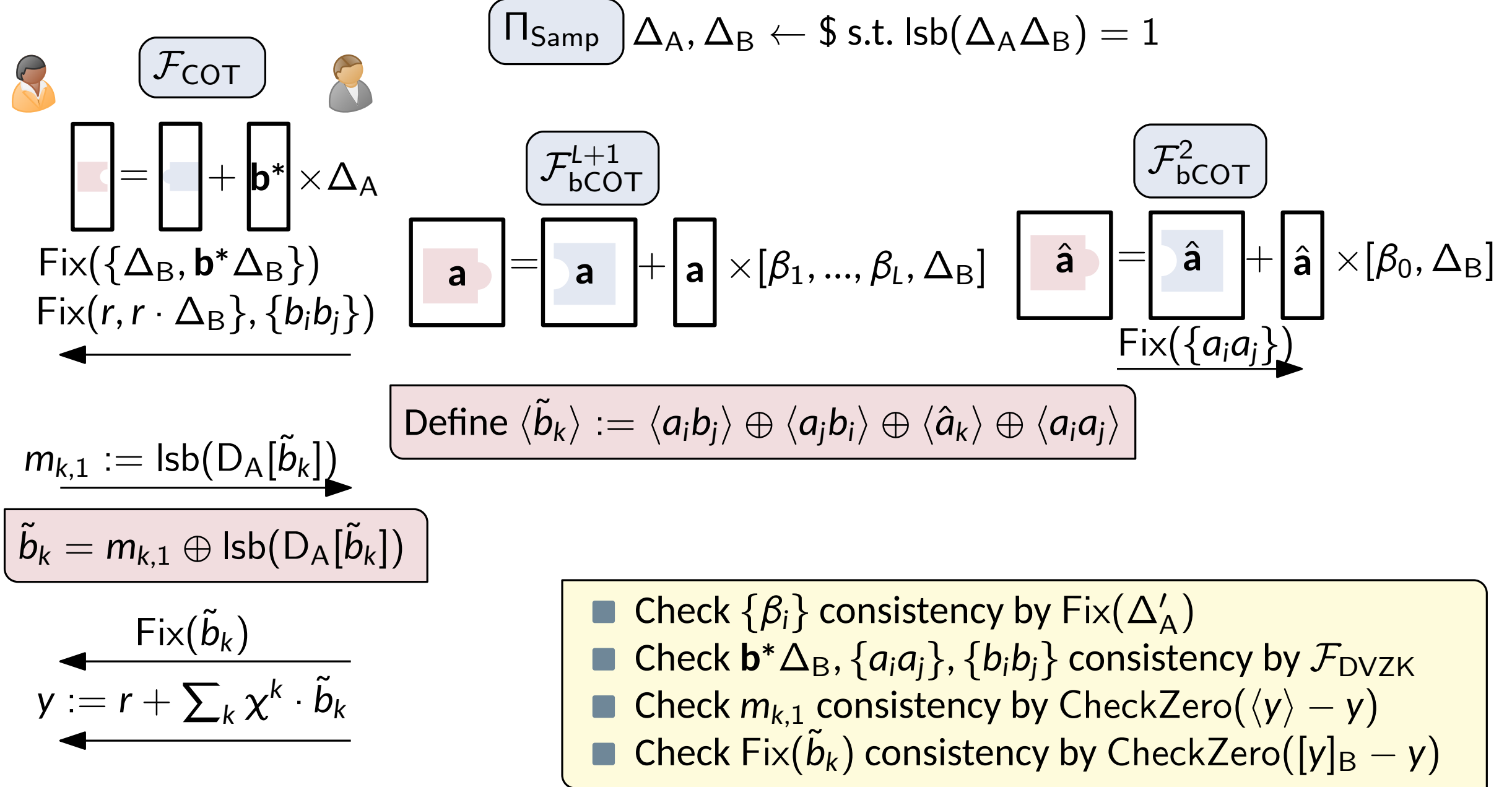
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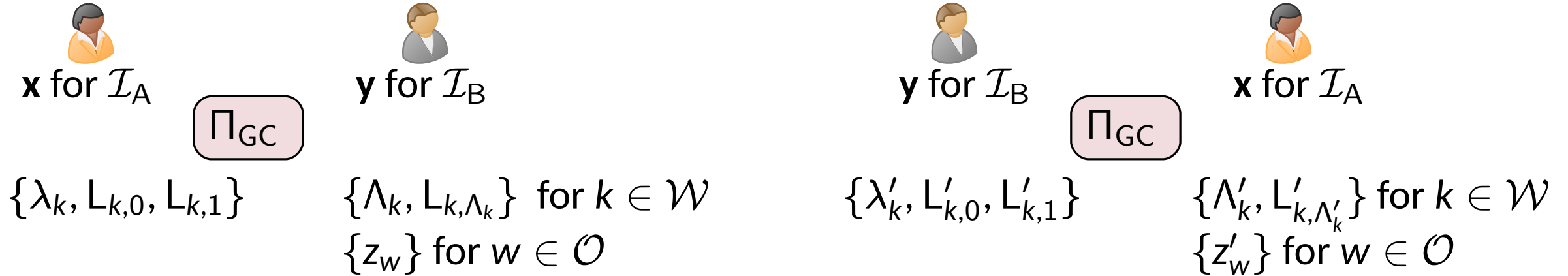


# Optimizing the One-way Communication Via Dual Execution

- Optimized  $\mathcal{F}_{\text{cpre}}$  + DILO-WRK =   $\rightarrow$  :  $2\kappa + 3\rho + 2$  bits,   $\rightarrow$  : 2 bits
- How about optimizing one-way communication? Maybe dual execution?





# Optimizing the One-way Communication Via Dual Execution

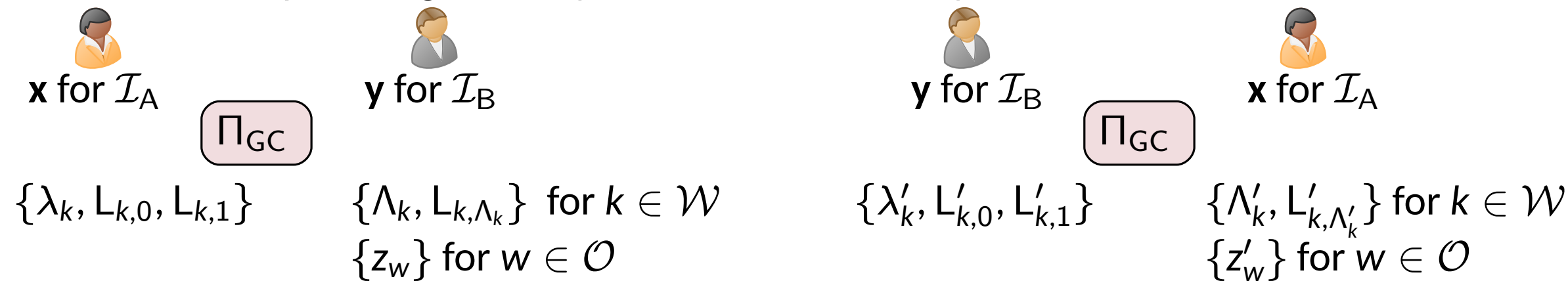
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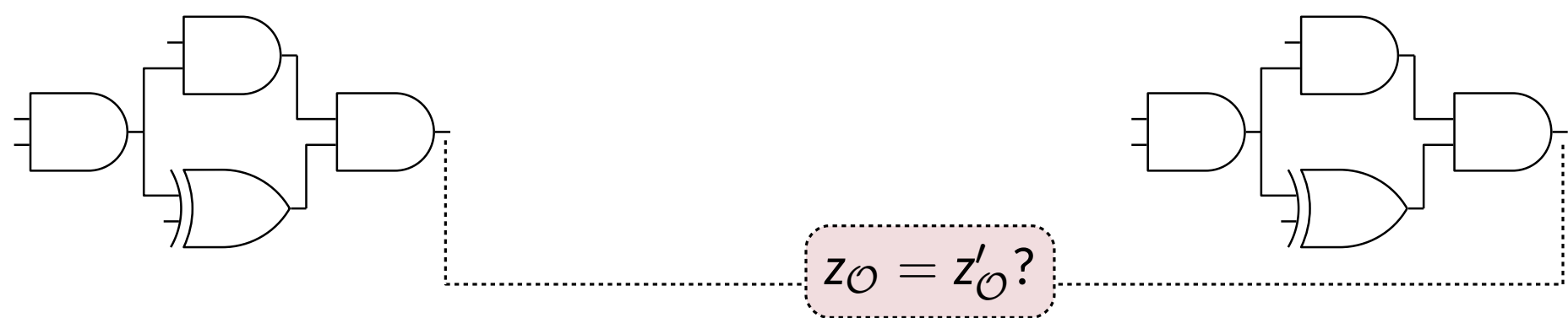


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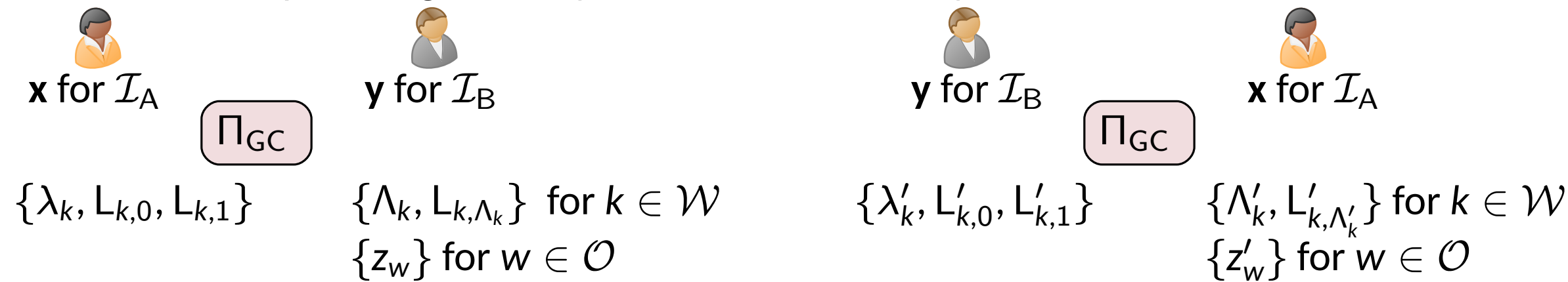


- [HEK12, HsV20]: Check for equality in circuit outputs

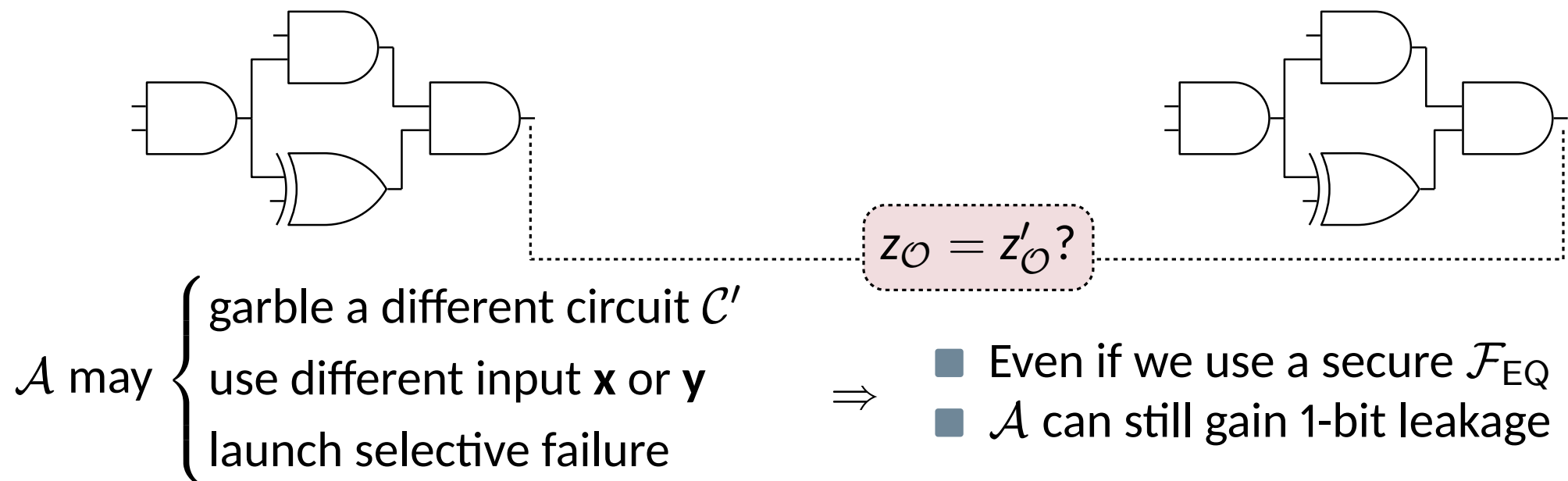


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# Optimizing the One-way Communication Via Dual Execution



$\mathcal{F}_{\text{cpre}}$

$$[\mathbf{a}], [\hat{\mathbf{a}}], [\mathbf{b}], [\hat{\mathbf{b}}], \Delta_A, \Delta_B \leftarrow \$$$

$$\text{s.t. } \hat{a}_k \oplus \hat{b}_k = (a_i \oplus b_i) \cdot (a_j \oplus b_j)$$

$\Pi_{\text{DG}}$

$$\{\lambda_k, L_{k,0}, L_{k,1}\} \quad \{\Lambda_k, L_{k,\Lambda_k}\} \text{ for } k \in \mathcal{W}$$



$\mathcal{F}_{\text{cpre}}$

$$[\mathbf{a}'], [\hat{\mathbf{a}}'], [\mathbf{b}'], [\hat{\mathbf{b}}'], \Delta_A, \Delta_B \leftarrow \$$$

$$\text{s.t. } \hat{a}'_k \oplus \hat{b}'_k = (a'_i \oplus b'_i) \cdot (a'_j \oplus b'_j)$$

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$$\{\lambda'_k, L'_{k,0}, L'_{k,1}\} \quad \{\Lambda'_k, L'_{k,\Lambda'_k}\} \text{ for } k \in \mathcal{W}$$

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$$\{\lambda_k, L_{k,0}, L_{k,1}\} \quad \{\Lambda_k, L_{k,\Lambda_k}\} \text{ for } k \in \mathcal{W}$$

$$L_{k,\Lambda_k} = L_{k,0} \oplus \Lambda_k \cdot \Delta_A$$



$\mathcal{F}_{\text{cpre}}$



$$[\mathbf{a}'], [\hat{\mathbf{a}}'], [\mathbf{b}'], [\hat{\mathbf{b}}'], \Delta_A, \Delta_B \leftarrow \$$$

$$\text{s.t. } \hat{a}'_k \oplus \hat{b}'_k = (a'_i \oplus b'_i) \cdot (a'_j \oplus b'_j)$$

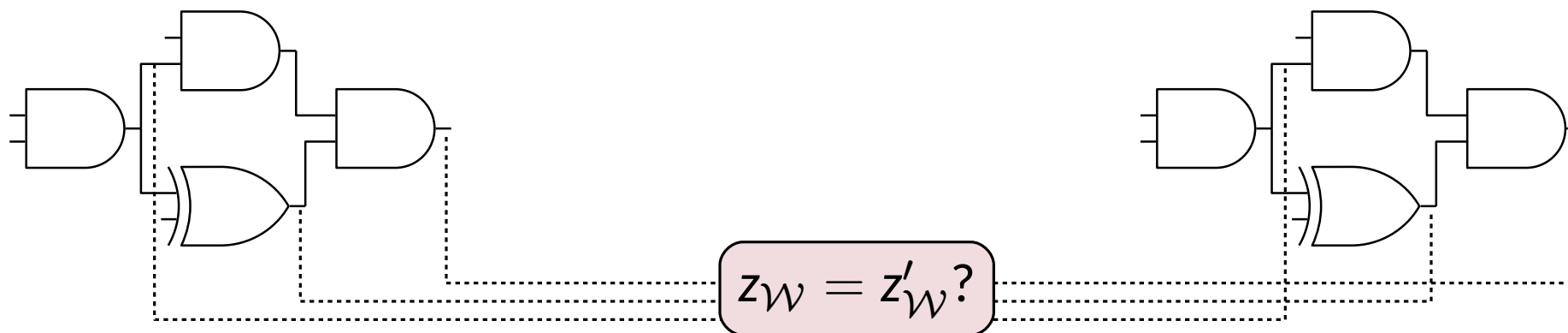
$\Pi_{\text{DG}}$

$$\{\lambda'_k, L'_{k,0}, L'_{k,1}\} \quad \{\Lambda'_k, L'_{k,\Lambda'_k}\} \text{ for } k \in \mathcal{W}$$

$$L'_{k,\Lambda'_k} = L'_{k,0} \oplus \Lambda'_k \cdot \Delta'_B$$

- Color bits and wire masks are authenticated for every wire
- This enables checking equality for every wire across two executions

[HK21] Garbled Sharing



# Optimizing the One-way Communication Via Dual Execution



$\mathcal{F}_{\text{cpre}}$



$$[\mathbf{a}], [\hat{\mathbf{a}}], [\mathbf{b}], [\hat{\mathbf{b}}], \Delta_A, \Delta_B \leftarrow \$$$

$$\text{s.t. } \hat{a}_k \oplus \hat{b}_k = (a_i \oplus b_i) \cdot (a_j \oplus b_j)$$

$\Pi_{\text{DG}}$

$$\{\lambda_k, L_{k,0}, L_{k,1}\} \quad \{\Lambda_k, L_{k,\Lambda_k}\} \text{ for } k \in \mathcal{W}$$

$$L_{k,\Lambda_k} = L_{k,0} \oplus \Lambda_k \cdot \Delta_A$$



$\mathcal{F}_{\text{cpre}}$



$$[\mathbf{a}'], [\hat{\mathbf{a}}'], [\mathbf{b}'], [\hat{\mathbf{b}}'], \Delta_A, \Delta_B \leftarrow \$$$

$$\text{s.t. } \hat{a}'_k \oplus \hat{b}'_k = (a'_i \oplus b'_i) \cdot (a'_j \oplus b'_j)$$

$\Pi_{\text{DG}}$

$$\{\lambda'_k, L'_{k,0}, L'_{k,1}\} \quad \{\Lambda'_k, L'_{k,\Lambda'_k}\} \text{ for } k \in \mathcal{W}$$

$$L'_{k,\Lambda'_k} = L'_{k,0} \oplus \Lambda'_k \cdot \Delta'_B$$

$$\text{Checks } (a_w \oplus b_w \oplus \Lambda_w) \cdot (\Delta_A \oplus \Delta_B) = (a'_w \oplus b'_w \oplus \Lambda'_w) \cdot (\Delta_A \oplus \Delta_B)$$

$$V_w^A = (a_w \oplus a'_w \oplus \Lambda'_w) \Delta_A \oplus M_A[a_w] \oplus M_A[a'_w] \oplus M_A[\Lambda'_w] \oplus K_A[b_w] \oplus K_A[b'_w] \oplus K_A[\Lambda_w],$$

$$V_w^B = (b_w \oplus b'_w \oplus \Lambda_w) \Delta_B \oplus M_B[b_w] \oplus M_B[b'_w] \oplus M_B[\Lambda_w] \oplus K_B[a_w] \oplus K_B[a'_w] \oplus K_B[\Lambda'_w].$$

# Conclusion

- Further optimization on the compression technique of [DILO22]
- Dual-key authentication and efficient generation
- Dual execution upon distribution garbling eliminates 1-bit leakage
- Malicious 2PC with one-way comm. of  $2\kappa + 5$  bits, two way comm. of  $2\kappa + 3\rho + 2$  bits

2PC	Rounds		Communication per AND gate	
	Prep.	Online	one-way (bits)	two-way (bits)
Half-gates	1	2	$2\kappa$	$2\kappa$
HSS-PCG	8	2	$8\kappa + 11$ (4.04 $\times$ )	$16\kappa + 22$ (8.09 $\times$ )
KRRW-PCG	4	4	$5\kappa + 7$ (2.53 $\times$ )	$8\kappa + 14$ (4.05 $\times$ )
DILO	7	2	$2\kappa + 8\rho + 1$ (2.25 $\times$ )	$2\kappa + 8\rho + 5$ (2.27 $\times$ )
This work	8	3	$2\kappa + 5$ ( $\approx 1$ $\times$ )	$4\kappa + 10$ (2.04 $\times$ )
This work+DILO	8	2	$2\kappa + 3\rho + 2$ (1.48 $\times$ )	$2\kappa + 3\rho + 4$ ( $\approx 1.48$ $\times$ )

Thanks for your listening

*Merci beaucoup*

