

Actively Secure Half-Gates with Minimum Overhead under Duplex Networks

Malicious Half-Gates as Sleek as Semi-Honest

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¹ * Some acknowledgments?

Background on Constant Round 2PC

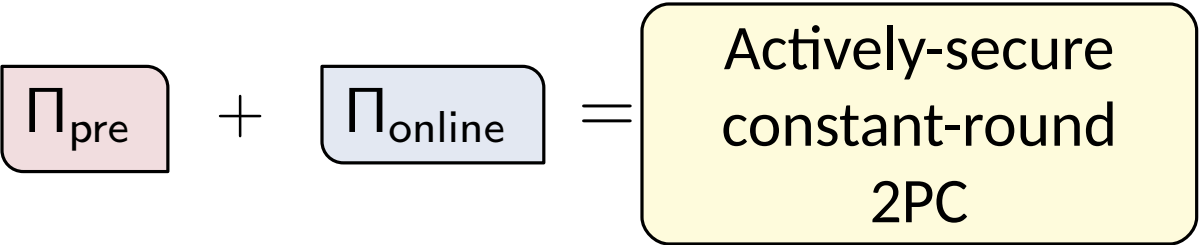
- Garbled circuit is the canonical technique in constant round 2PC

Scheme	XOR	AND (bits)
Textbook Yao	8κ	8κ
Point&Permute	4κ	4κ
GRR3	3κ	3κ
GRR2	2κ	2κ
Free-XOR	0	3κ
fleXOR	$\{0, 1, 2\}\kappa$	2κ
Half-gates	0	2κ
Three-halves	0	$1.5\kappa + 5$

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- How to boost GC to malicious security?
- AG [KRW17]: Use IT-MAC

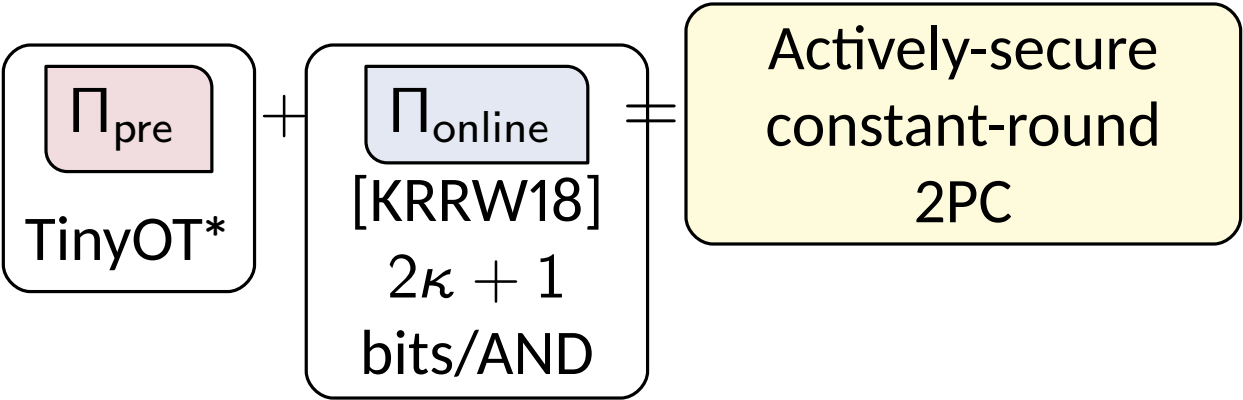
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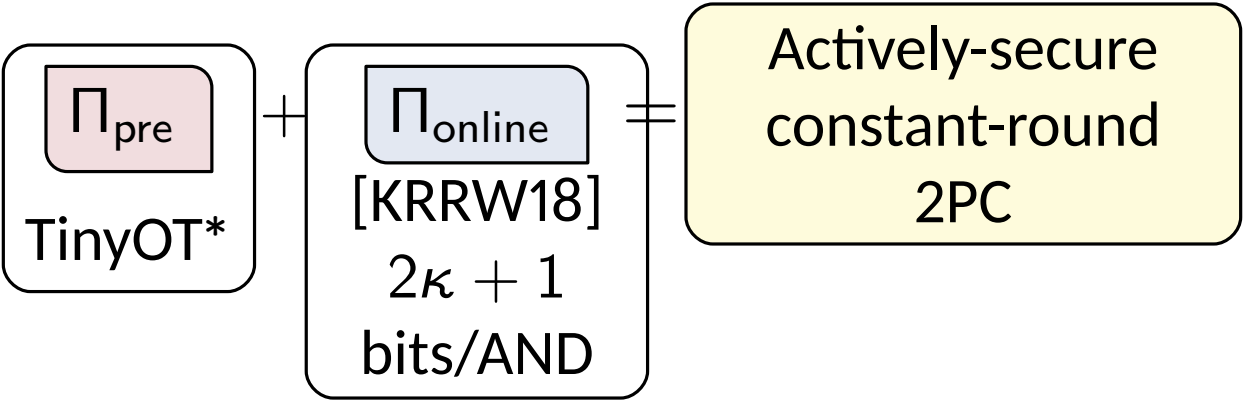


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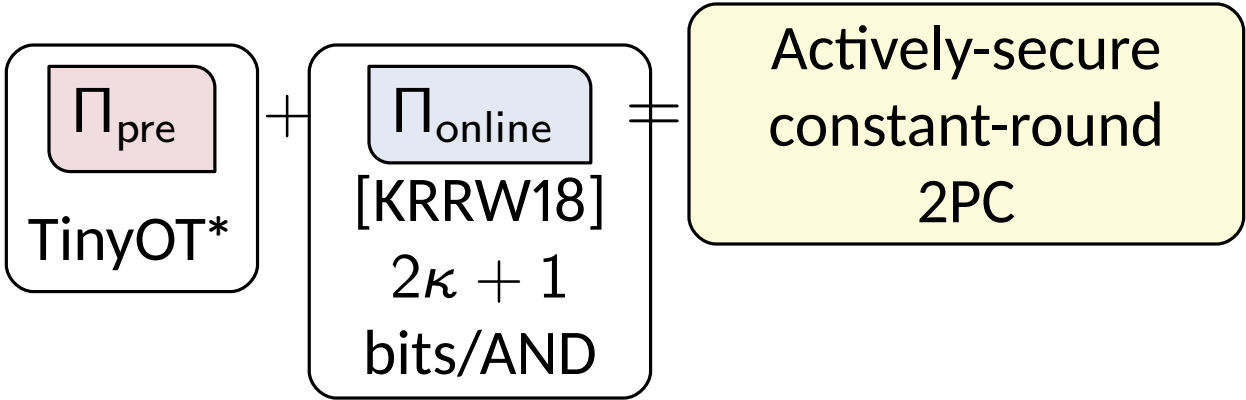
[DILO22]

- Instantiate \mathcal{F}_{pre} using PCG
- Optimize Π_{online} to minimize comm.
- $2\kappa + 8\rho + O(1)$ bits/AND in \mathcal{F}_{COT} -hybrid
- $2\kappa + 4\rho + O(1)$ bits/AND in $\mathcal{F}_{\text{DAMT}}$ -hybrid

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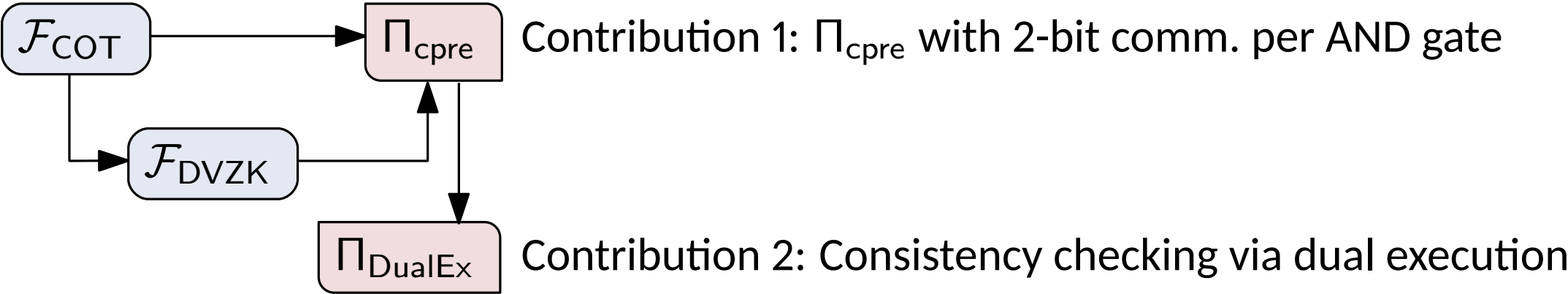
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Can we do better?

Our Contributions

- Authenticated garbling with one-way comm. as small as semi-honest half-gates

2PC	Rounds		Communication per AND gate	
	Prep.	Online	one-way (bits)	two-way (bits)
Half-gates	1	2	2κ	2κ
HSS-PCG	8	2	$8\kappa + 11$ (4.04 \times)	$16\kappa + 22$ (8.09 \times)
KRRW-PCG	4	4	$5\kappa + 7$ (2.53 \times)	$8\kappa + 14$ (4.05 \times)
DILO	7	2	$2\kappa + 8\rho + 1$ (2.25 \times)	$2\kappa + 8\rho + 5$ (2.27 \times)
This work	8	3	$2\kappa + 5$ ($\approx 1\times$)	$4\kappa + 10$ (2.04 \times)
This work+DILO	8	2	$2\kappa + 3\rho + 2$ (1.48 \times)	$2\kappa + 3\rho + 4$ ($\approx 1.48\times$)



Semi-Honest Garbled Circuit

$$\Lambda_k := \lambda_k \oplus (\Lambda_i \oplus \lambda_i) \cdot (\Lambda_j \oplus \lambda_j)$$

i	j	k	$i \text{---} \text{AND} \text{---} j$	Λ_i	Λ_j	ciphertext
0	0	0		0	0	$H(L_{i,0}, L_{j,0}) \oplus L_{k,0} \oplus (\lambda_i \cdot \lambda_j \oplus \lambda_k) \Delta_A$
0	1	0	Encrypt	0	1	$H(L_{i,0}, L_{j,1}) \oplus L_{k,0} \oplus (\lambda_i \cdot \bar{\lambda}_j \oplus \lambda_k) \Delta_A$
1	0	0	& Sort	1	0	$H(L_{i,1}, L_{j,0}) \oplus L_{k,0} \oplus (\bar{\lambda}_i \cdot \lambda_j \oplus \lambda_k) \Delta_A$
1	1	1	→	1	1	$H(L_{i,1}, L_{j,1}) \oplus L_{k,0} \oplus (\bar{\lambda}_i \cdot \bar{\lambda}_j \oplus \lambda_k) \Delta_A$

$$c_k := \text{lsb}(L_{k,0})$$



Sample $\{\lambda_w, L_{w,0}\}$
and Δ

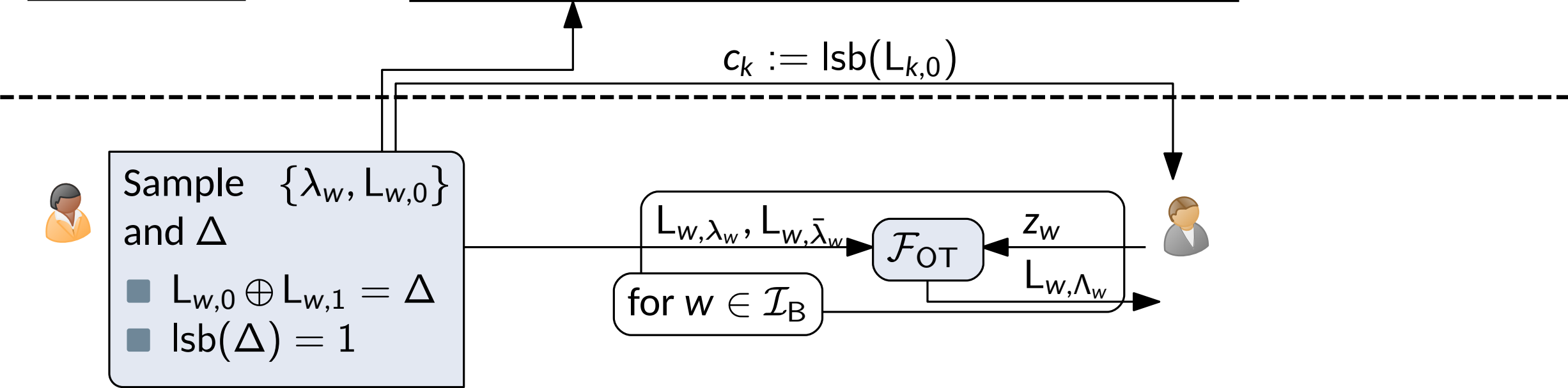
- $L_{w,0} \oplus L_{w,1} = \Delta$
- $\text{lsb}(\Delta) = 1$



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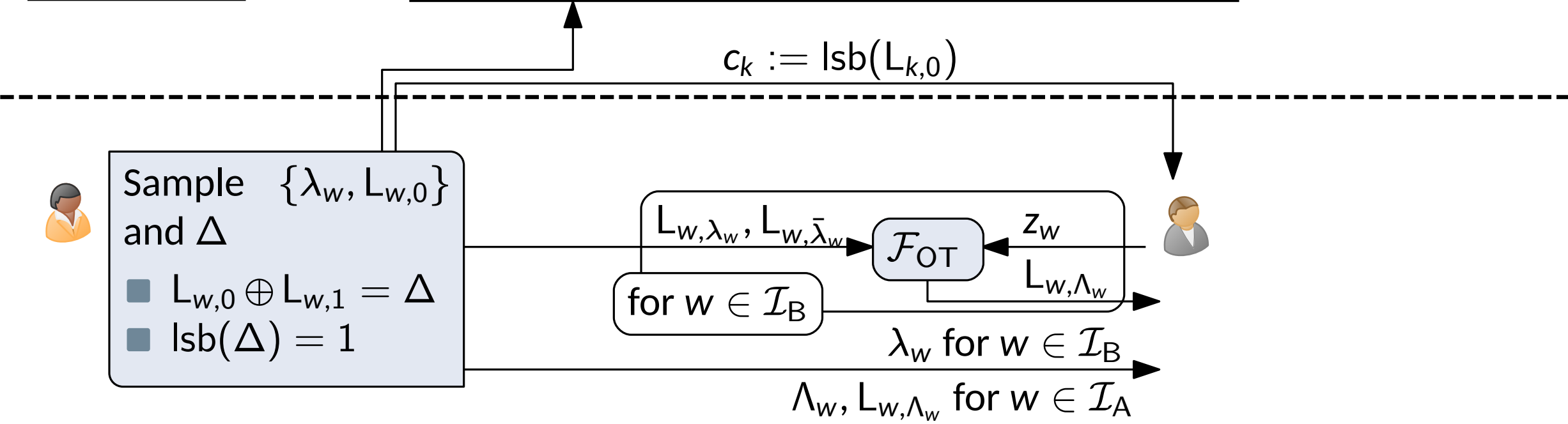
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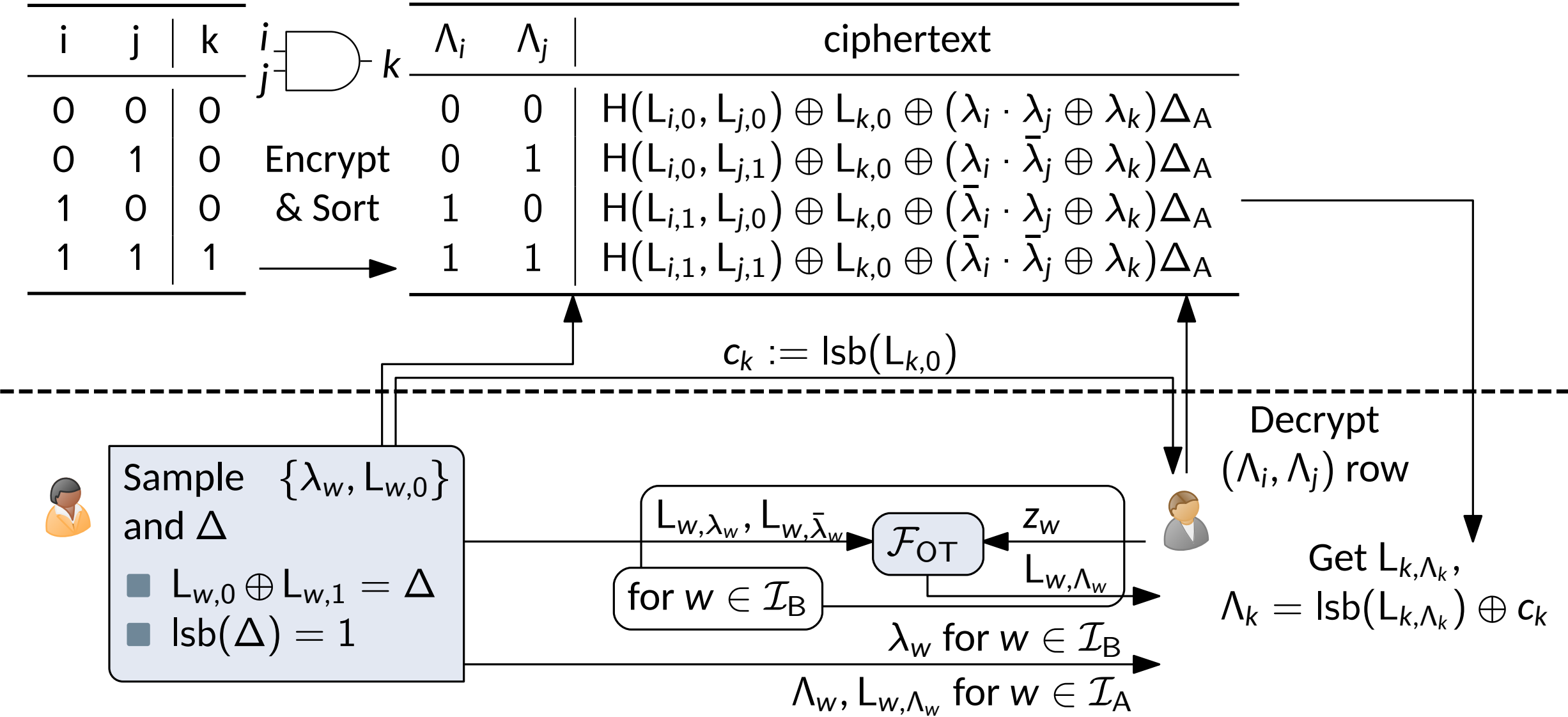
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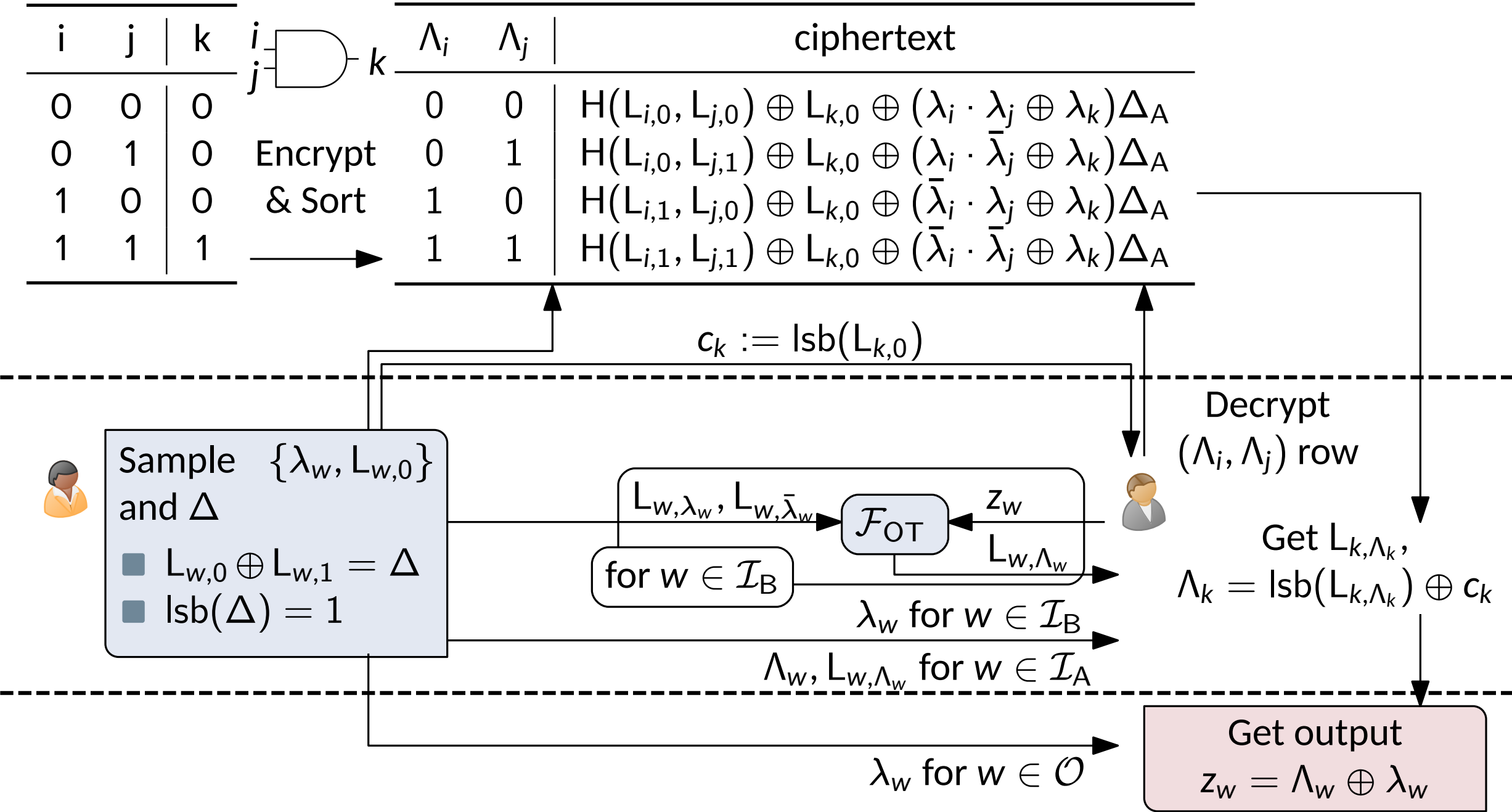
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Authenticated Garbling = Distributed Garbling + Checking



controls garbling so it can

- mount selective-failure attack on $\Lambda := z \oplus \lambda \Rightarrow$ Secret share $\lambda := a \oplus b$
- garble a different circuit without detection \Rightarrow enforce AND correlation by IT-MAC, equality check, etc.

Λ_i	Λ_j	Masked L_{k,Λ_k}
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\mathcal{F}_{pre}

samples

$[\mathbf{a}], [\hat{\mathbf{a}}], [\mathbf{b}], [\hat{\mathbf{b}}]$

Δ_A, Δ_B



gets $\mathbf{a}, \hat{\mathbf{a}}, M[\mathbf{a}], M[\hat{\mathbf{a}}], K[\mathbf{b}], K[\hat{\mathbf{b}}]$



gets $\mathbf{b}, \hat{\mathbf{b}}, K[\mathbf{a}], K[\hat{\mathbf{a}}], M[\mathbf{b}], M[\hat{\mathbf{b}}]$

s.t. $\hat{a}_k \oplus \hat{b}_k = (a_i \oplus b_i) \cdot (a_j \oplus b_j)$

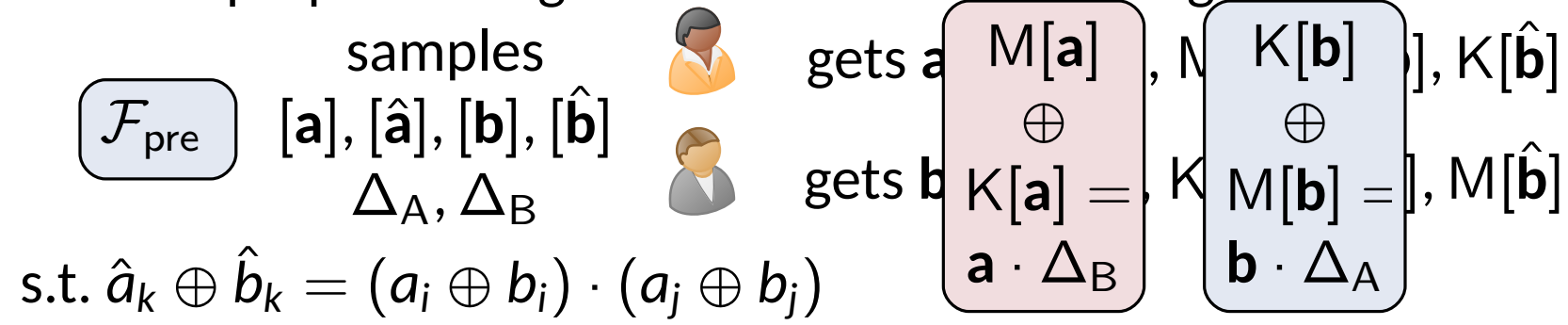
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\mathcal{F}_{pre}

samples

$$[a], [\hat{a}], [b], [\hat{b}]$$
$$\Delta_A, \Delta_B$$



gets a , $M[a]$, $K[b]$, $K[\hat{b}]$



gets b , $K[a] = a \cdot \Delta_B$, $M[b] = b \cdot \Delta_A$, $M[\hat{b}]$

$$\text{s.t. } \hat{a}_k \oplus \hat{b}_k = (a_i \oplus b_i) \cdot (a_j \oplus b_j)$$

Λ_i	Λ_j	Alice's GC	Bob's GC
0	0	$L_{k,0} \oplus K[\Lambda_{00}]$	$M[\Lambda_{00}]$
0	1	$L_{k,0} \oplus K[\Lambda_{01}]$	$M[\Lambda_{01}]$
1	0	$L_{k,0} \oplus K[\Lambda_{10}]$	$M[\Lambda_{10}]$
1	1	$L_{k,0} \oplus K[\Lambda_{11}]$	$M[\Lambda_{11}]$

Free-XOR GC \Rightarrow

$$|\Delta_A| = \kappa \approx 128$$

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Λ_i	Λ_j	Alice's AuthGC	Bob's AuthGC
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$$= \lambda_k \cdot \Delta_B \oplus \dots \oplus (\hat{a}_k \oplus \hat{b}_k) \cdot \Delta_B$$

Free-XOR GC \Rightarrow
 $|\Delta_A| = \kappa \approx 128$

IT-MAC Soundness \Rightarrow
 $|\Delta_B| = \rho \approx 40$

KRRW18: Distributed Half-Gates Garbling + Equality Checking

- $G_{k,0} = H(L_{i,0}) \oplus H(L_{i,1}) \oplus \lambda_j \cdot \Delta_A$
- $G_{k,1} = H(L_{j,0}) \oplus H(L_{j,1}) \oplus \lambda_i \cdot \Delta_A \oplus L_{i,0}$
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Eval:
$$\begin{aligned} &H(L_{i,\Lambda_i}) \oplus \Lambda_i \cdot G_{k,0} \oplus H(L_{j,\Lambda_i}) \oplus \Lambda_j \cdot G_{k,1} \oplus L_{i,\Lambda_i} \\ &= H(L_{i,0}) \oplus \Lambda_i \lambda_j \cdot \Delta_A \oplus H(L_{j,0}) \oplus \Lambda_j (\lambda_i \oplus \Lambda_i) \cdot \Delta_A \\ &= L_{k,0} \oplus \Lambda_k \cdot \Delta_A = L_{k,\Lambda_k} \end{aligned}$$

With \mathcal{F}_{pre} $G_{k,0}, G_{k,1}, L_{k,0}$ are already shared \Rightarrow Evaluator can get $\{\Lambda_w, L_{w,\Lambda_w}\}$ with $2\kappa + 1$ bits amortized comm.

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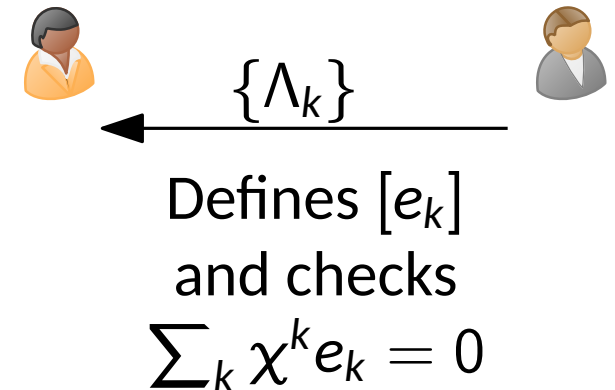
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-
- **b-mask** removes selective failure, now only need to check correct AND correlation

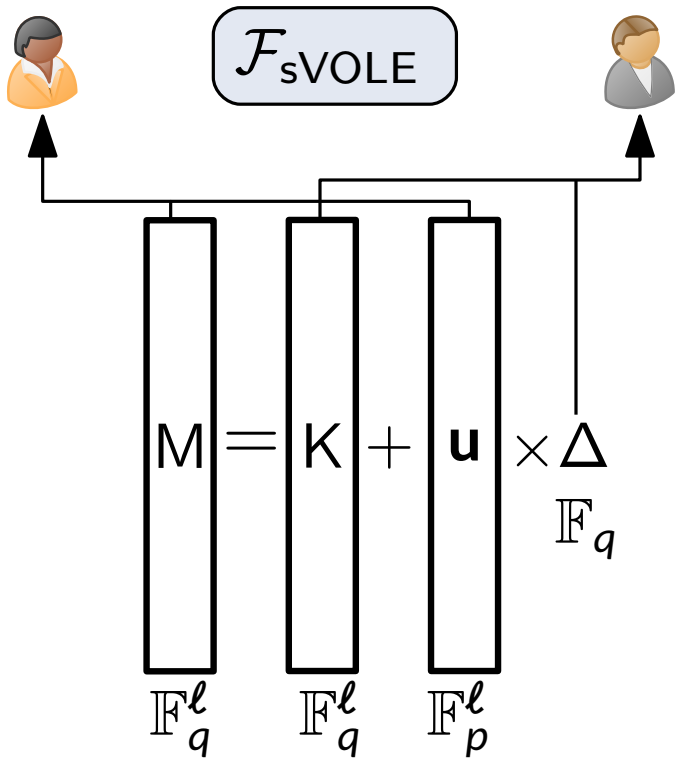
$$\Lambda_k \oplus \lambda_k = (\Lambda_i \oplus \lambda_i) \cdot (\Lambda_j \oplus \lambda_j) \Leftrightarrow e_k := \Lambda_k \oplus \lambda_k \oplus \Lambda_i \Lambda_j \oplus \Lambda_i \lambda_j \oplus \lambda_i \Lambda_j \oplus \lambda_i \lambda_j = 0$$

Check:

- Evaluator sends $\{\Lambda_w\}$ for all AND gates
- The checking equation reduces to equality check
- Use random linear combination to reduce comm.





Efficient COT/VOLE and Designated Verifier Zero Knowledge



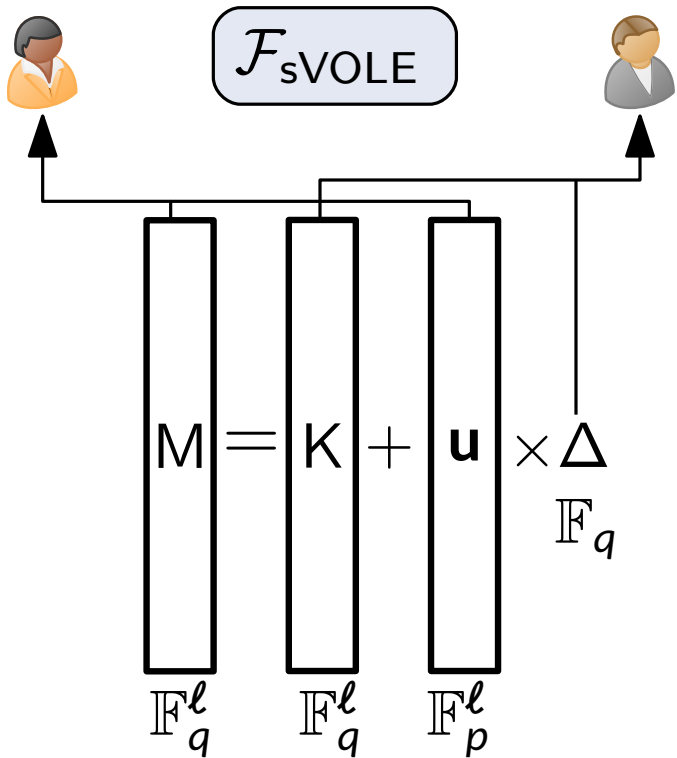
- Efficient protocol for \mathcal{F}_{COT} , $\mathcal{F}_{\text{sVOLE}}$ with sublinear comm. and linear comp. from LPN [YWL+20,CRR21,...]
- We refer the $\mathbb{F}_p = \mathbb{F}_2$ variant of $\mathcal{F}_{\text{sVOLE}}$ as \mathcal{F}_{COT}

Derandomization operation: Fix


 $\xrightarrow{\delta := x \oplus u}$




$M[x] := M[u]$
 $K[x] := K[u] \oplus \delta \cdot \Delta$

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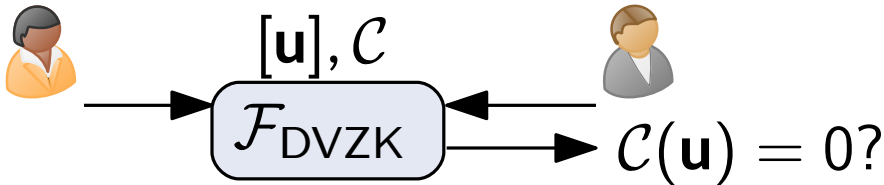
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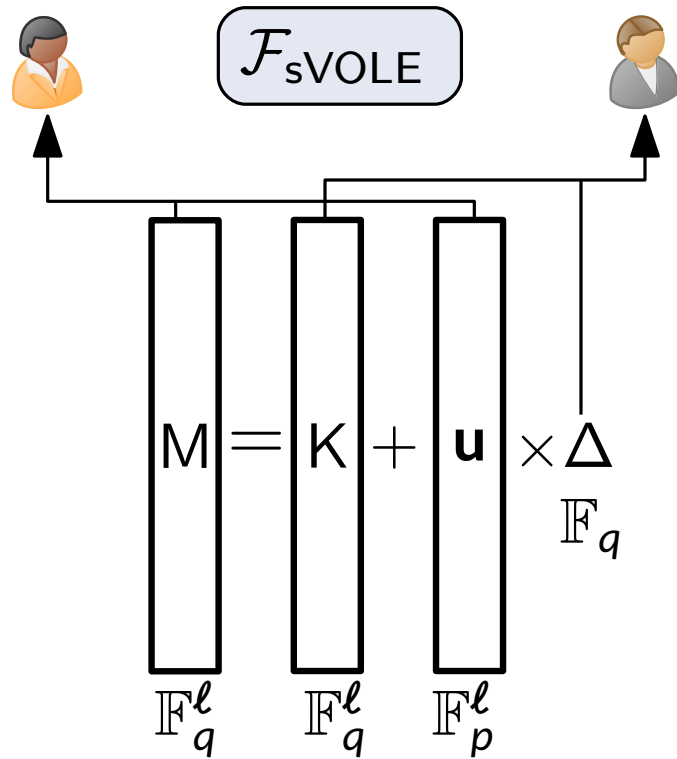
 $\xrightarrow{\delta := x \oplus u}$ 

$M[x] := M[u]$ $\qquad K[x] := K[u] \oplus \delta \cdot \Delta$

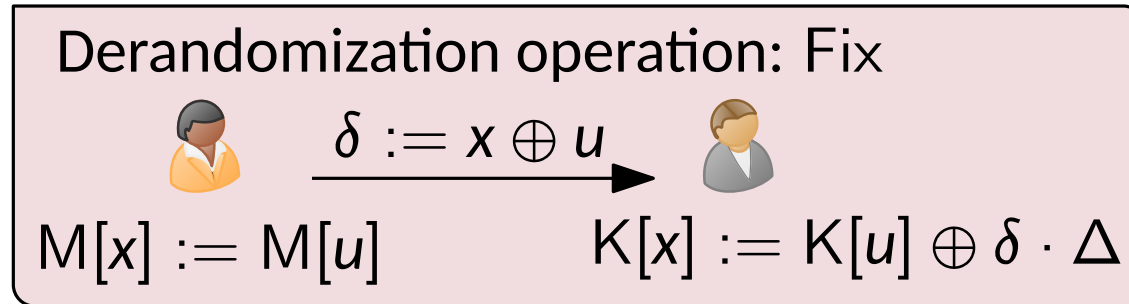
- Efficient proof for deg- d relations on u [DIO21, YSWW21, ...]



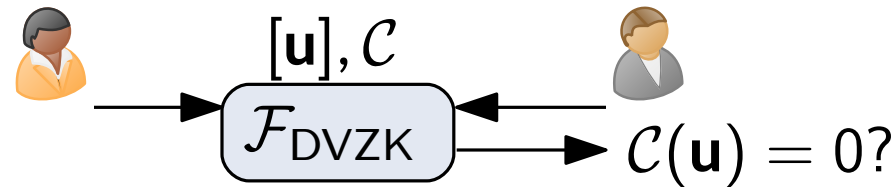
Efficient COT/VOLE and Designated Verifier Zero Knowledge



- Efficient protocol for $\mathcal{F}_{\text{COT}}, \mathcal{F}_{\text{sVOLE}}$ with sublinear comm. and linear comp. from LPN [YWL+20, CRR21, ...]
- We refer the $\mathbb{F}_p = \mathbb{F}_2$ variant of $\mathcal{F}_{\text{sVOLE}}$ as \mathcal{F}_{COT}

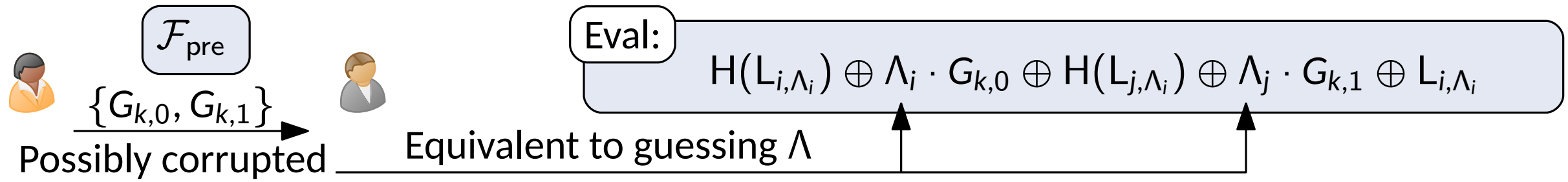


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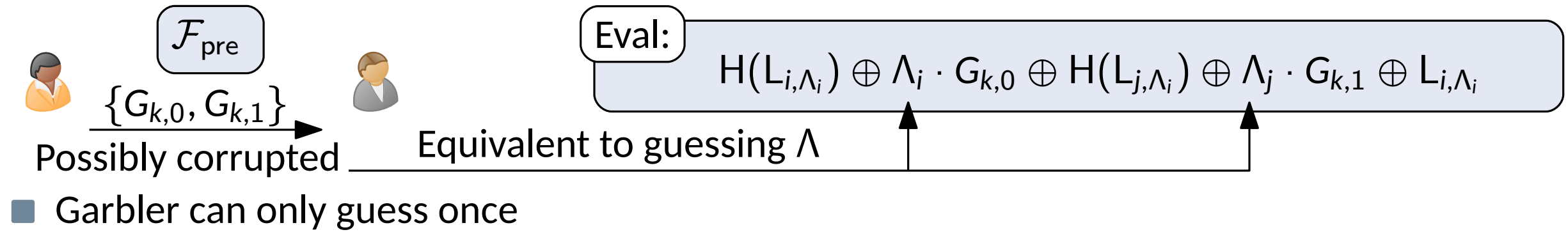


- In DILO, those PCG correlations are called “simple correlations”
- Unfortunately, we still don’t have a direct \mathcal{F}_{pre} PCG construction
- The closest is the $\mathcal{F}_{\text{DAMT}}$ correlation generated from Ring-LPN, but with ρ -time overhead

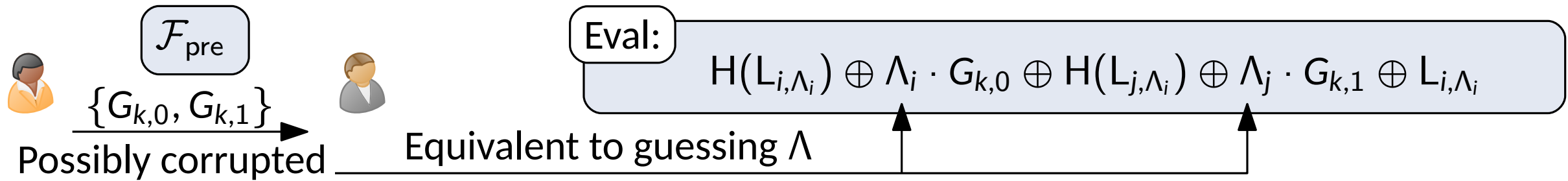
AG from Simple Correlations



AG from Simple Correlations

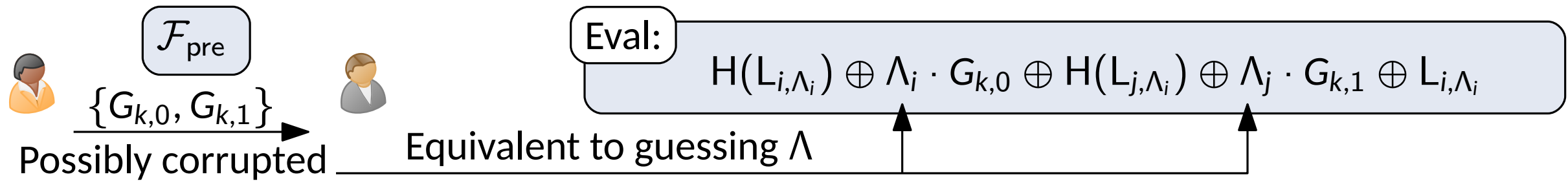


AG from Simple Correlations



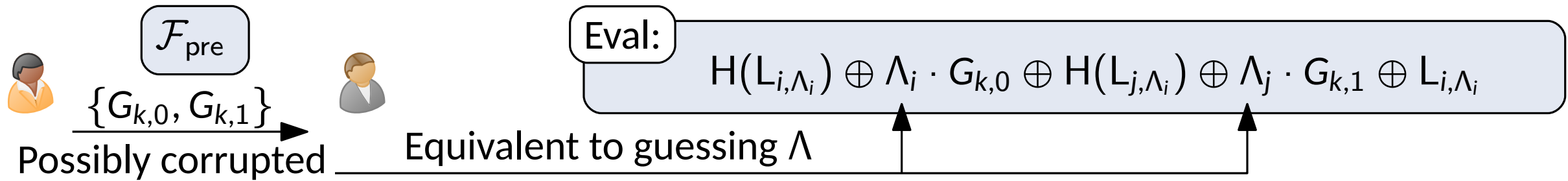
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- If \mathbf{b} is uniformly random, then guessing leaks no information

AG from Simple Correlations



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- If \mathbf{b} is uniformly random, then guessing leaks no information
- If #Guess is too large, then abort happens overwhelmingly, if #Guess is too little, then we don't require much entropy from \mathbf{b}

AG from Simple Correlations



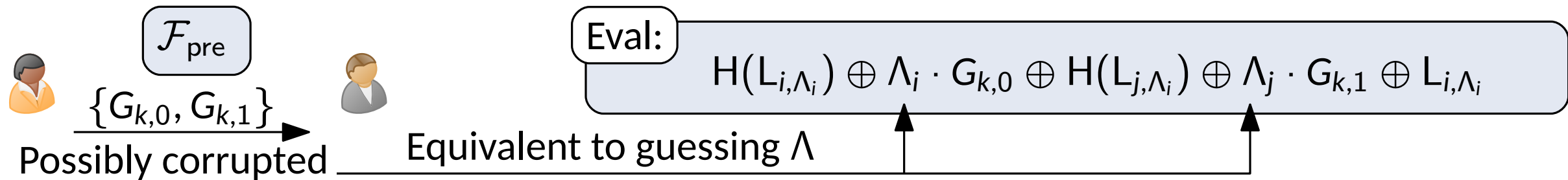
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DILO Observation 1

It suffices for \mathbf{b} to be ρ -wise independent

- #Guess $\leq \rho$: Abort is input-independent
- #Guess $> \rho$: Abort is overwhelming

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DILO Observation 1

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DILO Observation 2

We can construct ρ -wise independent \mathbf{b} by linear expansion

$$\mathbf{b} = \mathbf{M} \times \mathbf{b}^*$$

- For $L = O(\rho \cdot \log(\frac{n}{\rho}))$, a uniformly random \mathbf{M} suffices
- We can encode \mathbf{b}^* in \mathcal{F}_{COT} global keys

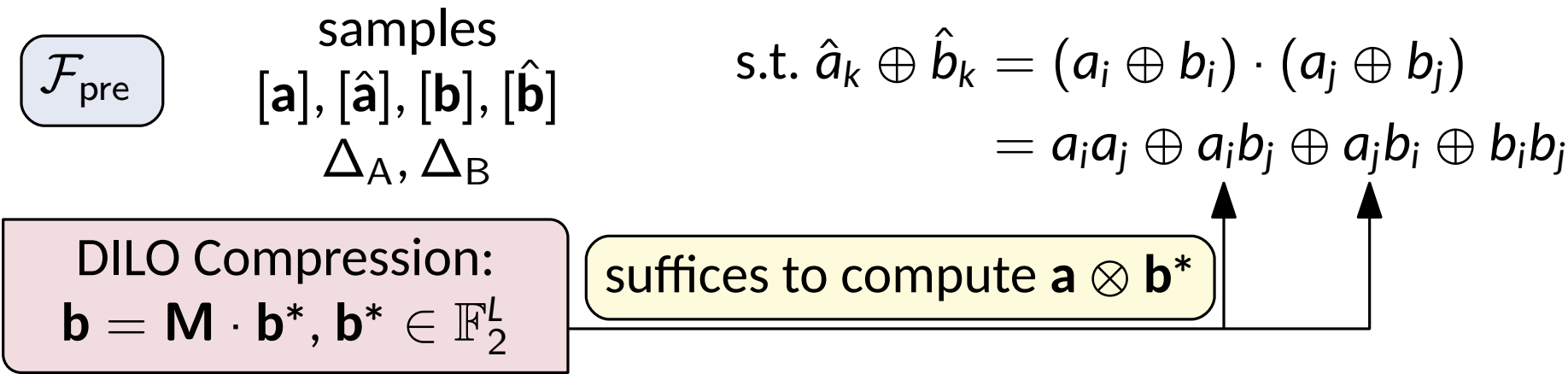
DILO Implementation of $\mathcal{F}_{\text{cpre}}$: Encoding \mathbf{b}^* as Global Keys

\mathcal{F}_{pre}

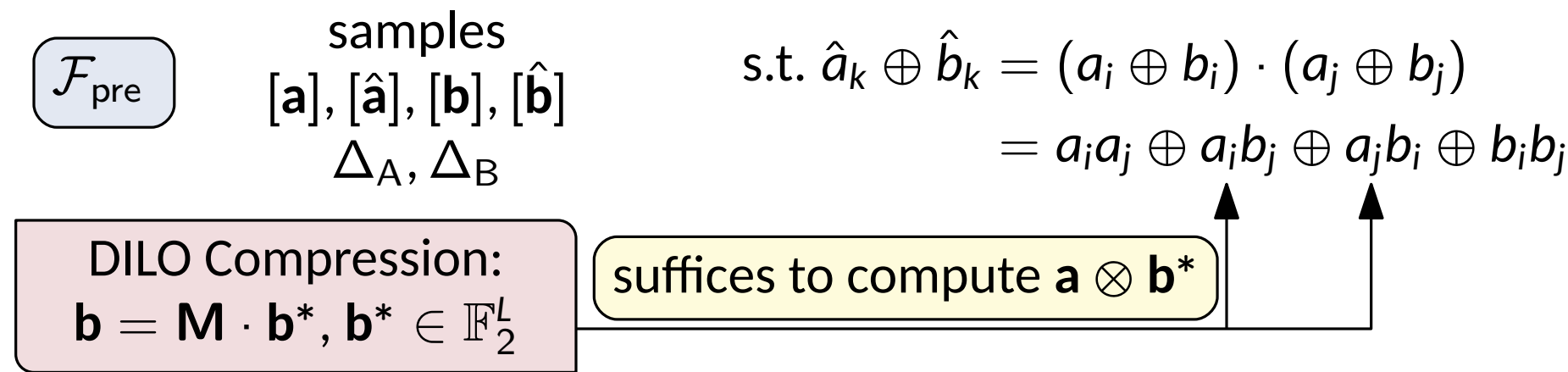
samples
 $[\mathbf{a}], [\hat{\mathbf{a}}], [\mathbf{b}], [\hat{\mathbf{b}}]$
 Δ_A, Δ_B

$$\begin{aligned} \text{s.t. } \hat{a}_k \oplus \hat{b}_k &= (a_i \oplus b_i) \cdot (a_j \oplus b_j) \\ &= a_i a_j \oplus a_i b_j \oplus a_j b_i \oplus b_i b_j \end{aligned}$$

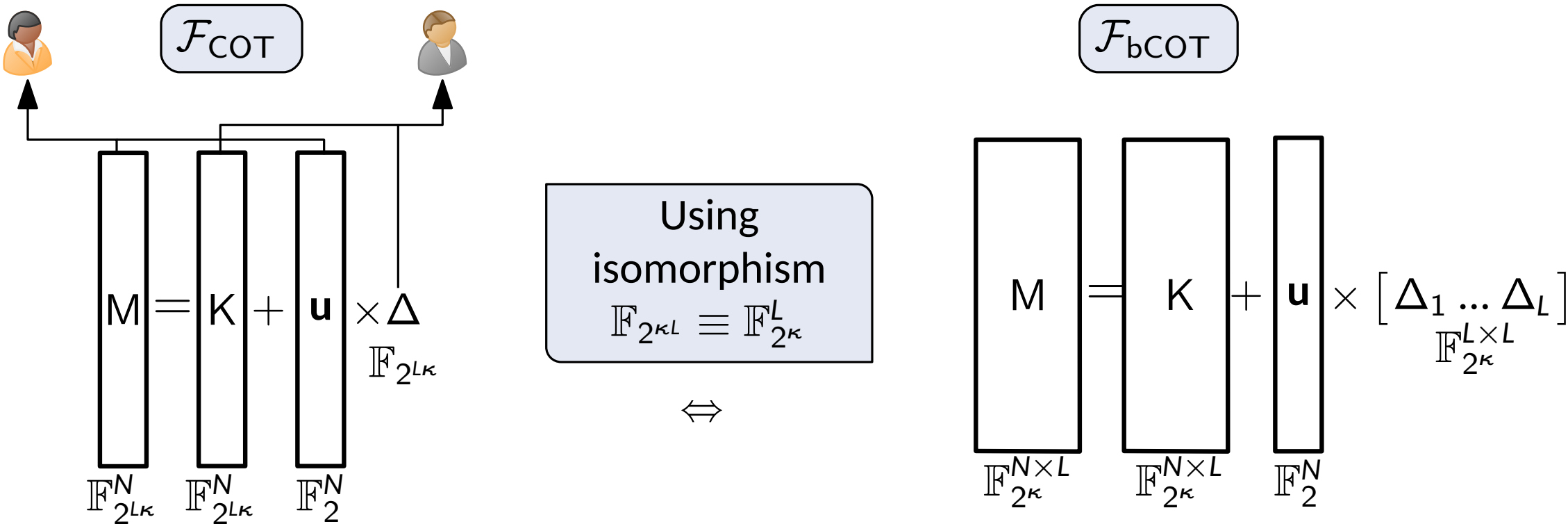
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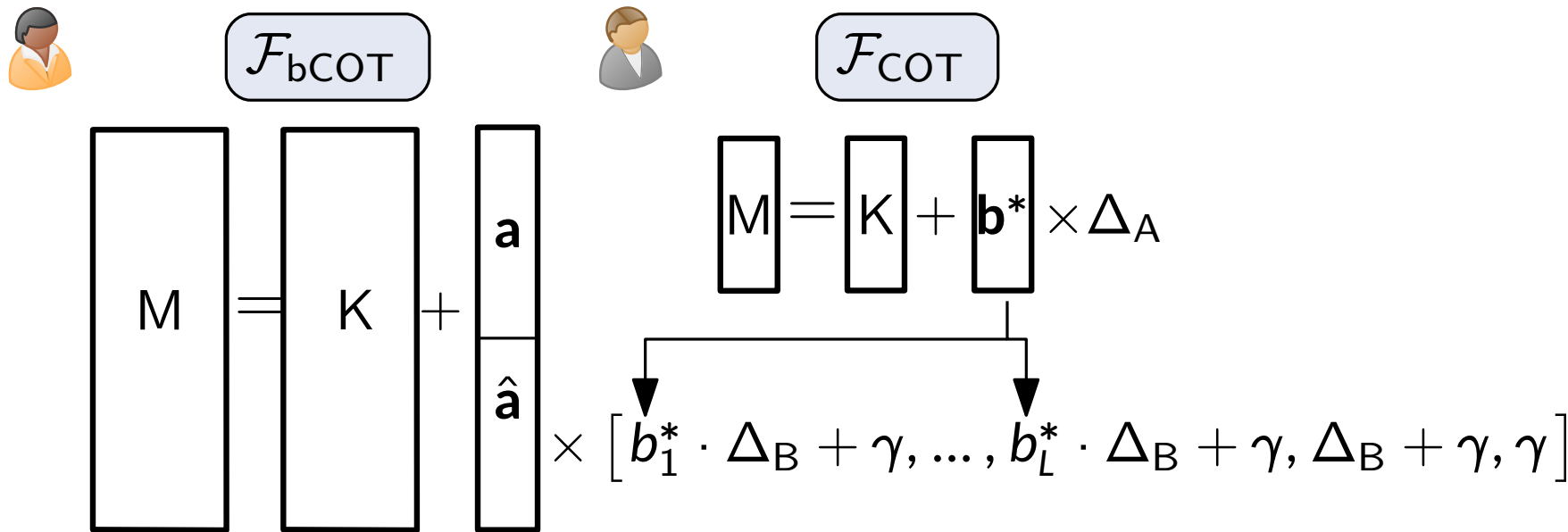
DILO Implementation of $\mathcal{F}_{\text{cpre}}$: Encoding \mathbf{b}^* as Global Keys



- COT can be extended to block COT, preserving PCG efficiency



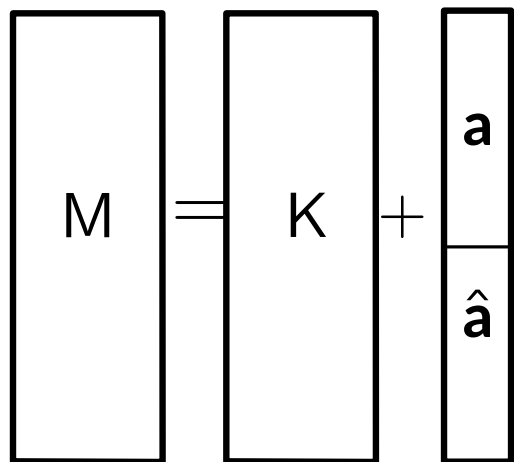
DILO Implementation of $\mathcal{F}_{\text{cpre}}$: Generating \tilde{b}_k



DILO Implementation of $\mathcal{F}_{\text{cpre}}$: Generating \tilde{b}_k



$\mathcal{F}_{\text{bcOT}}$



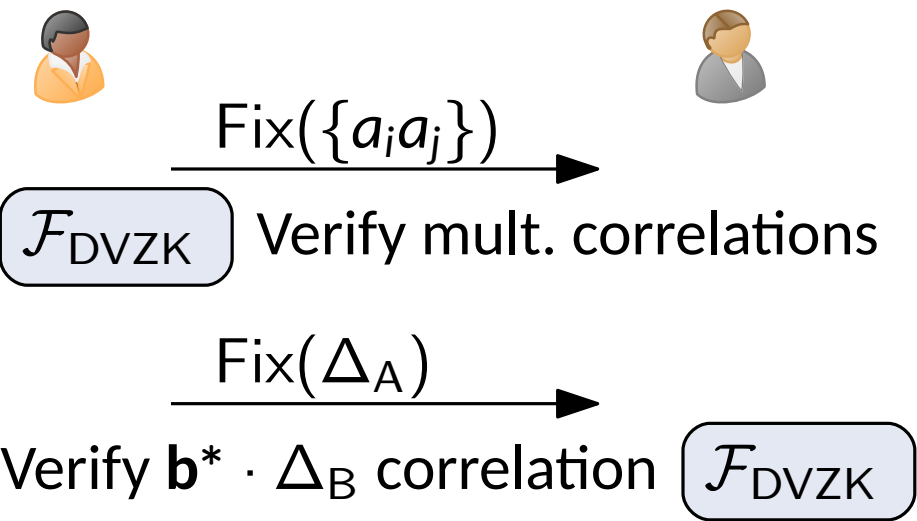
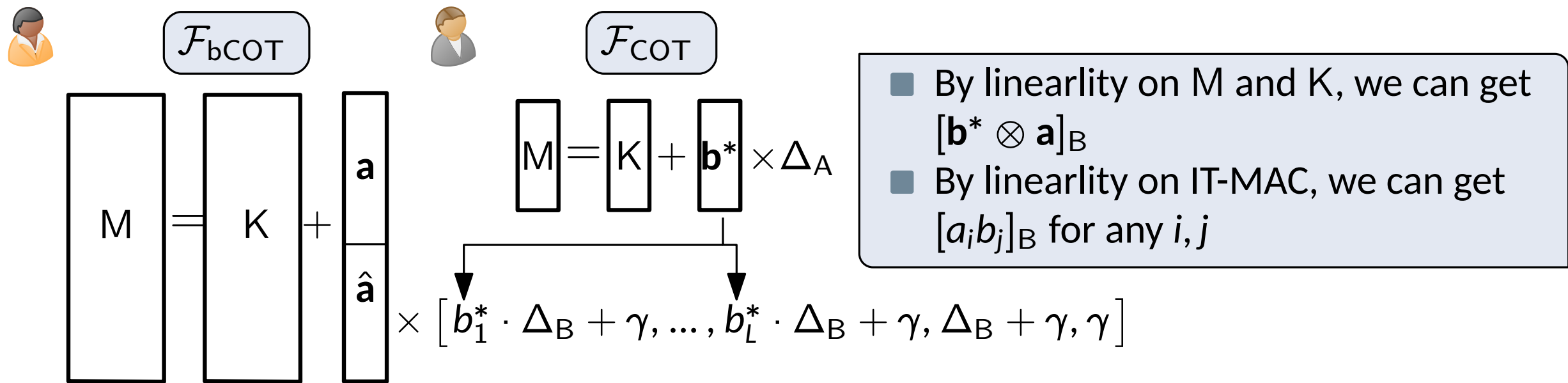
\mathcal{F}_{COT}

$$M = K + \mathbf{b}^* \times \Delta_A$$

$$\times [b_1^* \cdot \Delta_B + \gamma, \dots, b_L^* \cdot \Delta_B + \gamma, \Delta_B + \gamma, \gamma]$$

- By linearity on M and K, we can get $[\mathbf{b}^* \otimes \mathbf{a}]_B$
- By linearity on IT-MAC, we can get $[a_i b_j]_B$ for any i, j

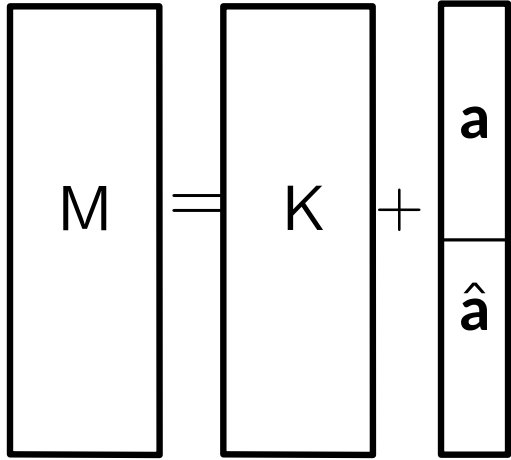
DILO Implementation of $\mathcal{F}_{\text{cpre}}$: Generating \tilde{b}_k



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$\mathcal{F}_{\text{bcOT}}$



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$\text{Fix}(\{a_i a_j\})$

$\mathcal{F}_{\text{DVZK}}$

Verify mult. correlations



Define $[\tilde{b}_k]_B := [\hat{a}_k \oplus a_i a_j \oplus a_i b_j \oplus a_j b_i]_B$

$\text{Fix}(\Delta_A)$

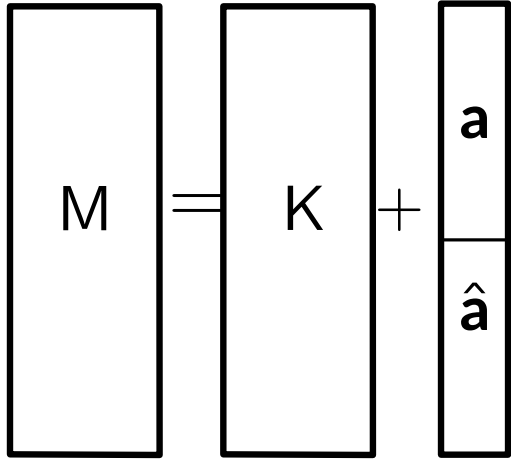
Verify $\mathbf{b}^* \cdot \Delta_B$ correlation

$\mathcal{F}_{\text{DVZK}}$

DILO Implementation of $\mathcal{F}_{\text{cpre}}$: Generating \tilde{b}_k



$\mathcal{F}_{\text{bcOT}}$



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$\text{Fix}(\Delta_A)$

Verify $\mathbf{b}^* \cdot \Delta_B$ correlation

$\mathcal{F}_{\text{DVZK}}$



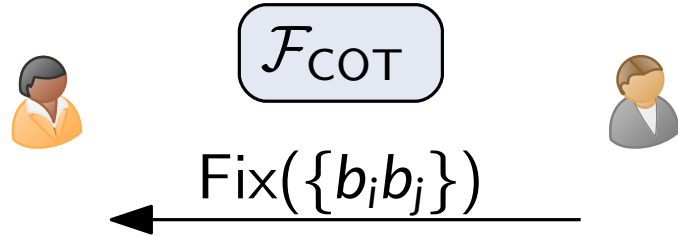
Define $[\tilde{b}_k]_B := [\hat{a}_k \oplus a_i a_j \oplus a_i b_j \oplus a_j b_i]_B$

$$m_{k,1} := M[\tilde{b}_k]$$

- Define $\tilde{b}_k := (m_{k,1} \oplus K[\tilde{b}_k]) \cdot \Delta_B^{-1}$
- Abort if $\tilde{b}_k \notin \{0, 1\}$
- Compute $\hat{b}_k = b_i b_j \oplus \tilde{b}_k$

DILO Implementation of $\mathcal{F}_{\text{cpre}}$: Generating $[\tilde{b}_k]_A$

- It suffices to compute \tilde{b}_k since $[\hat{b}_k]_A = [\tilde{b}_k]_A \oplus [b_i b_j]_A$

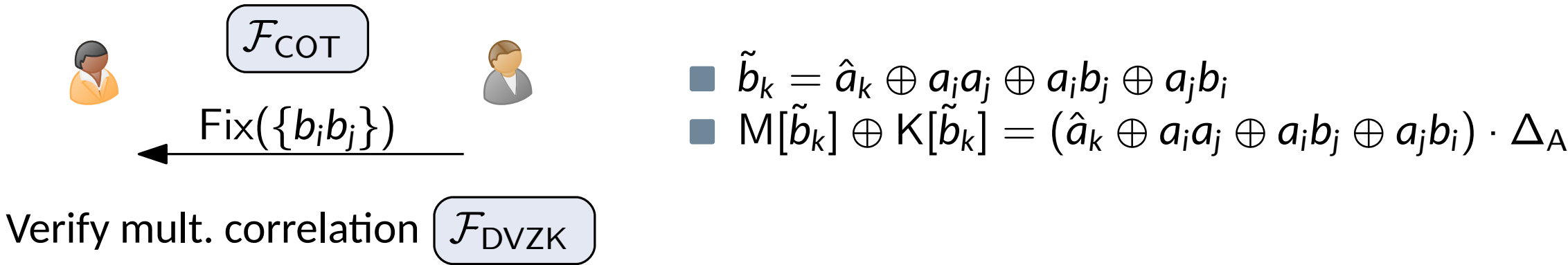


- $\tilde{b}_k = \hat{a}_k \oplus a_i a_j \oplus a_i b_j \oplus a_j b_i$
- $M[\tilde{b}_k] \oplus K[\tilde{b}_k] = (\hat{a}_k \oplus a_i a_j \oplus a_i b_j \oplus a_j b_i) \cdot \Delta_A$

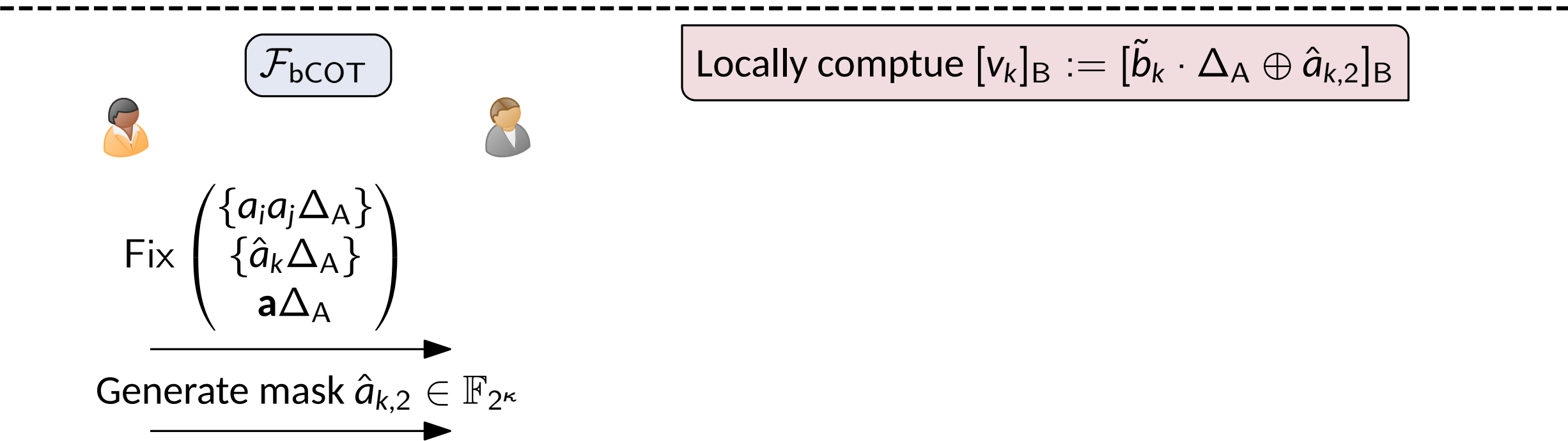
Verify mult. correlation $\mathcal{F}_{\text{DVZK}}$

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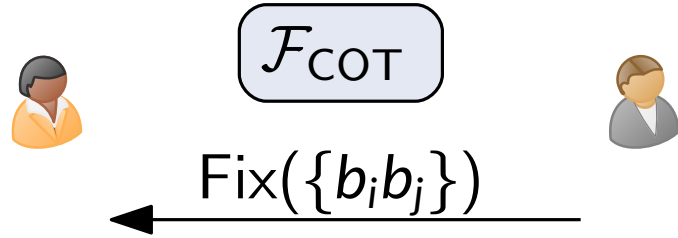


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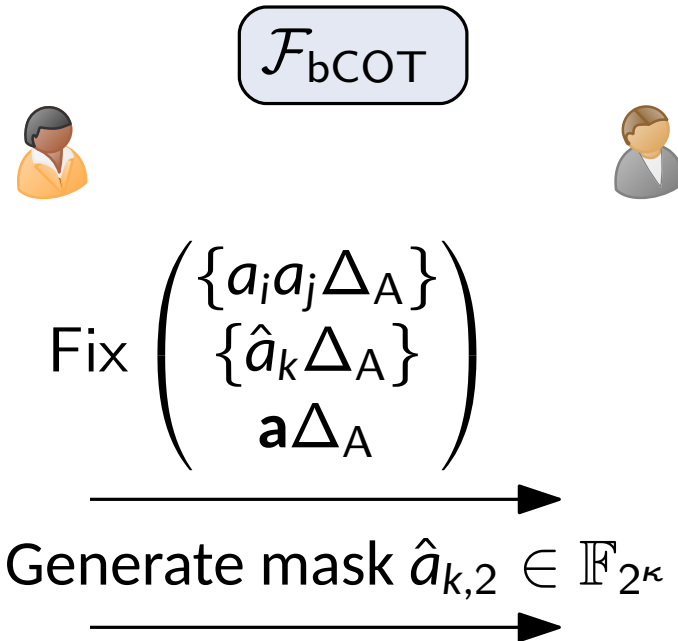
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Verify mult. correlation $\mathcal{F}_{\text{DVZK}}$



Locally compute $[v_k]_B := [\tilde{b}_k \cdot \Delta_A \oplus \hat{a}_{k,2}]_B$

$m_{k,2} := M[v_k]$

Define $K[\tilde{b}_k] := \hat{a}_{k,2}$

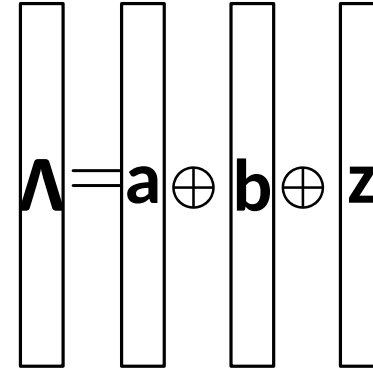
Define $M[\tilde{b}_k] := (m_{k,2} \oplus K[v_k]) \cdot \Delta_B^{-1}$

By the linearity of IT-MAC, $[\hat{b}_k]_A := [b_i b_j]_A \oplus [\tilde{b}_k]_A$

The Online Protocol

KRRW Check:

- Evaluator sends $\{\Lambda_w\}$ for all AND gates
- The checking equation reduces to equality check
- Use random linear combination to reduce comm.


$$\Lambda = a \oplus b \oplus z$$

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DILO-WRK Check

$$\begin{aligned} \Lambda_k \cdot \Delta_B &:= \lambda_k \cdot \Delta_B \oplus (\Lambda_i \oplus \lambda_i) \cdot (\Lambda_j \oplus \lambda_j) \cdot \Delta_B \\ &= \lambda_k \cdot \Delta_B \oplus \Lambda_i \Lambda_j \cdot \Delta_B \oplus \Lambda_i \lambda_j \cdot \Delta_B \oplus \Lambda_j \lambda_i \cdot \Delta_B \oplus (\hat{a}_k \oplus \hat{b}_k) \cdot \Delta_B \end{aligned}$$

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$$= \lambda_k \cdot \Delta_B \oplus \Lambda_i \Lambda_j \cdot \Delta_B \oplus \Lambda_i \lambda_j \cdot \Delta_B \oplus \Lambda_j \lambda_i \cdot \Delta_B \oplus (\hat{a}_k \oplus \hat{b}_k) \cdot \Delta_B$$

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GC

$$\begin{aligned} G_0 &= H(L_{i,0}) \oplus H(L_{i,1}) \oplus a_j \cdot \Delta_A \oplus K[b_j] \\ G_1 &= H(L_{j,0}) \oplus H(L_{j,1}) \oplus a_i \cdot \Delta_A \oplus K[b_i] \oplus L_{i,0} \end{aligned}$$

Use $G_{k,0}, G_{k,1}$ to recover L_k, Λ_k without checking

AuthGC

$$\begin{aligned} G'_{k,0} &= H'(L_{i,0}) \oplus H'(L_{j,0}) \oplus M[a_k] \oplus M[\hat{a}_k] \\ G'_{k,1} &= H'(L_{i,0}) \oplus H'(L_{i,1}) \oplus M[a_j] \\ G'_{k,2} &= H'(L_{j,0}) \oplus H'(L_{j,1}) \oplus M[a_i] \end{aligned}$$

Use $G'_{k,0}, G'_{k,1}, G'_{k,2}$ to recover Λ_k
Abort if $M[\Lambda_k] \oplus K[\Lambda_k] \notin \{0, \Delta_B\}$

$$L_{k,0} = H(L_{i,0}) \oplus H(L_{j,0}) \oplus (a_k \oplus \hat{a}_k) \cdot \Delta_A \oplus K[b_k \oplus \hat{b}_k]$$

Optimizing the Compressed Preprocessing Protocol

The overhead of DILO is
 $5\rho + 2$ bits per AND gate



1 bit

$\text{Fix}(\{b_i b_j\})$

$\rho + 1$ bits

$\text{Fix}(\{a_i a_j\})$

$m_{k,1} := M[\tilde{b}_k]$

4ρ bits

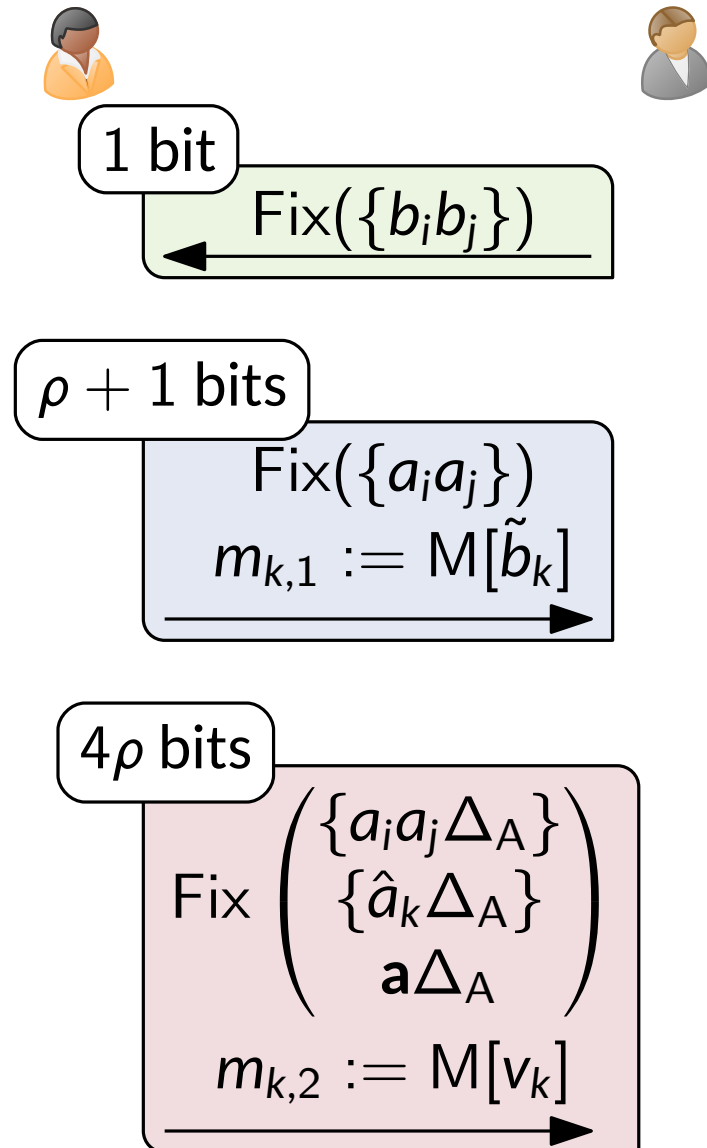
$\text{Fix} \begin{pmatrix} \{a_i a_j \Delta_A\} \\ \{\hat{a}_k \Delta_A\} \\ \mathbf{a} \Delta_A \end{pmatrix}$

$m_{k,2} := M[v_k]$

Optimizing the Compressed Preprocessing Protocol

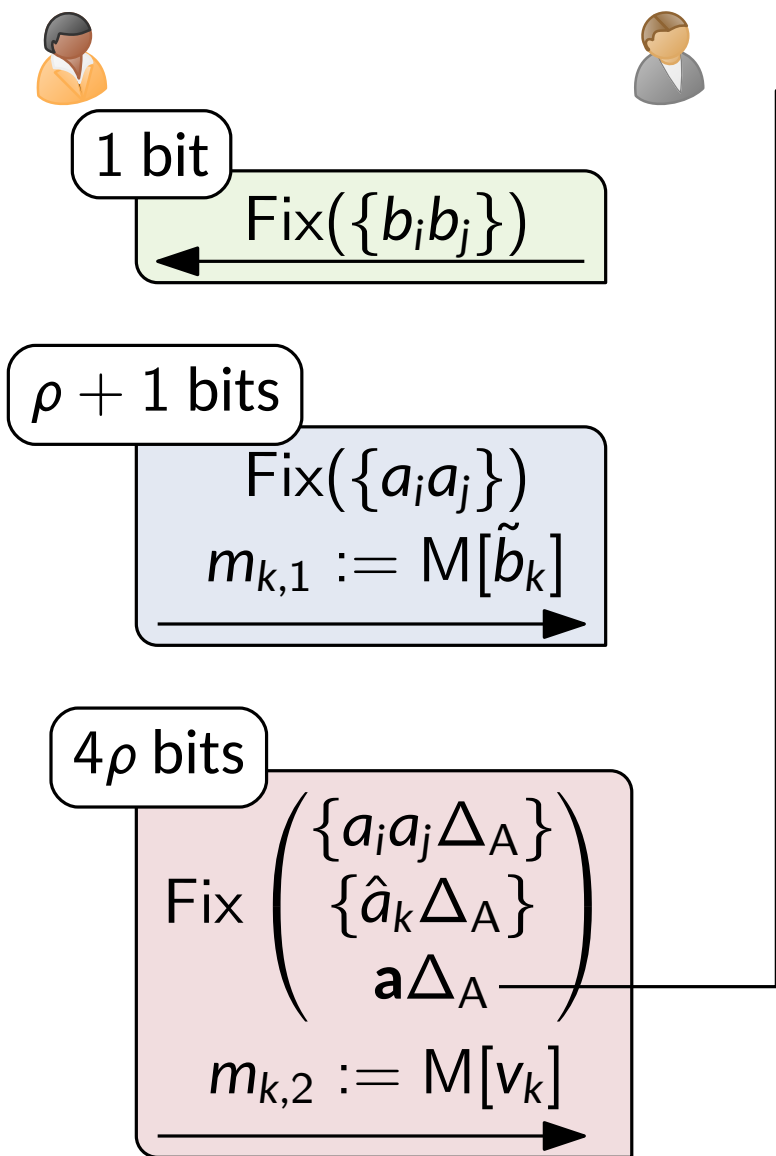
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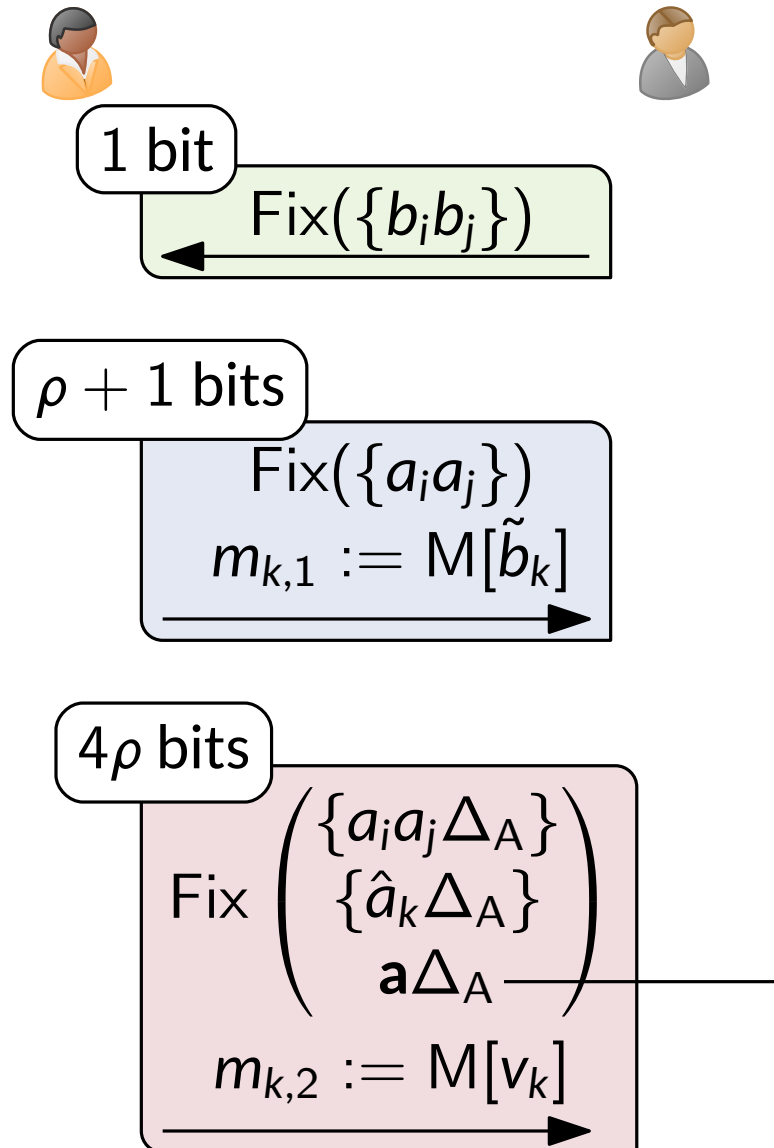


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- $[\mathbf{a} \Delta_A]_B \equiv [\mathbf{a}]_{\Delta_A \cdot \Delta_B}$
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- We denote it as $\langle \mathbf{a} \rangle$

Dual Key Authentication

Optimizing the Compressed Preprocessing Protocol





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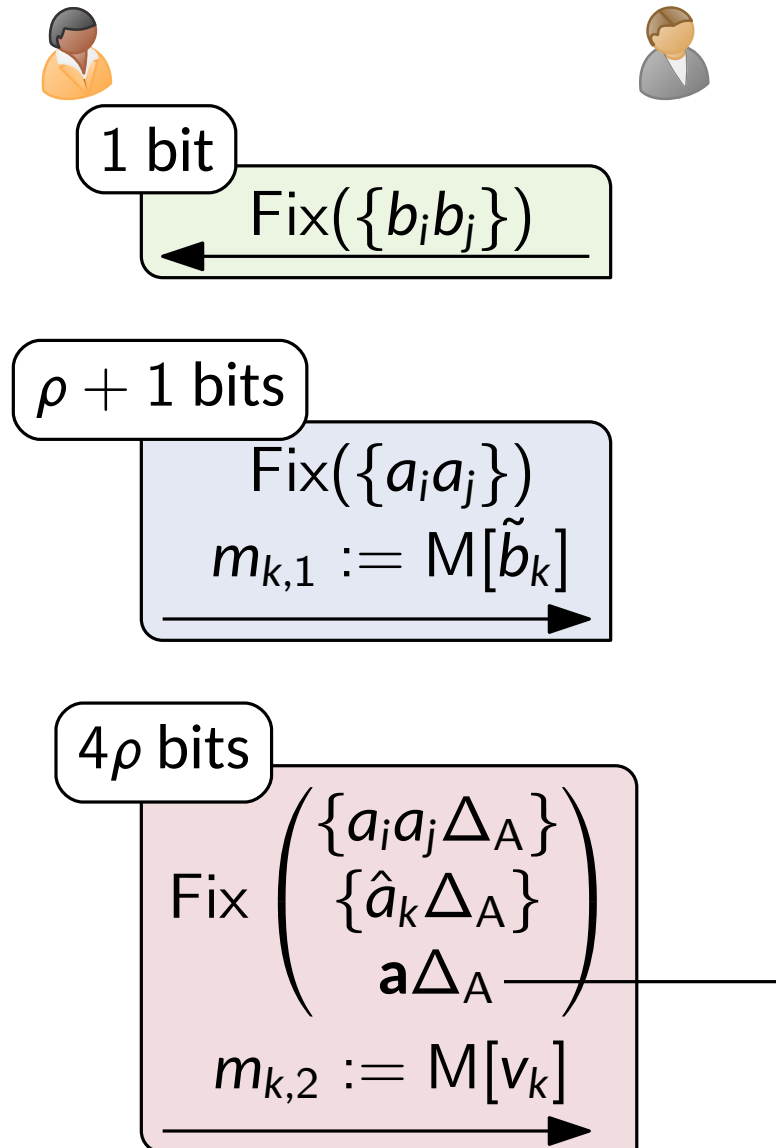
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- Suppose we generate $\langle \tilde{b}_k \rangle$ and $\langle r \rangle, [r]_B$ (mask for )
-  can open $y := \sum_k \chi^k \cdot \tilde{b}_k \oplus r$ and convince 
-  calls $\text{Fix}(\tilde{b}_k)$ and checks $\sum_k \chi^k [\tilde{b}_k] \oplus [r] \oplus y = 0$

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If so we can reduce 4ρ bits to 1 bit

Our goal is to generate $\langle \tilde{b}_k \rangle := \langle \hat{a}_k \rangle \oplus \langle a_i a_j \rangle \oplus \langle a_i b_j \rangle \oplus \langle a_j b_i \rangle$

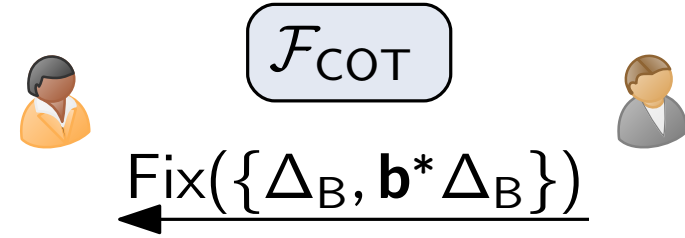
Optimizing the Compressed Preprocessing Protocol, Continued

- $\langle \tilde{b}_k \rangle := \langle \hat{a}_k \rangle \oplus \langle a_i a_j \rangle \oplus \langle a_i b_j \rangle \oplus \langle a_j b_i \rangle$

- $D_A[\hat{a}_k] \oplus D_B[\hat{a}_k] = \hat{a}_k \Delta_A \Delta_B$

- $D_A[a_i b_j] \oplus D_B[a_i b_j] = a_i b_j \Delta_A \Delta_B$

The compression technique allows encoding \mathbf{b} in $\mathcal{F}_{\text{bCOT}}$ global keys



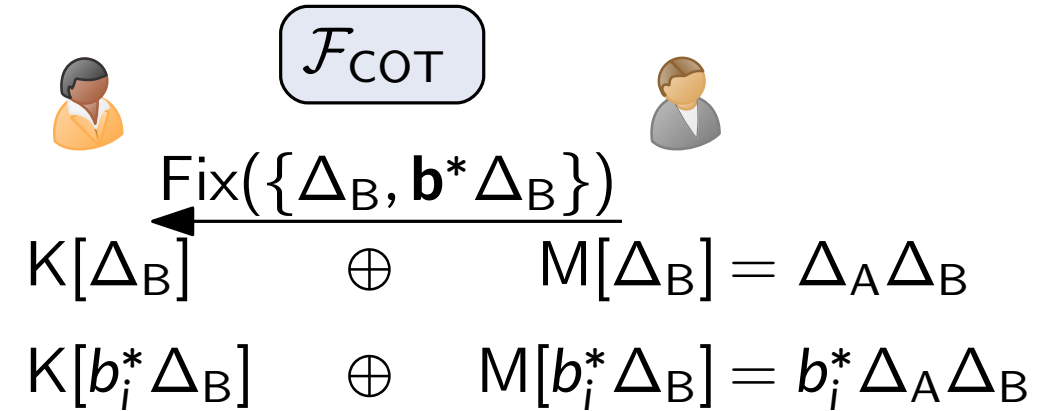
Optimizing the Compressed Preprocessing Protocol, Continued

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Optimizing the Compressed Preprocessing Protocol, Continued

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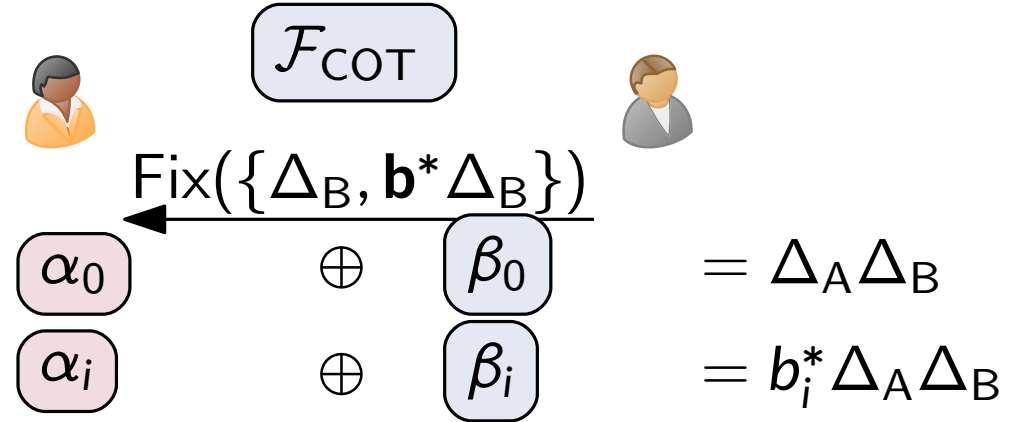
$\mathcal{F}_{\text{bCOT}}^{L+1}$

$$M[a] = K[a] + a \times [\beta_1, \dots, \beta_L, \Delta_B]$$

$\mathcal{F}_{\text{bCOT}}^2$

$$M[\hat{a}] = K[\hat{a}] + \hat{a} \times [\beta_0, \Delta_B]$$

The compression technique allows encoding \mathbf{b} in $\mathcal{F}_{\text{bCOT}}$ global keys



Optimizing the Compressed Preprocessing Protocol, Continued

- $\langle \tilde{b}_k \rangle := \langle \hat{a}_k \rangle \oplus \langle a_i a_j \rangle \oplus \langle a_i b_j \rangle \oplus \langle a_j b_i \rangle$

- $D_A[\hat{a}_k] \oplus D_B[\hat{a}_k] = \hat{a}_k \Delta_A \Delta_B$

- $D_A[a_i b_j] \oplus D_B[a_i b_j] = a_i b_j \Delta_A \Delta_B$



$\mathcal{F}_{\text{bCOT}}^{L+1}$

$$M[a] = K[a] + a \times [\beta_1, \dots, \beta_L, \Delta_B]$$

$\mathcal{F}_{\text{bCOT}}^2$

$$M[\hat{a}] = K[\hat{a}] + \hat{a} \times [\beta_0, \Delta_B]$$

The compression technique allows encoding \mathbf{b} in $\mathcal{F}_{\text{bCOT}}$ global keys

\mathcal{F}_{COT}



$\text{Fix}(\{\Delta_B, \mathbf{b}^* \Delta_B\})$

α_0	\oplus	β_0	$= \Delta_A \Delta_B$
α_i	\oplus	β_i	$= b_i^* \Delta_A \Delta_B$

\Leftrightarrow

$\alpha_0 \cdot \Delta_A^{-1}$	\oplus	$\beta_0 \cdot \Delta_A^{-1}$	$= \Delta_B$
$\alpha_i \cdot \Delta_A^{-1}$	\oplus	$\beta_i \cdot \Delta_A^{-1}$	$= b_i^* \Delta_B$

By $\text{Fix}(\Delta'_A)$

$K[\beta_0]$	\oplus	$\beta_0 \cdot \Delta'_A$	$= M[\beta_0]$
$K[\beta_i]$	\oplus	$\beta_i \cdot \Delta'_A$	$= M[\beta_i]$

Optimizing the Compressed Preprocessing Protocol, Continued

- $\langle \tilde{b}_k \rangle := \langle \hat{a}_k \rangle \oplus \langle a_i a_j \rangle \oplus \langle a_i b_j \rangle \oplus \langle a_j b_i \rangle$

- $D_A[\hat{a}_k] \oplus D_B[\hat{a}_k] = \hat{a}_k \Delta_A \Delta_B$

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The compression technique allows encoding \mathbf{b} in $\mathcal{F}_{\text{bCOT}}$ global keys



$\mathcal{F}_{\text{bCOT}}^{L+1}$

$$M[\mathbf{a}] = K[\mathbf{a}] + \mathbf{a} \times [\beta_1, \dots, \beta_L, \Delta_B]$$

$\mathcal{F}_{\text{bCOT}}^2$

$$M[\hat{\mathbf{a}}] = K[\hat{\mathbf{a}}] + \hat{\mathbf{a}} \times [\beta_0, \Delta_B]$$

\mathcal{F}_{COT}



$\text{Fix}(\{\Delta_B, \mathbf{b}^* \Delta_B\})$

$$\begin{array}{ccc} \alpha_0 & \oplus & \beta_0 \\ \alpha_i & \oplus & \beta_i \end{array} \quad \begin{array}{l} = \Delta_A \Delta_B \\ = b_i^* \Delta_A \Delta_B \end{array}$$

\Leftrightarrow

$$\begin{array}{ccc} \alpha_0 \cdot \Delta_A^{-1} & \oplus & \beta_0 \cdot \Delta_A^{-1} \\ \alpha_i \cdot \Delta_A^{-1} & \oplus & \beta_i \cdot \Delta_A^{-1} \end{array} \quad \begin{array}{l} = \Delta_B \\ = b_i^* \Delta_B \end{array}$$

By $\text{Fix}(\Delta'_A)$

$$\begin{array}{ccc} K[\beta_0] & \oplus & \beta_0 \cdot \Delta'_A \\ K[\beta_i] & \oplus & \beta_i \cdot \Delta'_A \end{array} \quad \begin{array}{l} = M[\beta_0] \\ = M[\beta_i] \end{array}$$

[DIO22] gives a modular way of proving equality under independent keys

Optimizing the Compressed Preprocessing Protocol, Completed



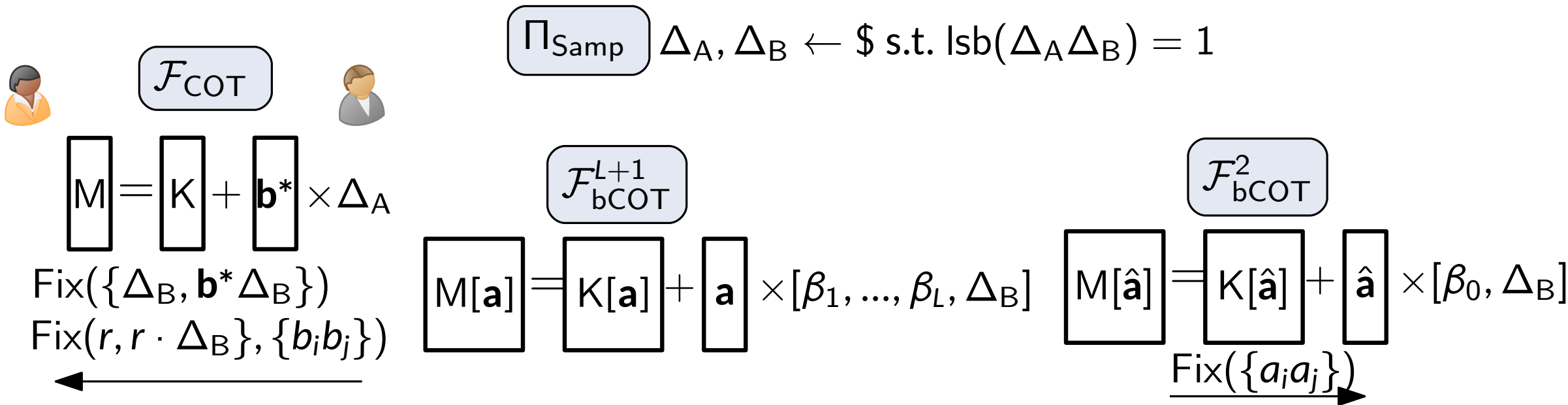
\mathcal{F}_{COT}



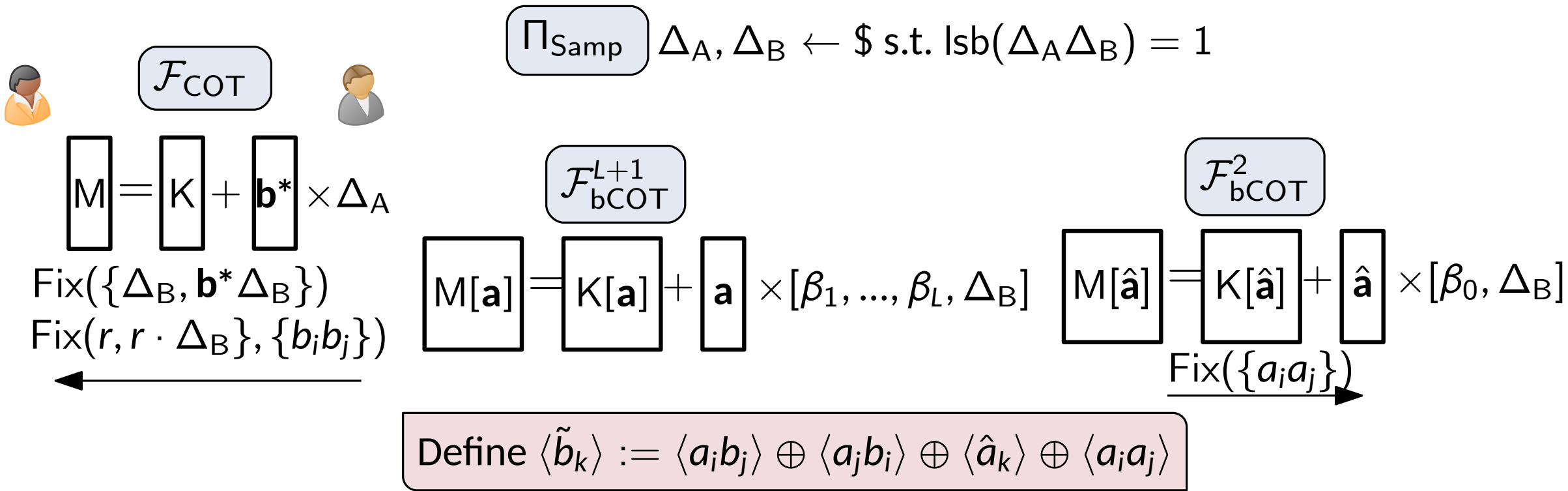
$$\boxed{M} = \boxed{K} + \boxed{\mathbf{b}^*} \times \Delta_A$$

$$\Pi_{\text{Samp}} \Delta_A, \Delta_B \leftarrow \$ \text{ s.t. } \text{lsb}(\Delta_A \Delta_B) = 1$$

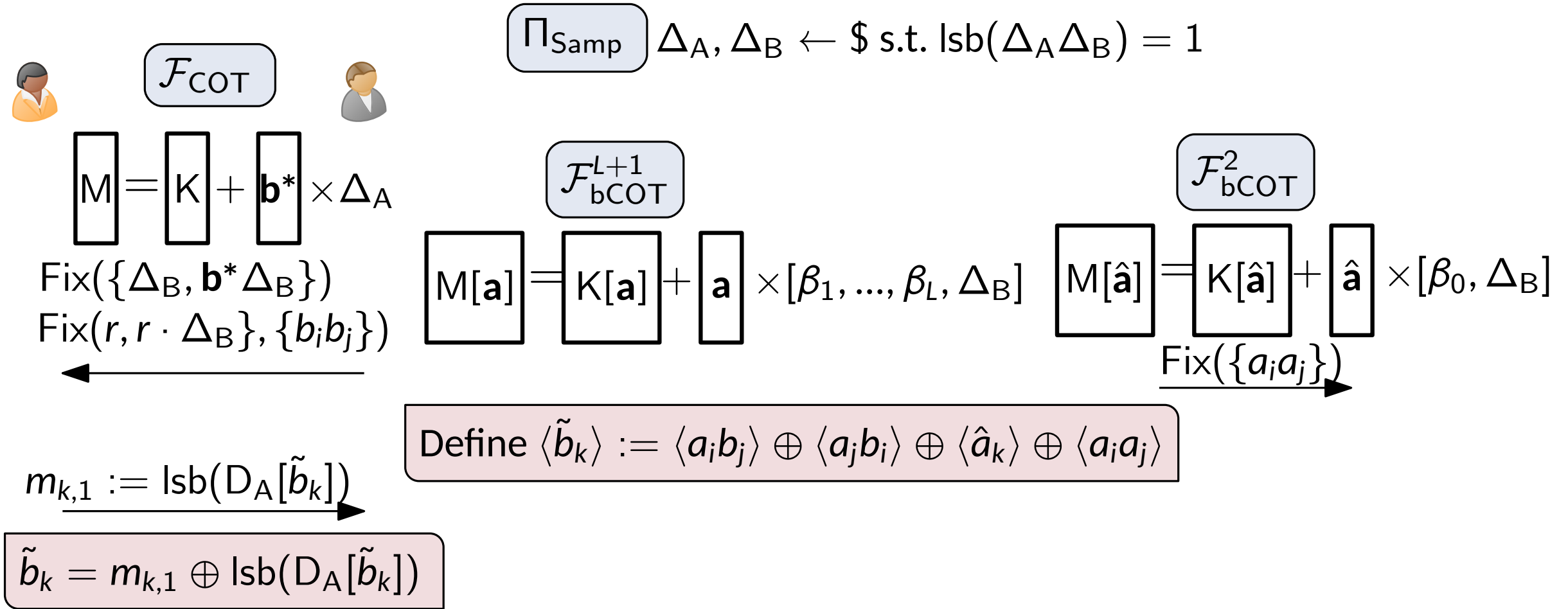
Optimizing the Compressed Preprocessing Protocol, Completed



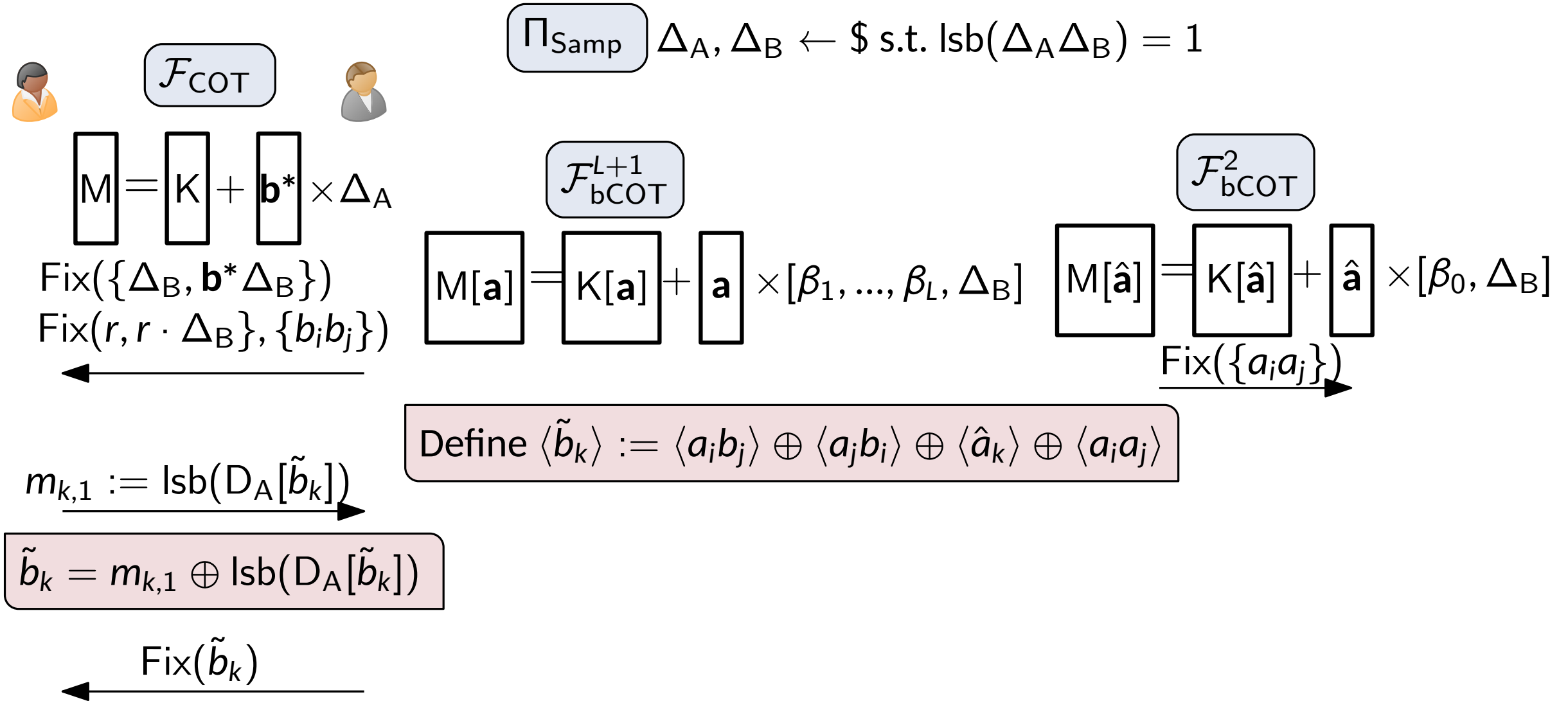
Optimizing the Compressed Preprocessing Protocol, Completed



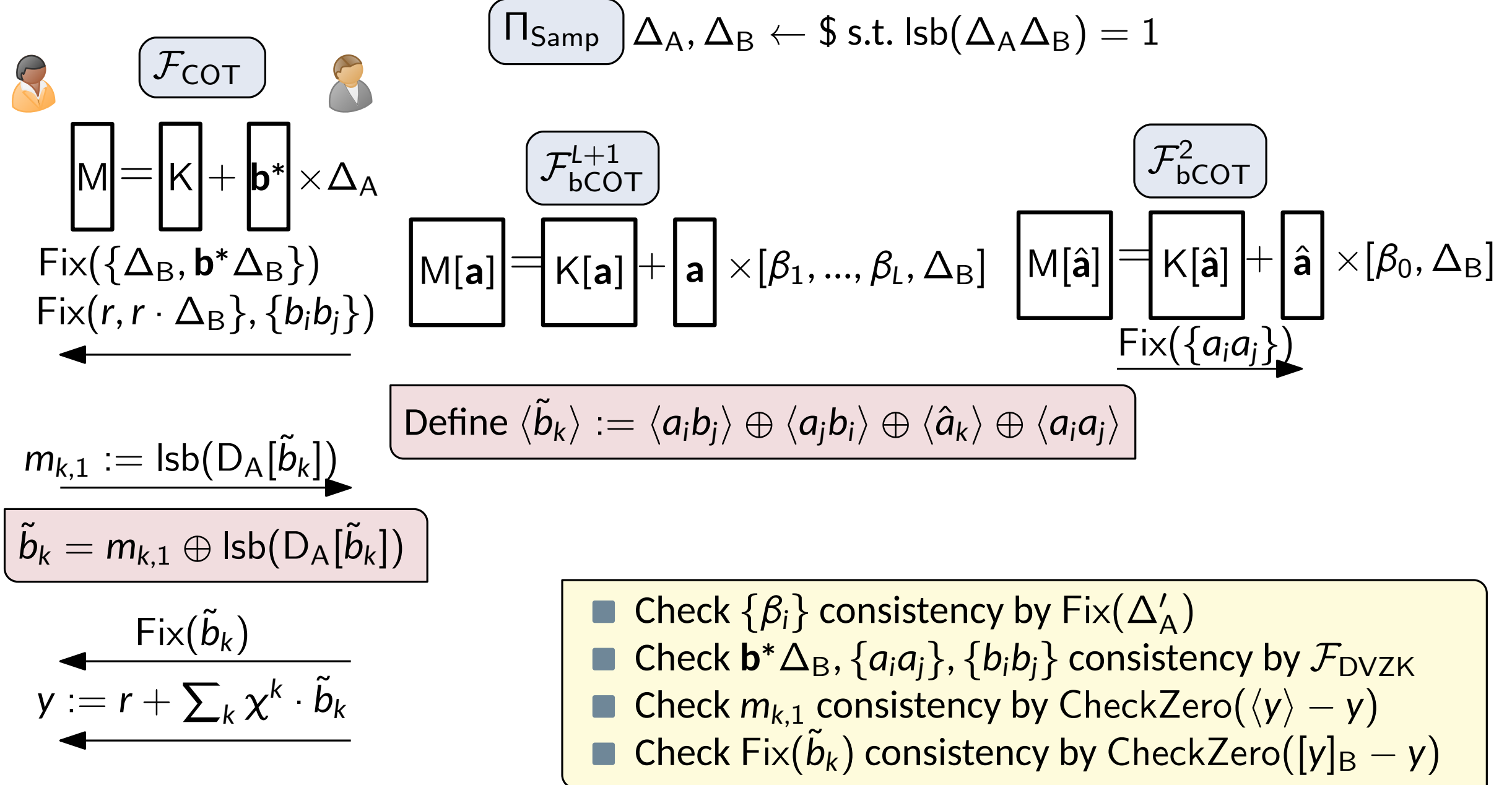
Optimizing the Compressed Preprocessing Protocol, Completed







Optimizing the Compressed Preprocessing Protocol, Completed







Optimizing the Compressed Preprocessing Protocol, Completed

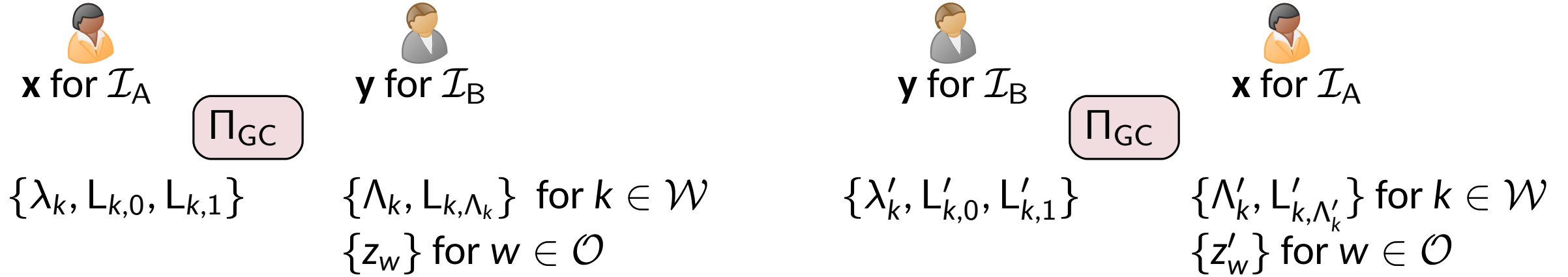


Optimizing the One-way Communication Via Dual Execution





- Optimized $\mathcal{F}_{\text{cpre}}$ + DILO-WRK =  \rightarrow : $2\kappa + 3\rho + 2$ bits,  \rightarrow : 2 bits
- How about optimizing one-way communication? Maybe dual execution?

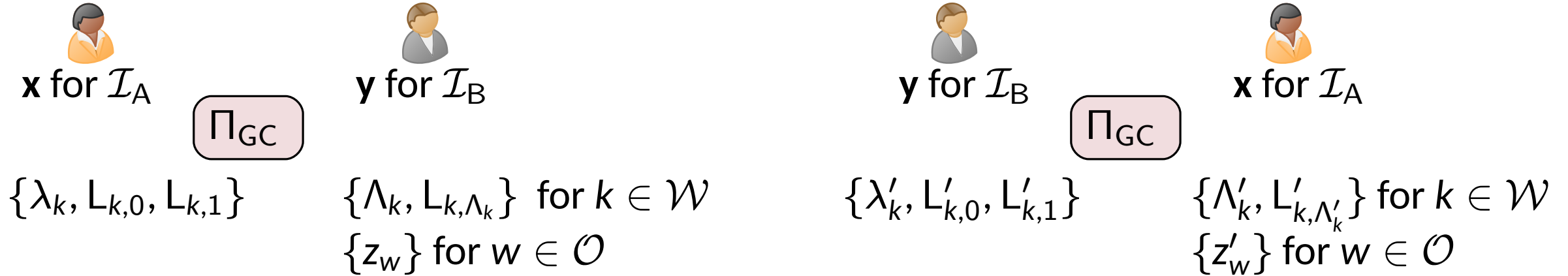
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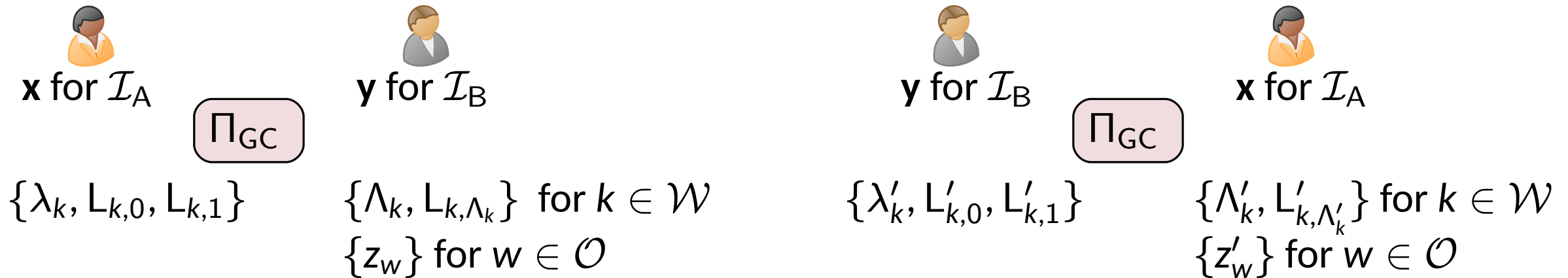


- Semi-honest GC + DualEx [HEK12, HsV20] : Check $z_w = z'_w$ for $w \in \mathcal{O}$

Optimizing the One-way Communication Via Dual Execution

■ Optimized $\mathcal{F}_{\text{cpre}}$ + DILO-WRK =  \rightarrow  : $2\kappa + 3\rho + 2$ bits,  \rightarrow  : 2 bits

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■ Semi-honest GC + DualEx [HEK12, HsV20] : Check $z_w = z'_w$ for $w \in \mathcal{O}$

\mathcal{A} may $\left\{ \begin{array}{l} \text{garble a different circuit } \mathcal{C}' \\ \text{use different input } \mathbf{x} \text{ or } \mathbf{y} \\ \text{launch selective failure} \end{array} \right. \Rightarrow$

- Even if we use a secure \mathcal{F}_{EQ}
- \mathcal{A} can still gain 1-bit leakage

Optimizing the One-way Communication Via Dual Execution

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$\mathcal{F}_{\text{cpre}}$

$$[\mathbf{a}], [\hat{\mathbf{a}}], [\mathbf{b}], [\hat{\mathbf{b}}], \Delta_A, \Delta_B \leftarrow \$$$

$$\text{s.t. } \hat{a}_k \oplus \hat{b}_k = (a_i \oplus b_i) \cdot (a_j \oplus b_j)$$

Π_{DG}

$$\{\lambda_k, L_{k,0}, L_{k,1}\} \quad \{\Lambda_k, L_{k,\Lambda_k}\} \text{ for } k \in \mathcal{W}$$

$\mathcal{F}_{\text{cpre}}$





$$[\mathbf{a}'], [\hat{\mathbf{a}}'], [\mathbf{b}'], [\hat{\mathbf{b}}'], \Delta'_A, \Delta'_B \leftarrow \$$$



$$\text{s.t. } \hat{a}'_k \oplus \hat{b}'_k = (a'_i \oplus b'_i) \cdot (a'_j \oplus b'_j)$$

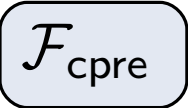
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$$\{\lambda'_k, L'_{k,0}, L'_{k,1}\} \quad \{\Lambda'_k, L'_{k,\Lambda'_k}\} \text{ for } k \in \mathcal{W}$$

Optimizing the One-way Communication Via Dual Execution


- Optimized $\mathcal{F}_{\text{cpre}}$ + DILO-WRK =  \rightarrow  : $2\kappa + 3\rho + 2$ bits,  \rightarrow  : 2 bits
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$$[\mathbf{a}], [\hat{\mathbf{a}}], [\mathbf{b}], [\hat{\mathbf{b}}], \Delta_A, \Delta_B \leftarrow \$$$

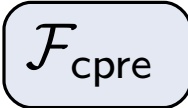
$$\text{s.t. } \hat{a}_k \oplus \hat{b}_k = (a_i \oplus b_i) \cdot (a_j \oplus b_j)$$



$\{\lambda_k, L_{k,0}, L_{k,1}\}$
 $\{\Lambda_k, L_{k,\Lambda_k}\} \text{ for } k \in \mathcal{W}$

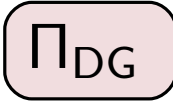
$$L_{k,\Lambda_k} = L_{k,0} \oplus \Lambda_k \cdot \Delta_A$$



$$[\mathbf{a}'], [\hat{\mathbf{a}}'], [\mathbf{b}'], [\hat{\mathbf{b}}'], \Delta'_A, \Delta'_B \leftarrow \$$$

$$\text{s.t. } \hat{a}'_k \oplus \hat{b}'_k = (a'_i \oplus b'_i) \cdot (a'_j \oplus b'_j)$$



$\{\lambda'_k, L'_{k,0}, L'_{k,1}\}$
 $\{\Lambda'_k, L'_{k,\Lambda'_k}\} \text{ for } k \in \mathcal{W}$

$$L'_{k,\Lambda'_k} = L'_{k,0} \oplus \Lambda'_k \cdot \Delta'_B$$

- Color bits and wire masks are authenticated for every wire
- This enables checking equality for every wire across two executions

[HK21] Garbled Sharing

Optimizing the One-way Communication Via Dual Execution



$\mathcal{F}_{\text{cpre}}$



$$[\mathbf{a}], [\hat{\mathbf{a}}], [\mathbf{b}], [\hat{\mathbf{b}}], \Delta_A, \Delta_B \leftarrow \$$$

$$\text{s.t. } \hat{a}_k \oplus \hat{b}_k = (a_i \oplus b_i) \cdot (a_j \oplus b_j)$$

Π_{DG}

$$\{\lambda_k, L_{k,0}, L_{k,1}\} \quad \{\Lambda_k, L_{k,\Lambda_k}\} \text{ for } k \in \mathcal{W}$$

$$L_{k,\Lambda_k} = L_{k,0} \oplus \Lambda_k \cdot \Delta_A$$



$\mathcal{F}_{\text{cpre}}$



$$[\mathbf{a}'], [\hat{\mathbf{a}}'], [\mathbf{b}'], [\hat{\mathbf{b}}'], \Delta_A, \Delta_B \leftarrow \$$$

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Π_{DG}

$$\{\lambda'_k, L'_{k,0}, L'_{k,1}\} \quad \{\Lambda'_k, L'_{k,\Lambda'_k}\} \text{ for } k \in \mathcal{W}$$

$$L'_{k,\Lambda'_k} = L'_{k,0} \oplus \Lambda'_k \cdot \Delta'_B$$

$$\text{Checks } (a_w \oplus b_w \oplus \Lambda_w) \cdot (\Delta_A \oplus \Delta_B) = (a'_w \oplus b'_w \oplus \Lambda'_w) \cdot (\Delta_A \oplus \Delta_B)$$

$$V_w^A = (a_w \oplus a'_w \oplus \Lambda'_w) \Delta_A \oplus M_A[a_w] \oplus M_A[a'_w] \oplus M_A[\Lambda'_w] \oplus K_A[b_w] \oplus K_A[b'_w] \oplus K_A[\Lambda_w],$$

$$V_w^B = (b_w \oplus b'_w \oplus \Lambda_w) \Delta_B \oplus M_B[b_w] \oplus M_B[b'_w] \oplus M_B[\Lambda_w] \oplus K_B[a_w] \oplus K_B[a'_w] \oplus K_B[\Lambda'_w].$$

Conclusion

- Further optimization on the compression technique of [DILO22]
- Dual-key authentication and efficient generation
- Dual execution upon distribution garbling eliminates 1-bit leakage
- Malicious 2PC with one-way comm. of $2\kappa + 5$ bits, two way comm. of $2\kappa + 3\rho + 2$ bits

2PC	Rounds		Communication per AND gate	
	Prep.	Online	one-way (bits)	two-way (bits)
Half-gates	1	2	2κ	2κ
HSS-PCG	8	2	$8\kappa + 11$ (4.04 \times)	$16\kappa + 22$ (8.09 \times)
KRRW-PCG	4	4	$5\kappa + 7$ (2.53 \times)	$8\kappa + 14$ (4.05 \times)
DILO	7	2	$2\kappa + 8\rho + 1$ (2.25 \times)	$2\kappa + 8\rho + 5$ (2.27 \times)
This work	8	3	$2\kappa + 5$ (≈ 1 \times)	$4\kappa + 10$ (2.04 \times)
This work+DILO	8	2	$2\kappa + 3\rho + 2$ (1.48 \times)	$2\kappa + 3\rho + 4$ (≈ 1.48 \times)

Thanks for your listening

Merci beaucoup

