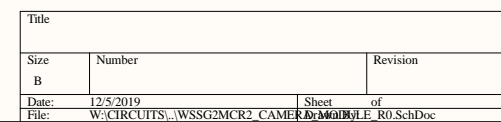
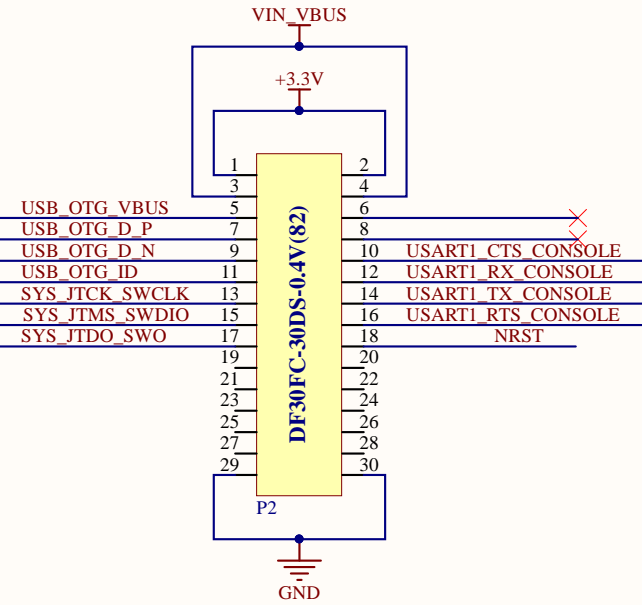
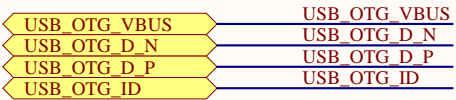
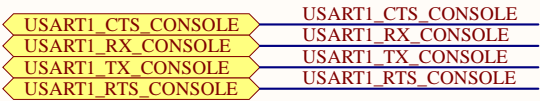
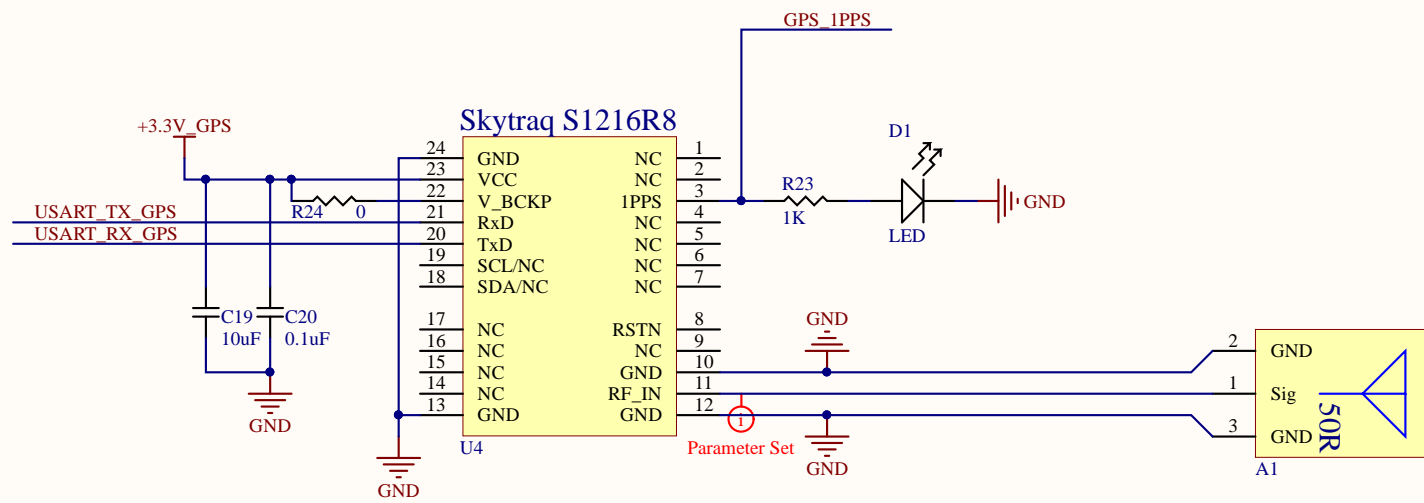
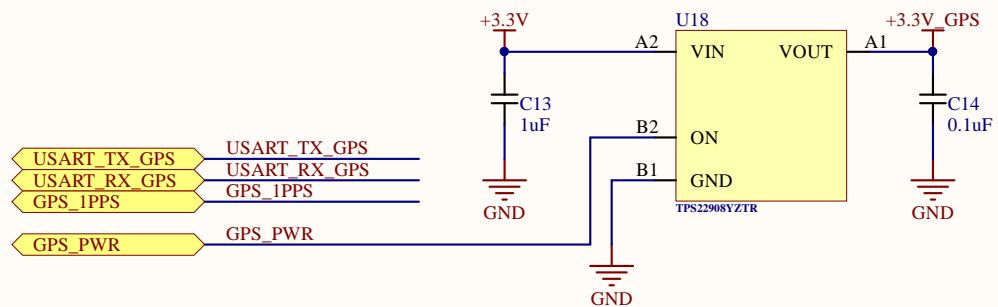


Title		
Size	Number	Revision
A		
Date:	12/5/2019	Sheet of
File:	W:\CIRCUITS\...\WSSG2MCR2_ATTITUDE_CONTROL_R0.SchDoc	2 of 2

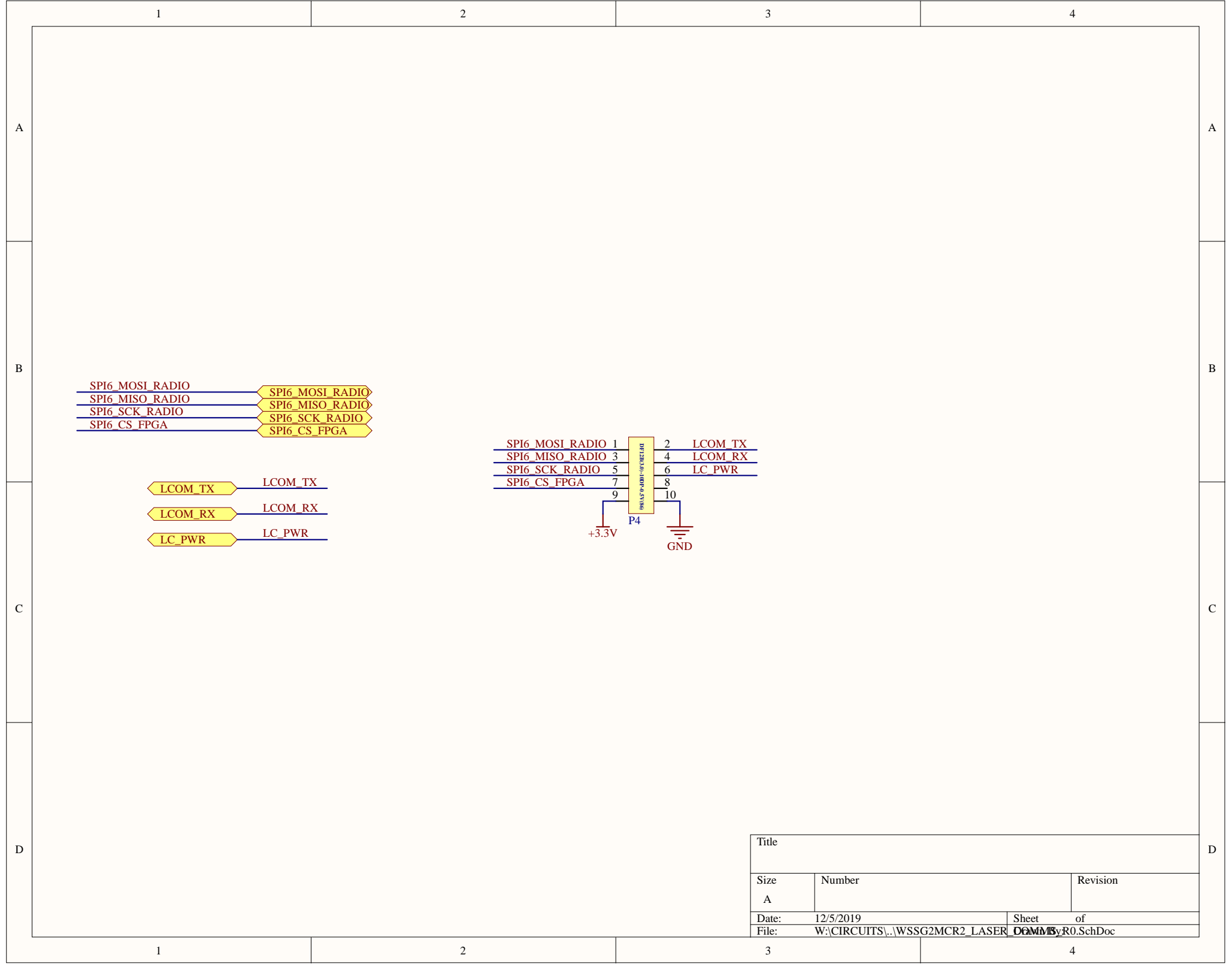




Title		
Size	Number	Revision
A		
Date:	12/5/2019	Sheet of
File:	W:\CIRCUITS\..\WSSG2MCR2_EXTEND\Drawings\	R0.SchDoc

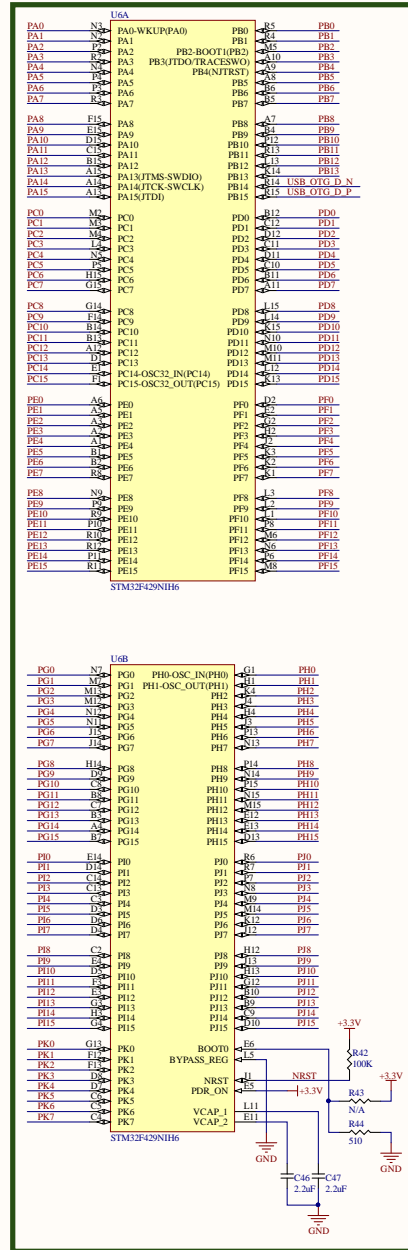


Title		
Size A	Number	Revision
Date:	12/5/2019	Sheet of
File:	W:\CIRCUITS\...\WSSG2MCR2_GPS_R01.dwg	
Drawn By:	B. Scammon	

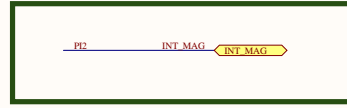




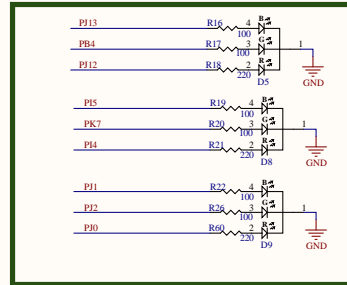
MCU



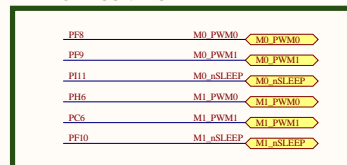
SENSOR



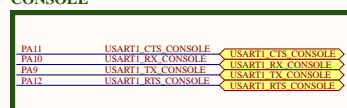
OPTICAL BEACON



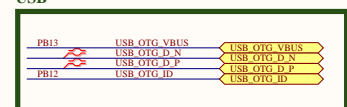
ATTITUDE CONTROL



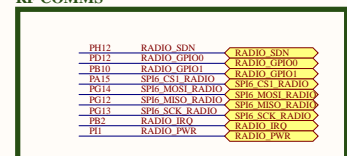
CONSOLE



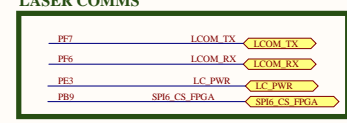
USB



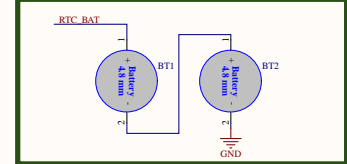
RF COMMS



LASER COMMS



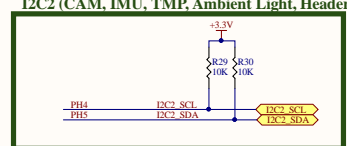
RTC BACKUP



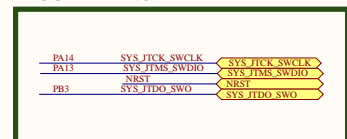
PDU



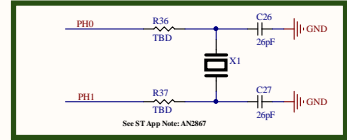
I2C (CAM, IMU, TMP, Ambient Light, Header)



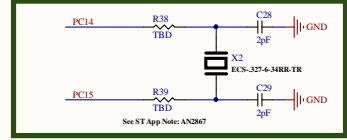
PROGRAMMING HEADER



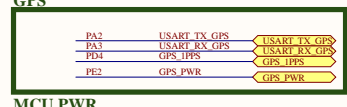
8.0 MHz



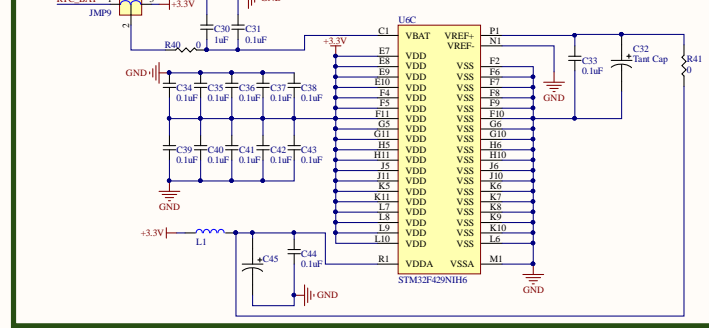
32.768 KHz



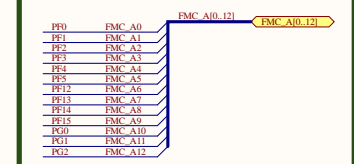
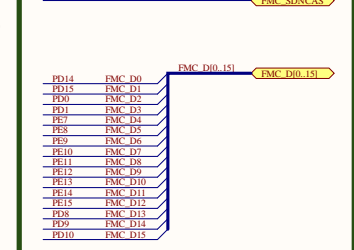
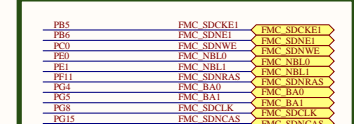
GPS



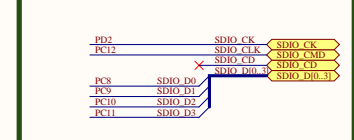
MCU PWR



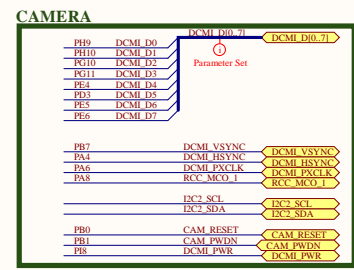
SDRAM

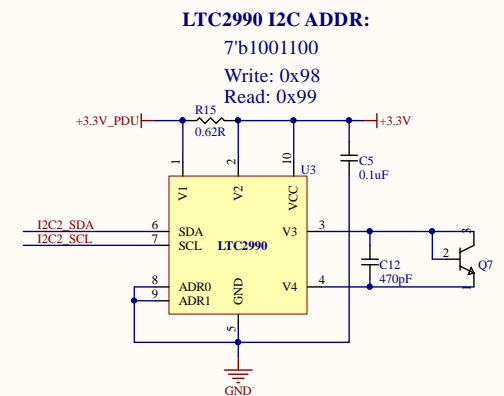
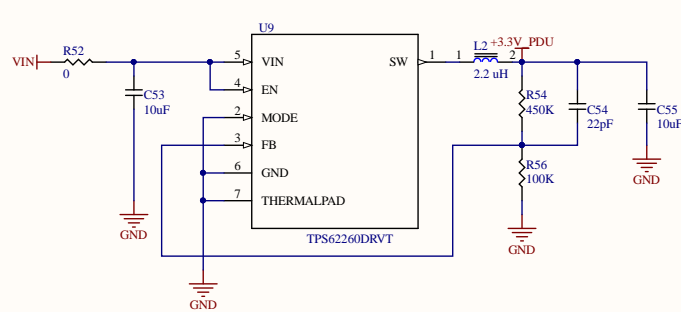
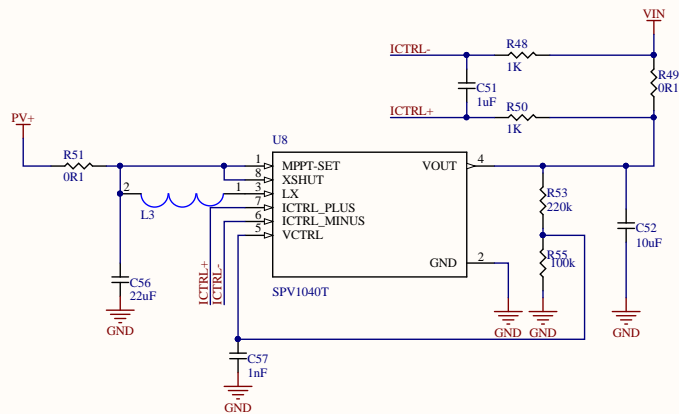
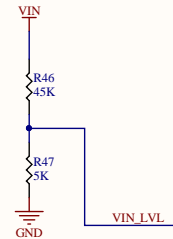
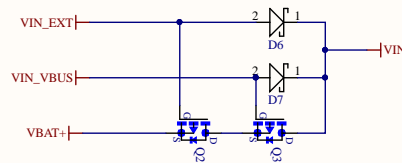
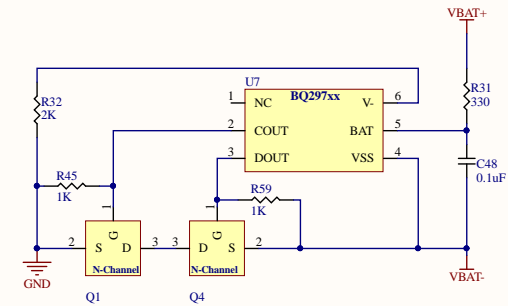
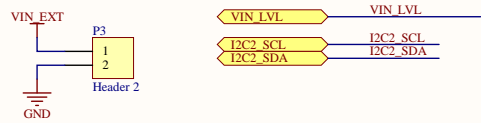


SD CARD



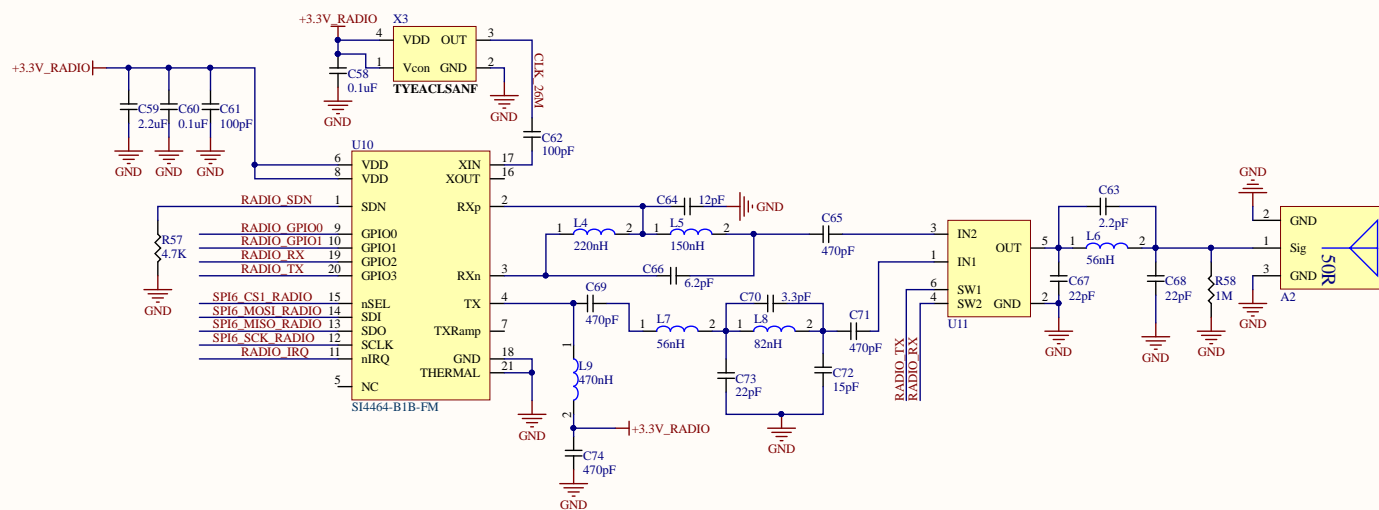
CAMERA



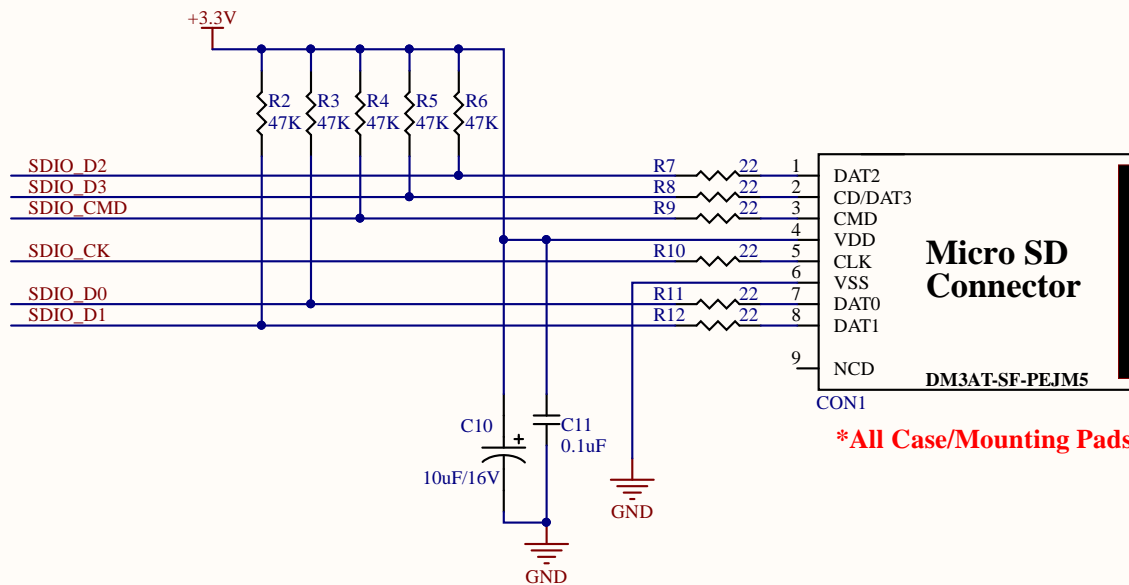
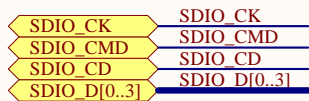


LTC2990 I2C ADDR:
7'b1001100
Write: 0x98
Read: 0x99

Title		
Size B	Number	Revision
Date: 12/5/2019	Sheet 1 of 1	By: R. B. B.
File: W:\CIRCUITS\WSSG2MCR2_PDU_R001.dwg		

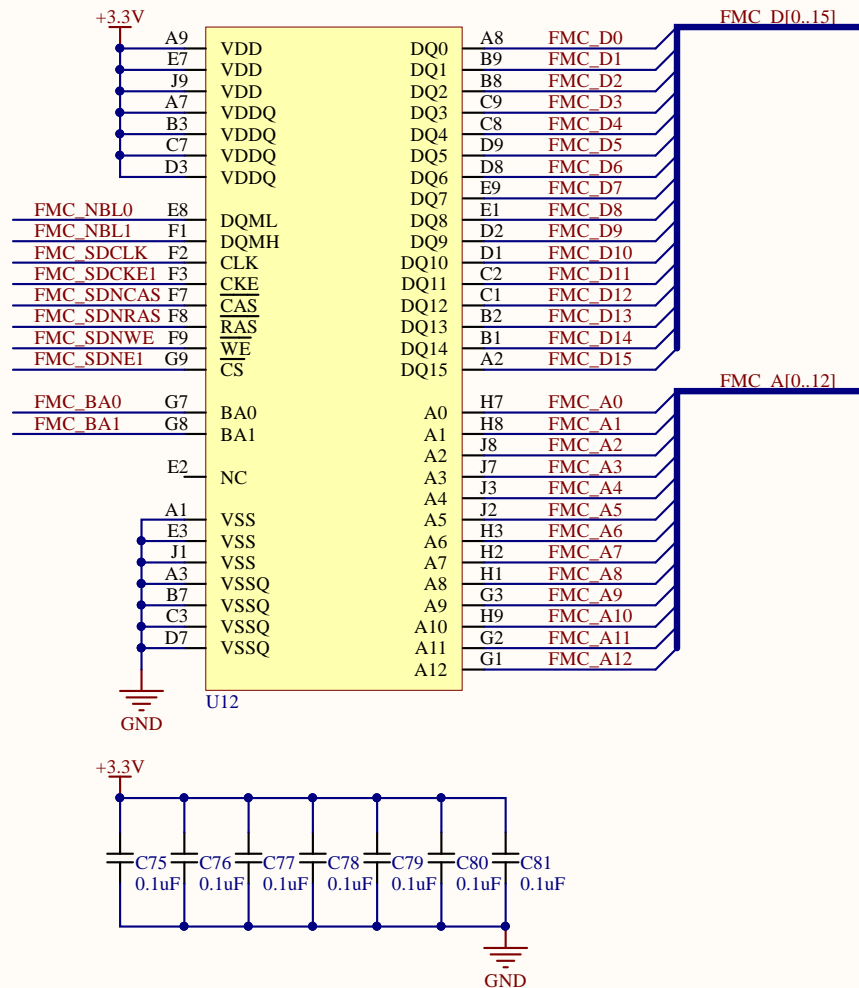
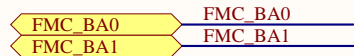
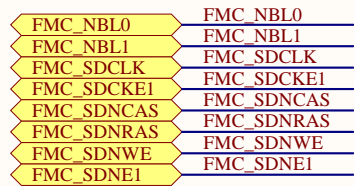


Title		
Size B	Number	Revision
Date: 12/5/2019	Sheet	of
File: W:\CIRCUITS\WSSG2MCR2_RF_COMB\WSSG2MCR2_RF_COMB.dwg	Drawn by: sch	Doc



***All Case/Mounting Pads to GND (Not Shown)**

Title		
Size	Number	Revision
A		
Date:	12/5/2019	Sheet of
File:	W:\CIRCUITS\..\WSSG2MCR2_SD_R0	Drawn By:



Title		
Size	Number	Revision
A		
Date:	12/5/2019	Sheet of
File:	W:\CIRCUITS\..\WSSG2MCR2_SDRAM\DR061B.doc	

