Eric Nieto Gonzalez

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For Process Integration Engineering Internship

Dear TSMC,

As an Electrical Engineering major at the University of Illinois Urbana-Champaign with a strong interest in semiconductors, I am very interested in the Process Integration Engineering Internship. I learned about this position through the UIUC Engineering Spring Career Fair where I met Nicholas C. Based on the qualifications listed on the TSMC website, Nicholas' descriptions of the role, and my aligned values of improving semiconductors, I feel I am a good fit for this position.

I take pride in my drive and commitment, particularly in leadership roles within campus courses and organizations. For instance, in my IC *Device Theory and Fabrication* course, we received the opportunity to be in a fabrication lab and learn about the physical theory and design of devices, emphasizing on semiconductor properties. Techniques covered include epitaxial growth, oxidation, and photolithography to name a few for fabricating devices. Within this process, I took the initiative to lead our team's wafer etching process. This critical task involves handling hazardous chemicals like *Hydrofluoric Acid*, and I volunteered for it not only to safeguard my team but also to gain hands-on experience in a field I am passionate about pursuing. My proactive approach reflects my dedication to both my team's success and my professional growth. This semester, I've been building on this experience by taking courses in *Nanotechnology* and *LEDs and Solar Cells*, where I have been learning skills in theoretical calculations and additional cleanroom lab experience.

I am confident that my professional and educational background, complemented by my motivation and energy, would be great assets to TSMC. Although the attached resume outlines my accomplishments, a personal interview would be the only way to fully illustrate why I am an excellent fit for this position. Thank you for your time and consideration.

Sincerely,

Eric Nieto Gonzalez

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