Grade:_____

Name:_____

Name:_____   Lab Worksheet

Provide the truth table for your implemented circuit

S2	S1	S0	A	P
0	0	0		
0	0	1		
0	1	0		
0	1	1		
1	0	0		
1	0	1		
1	1	0		
1	1	1		

Indicate which TTL DIPs are
placed where on the protoboard