

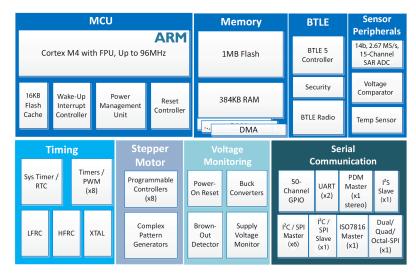
Apollo3 Blue Low Power MCU Family

Product Brief



capability and extended life to battery operated devices.

The Apollo3 Blue brings several new features to Ambiq's SPOT-based Apollo MCU Family including an integrated DMA engine, QSPI interface, and advanced stepper motor control for ultra-low power analog watch hand management. With unprecedented energy efficiency and PDM microphone inputs, the Apollo3 Blue also forms the core of Ambiq's Voice-on-SPOT™ reference platform making it the perfect device for customers who want to add always-on voice assistant integration and command recognition to battery-powered devices. To increase design flexibility and enable connections to the phone and cloud, the Apollo3 Blue provides a dedicated second core for the ultra-low power BLE5 connectivity platform providing superior RF throughput and leaving plenty of resources available for user applications.



Block Diagram for the Ultra-Low Power Apollo3 Blue MCU



Apollo3 Blue AMA3B1KK-KBR EVB

Feature Highlights:

- An ideal solution for battery-powered applications requiring sensor measurement and data analysis.
- Serves as an applications processor for one or more sensors and has a fully integrated BLE5 radio.
- A host processor can communicate with the Apollo3 Blue MCU over its serial slave port using the I²C, SPI or I²S protocol.
- TurboSPOT technology allows applications to meet critical timing as/when needed while still providing extremely high energy efficiency operation.
- Enables months and years of battery life for products only achieving days or months of battery life today.
- Implementation of the Cortex-M4F core delivers both greater performance and much lower power than 8-bit, 16-bit, and other comparable 32-bit cores.
- Supports highly optimized PWM pattern generation for complex, efficient stepper motor control operation.
- Supported by a complete suite of standard software development tools.

Features and Specifications

Ultra-Low Supply Current

- $6 \mu A/MHz$ executing from flash or RAM at 3.3 V
- 1 µA deep sleep mode (BLE Off) with RTC at 3.3 V (BLE in SD)

High-Performance Arm Cortex-M4 Processor

- Up to 48 MHz nominal clock frequency with 96 MHz performance TurboSPOT Mode
- Floating Point Unit
- Memory protection unit (MPU)
- · Wake-up interrupt controller with 32 interrupts

Integrated Bluetooth® Low-Energy Module

- RF sensitivity: -94 dBm (typical)
- Tx: 3 mA @ 0 dBm, Rx: 3 mA
- Tx peak output power: 4.0 dBm (max)

Ultra-low Power Memory

- · Up to 1MB of flash memory for code/data
- Up to 384KB of low power RAM for code/data
- 16KB 2-way Associative/Direct-Mapped Cache

Ultra-low Power Interface for On- and Off-Chip Sensors

- 14-bit ADC, 15 selectable input channels available
- Up to 2.67 MS/s sampling rate
- Voltage Comparator (VCOMP)
- Temperature sensor with +/-3°C accuracy after calibration

Ultra-low Power Flexible Serial Peripherals

- ISO7816 Secure interface
- 1x 2/4/8-bit SPI master interface (MSPI)
- 6x I²C/SPI masters for peripheral communication
- 1x I²C/SPI slave for host communications
- 2x UART modules with 32-location Tx and Rx FIFOs
- PDM for mono and stereo audio microphones
- 1x 12S slave for PDM audio pass-through

Rich Set of Clock Sources

- · 32.768 KHz Crystal (XTAL) oscillator
- Low Frequency RC (LFRC) oscillator (1.024 KHz)
- High Frequency RC (HFRC) oscillator (48 MHz)
- RTC based on Ambig's AM08X5/18X5 family

Wide Operating Range

1.755-3.63 V, -40°C to 85°C

Applications

- Voice-on-SPOT compatible for always-listening keyword detect, audio command recognition and voice assistant integration in battery-powered devices including:
 - Bluetooth headsets, earbuds, and truly wireless earbuds
 - Remote and Gaming Controls
 - Smart home
- Wearables including smart watches and fitness/activity trackers
- Hearing aids, Digital Health Monitoring and Sensing Devices
- Smart Home Automation, Security and Lighting control applications

Package Options

- · 4.5mm x 4.5mm, 81-pin BGA with 50 GPIO
- 3.25mm x 3.37mm, 66-pin CSP with 37 GPIO

Ordering Information

- AMA3B1KK-KBR-B0 (384KB, 81-pin BGA)
- AMA3B1KK-KCR-B0 (384KB, 66-pin CSP)
- AMAP31KK-KCR (non-BLE, 384KB, 66-pin CSP)





AMA3B1KK-KBR-B0 BGA

AMA3B1KK-KCR-B0 CSP





