Riddock Moran

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| EDUCATION | |
|--|---|
| University of Nebraska- Lincoln | Graduate in May 2022 |
| Bachelor of Science in Computer Engineering | GPA: 3.89/4.00 |
| Lincoln, NE | |
| Work Experience | |
| UNL Computer Science and Engineering - | |
| Computer Science I Course Leader | August 2020 – Present |
| Assist Instructors and mentor Learning Assistants | C |
| Do quality checks on grading and evaluate LA performance | rmance |
| Lincoln, NE | |
| Lincoln Financial Group - | |
| Software Engineering Intern | June 2020 – July 2020 |
| Replatformed Java application onto Docker container | r |
| Created GitLab pipeline for continuous deployment | |
| Omaha, NE | |
| UNL Computer Science and Engineering - | |
| Computer Science I Learning Assistant | August 2019 – May 2020 |
| • Help students learn basic computer science using C | |
| Lincoln, NE | |
| CLASS EXPERIENCE | |
| Human Computer Interaction | Fall 2020 |
| Operating System Kernels – C | Fall 2020 |
| Software Engineering – C# .NET, SQL | Fall 2020 |
| Data Structures & Algorithms – C++ | Spring 2020 |
| Embedded Systems – Arduino, C, C++ | Spring 2020 |
| Built mobile robot controlled by ultrasonic sensor and | |
| · · · · · · · · · · · · · · · · · · · | d IR remote |
| Created 120V wall fan system controlled by humidity | |
| Created 120V wall fan system controlled by humidity and user input | |
| Created 120V wall fan system controlled by humidity and user input Computer Organization – Assembly, VHDL | y, temperature, Fall 2019 |
| Created 120V wall fan system controlled by humidity and user input Computer Organization – Assembly, VHDL Designed a 16-bit processor to run Assembly code or | y, temperature, Fall 2019 n an FPGA board |
| Created 120V wall fan system controlled by humidity and user input Computer Organization – Assembly, VHDL Designed a 16-bit processor to run Assembly code or Computer Science II – Java, SQL | y, temperature, Fall 2019 an an FPGA board Spring 2019 |
| Created 120V wall fan system controlled by humidity and user input Computer Organization – Assembly, VHDL Designed a 16-bit processor to run Assembly code or | y, temperature, Fall 2019 n an FPGA board |
| Created 120V wall fan system controlled by humidity and user input Computer Organization – Assembly, VHDL Designed a 16-bit processor to run Assembly code or Computer Science II – Java, SQL Unix Programming – Unix Computer Science I – C | Fall 2019 an an FPGA board Spring 2019 Spring 2019 |
| Created 120V wall fan system controlled by humidity and user input Computer Organization – Assembly, VHDL Designed a 16-bit processor to run Assembly code or Computer Science II – Java, SQL Unix Programming – Unix Computer Science I – C INVOLVEMENT | Fall 2019 n an FPGA board Spring 2019 Spring 2019 Fall 2018 |
| Created 120V wall fan system controlled by humidity and user input Computer Organization – Assembly, VHDL Designed a 16-bit processor to run Assembly code or Computer Science II – Java, SQL Unix Programming – Unix | Fall 2019 an an FPGA board Spring 2019 Spring 2019 |