

# Riddock Moran

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## EDUCATION

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<b>University of Nebraska- Lincoln</b>	Graduate in May 2022
Bachelor of Science in Computer Engineering	<b>GPA: 3.73/4.00</b>
Lincoln, NE	

## WORK EXPERIENCE

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<b>Vertiv - Engineering Intern</b>	May 2021 – Present
<ul style="list-style-type: none"><li>Created testing process for new product</li><li>Optimized end-of-line times and tested multiple products and components</li></ul>	
Lincoln, NE	
<b>UNL School of Computing - CS II Learning Assistant</b>	January 2021 – May 2021
<ul style="list-style-type: none"><li>Helped students learn software development fundamentals using Java</li></ul>	
Lincoln, NE	
<b>UNL School of Computing - CS I Course Leader</b>	August 2020 – November 2020
<ul style="list-style-type: none"><li>Assist Instructors and mentor Learning Assistants</li><li>Do quality checks on grading and evaluate LA performance</li></ul>	
Lincoln, NE	
<b>Lincoln Financial Group - Software Engineering Intern</b>	June 2020 – July 2020
<ul style="list-style-type: none"><li>Replatformed Java application onto Docker container</li><li>Created GitLab pipeline for continuous deployment</li></ul>	
Omaha, NE	

## CLASS EXPERIENCE

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<b>Internet of Things – C++</b>	Fall 2021
<ul style="list-style-type: none"><li>Send data via radio and upload to Azure IoT</li></ul>	
<b>Senior Design – React</b>	Fall 2021
<ul style="list-style-type: none"><li>Designing an oscilloscope app for audio signals for use in Advanced Embedded Systems class</li></ul>	
<b>Advanced Embedded Systems – VHDL</b>	Spring 2021
<ul style="list-style-type: none"><li>Implemented oscilloscope and wave generator on FPGA</li></ul>	
<b>Software Engineering for Robotics – C++</b>	Spring 2021
<b>Operating System Kernels – C</b>	Fall 2020
<b>Data Structures &amp; Algorithms – C++</b>	Spring 2020
<b>Embedded Systems – Arduino, C, C++</b>	Spring 2020
<b>Computer Organization – Assembly, VHDL</b>	Fall 2019

## INVOLVEMENT

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<b>CSE-Student Advisory Board</b>	February 2019 – Present
<b>UNL Honors</b>	August 2018 – Present