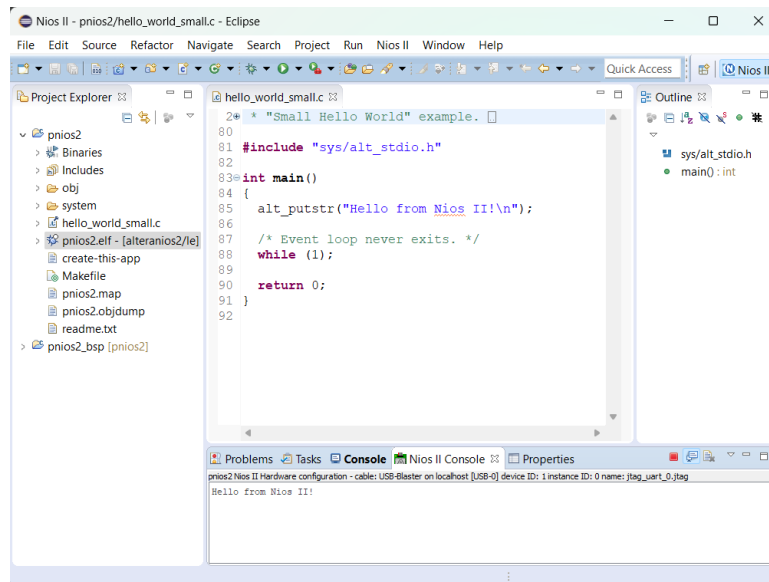


Kelas : 47-03

## Hasil “Hello World” Eclipse



The screenshot displays the RTL Viewer interface for the project 'D:/Apikasi/FPGA/pnios2/pnios2'. The top menu bar includes 'File', 'Edit', 'View', 'Tools', 'Window', and 'Help'. The 'Netlist Navigator' on the left shows the hierarchy: 'pnios2' > 'pnios2:1'. The main area shows a complex logic diagram with multiple green rectangular blocks representing logic components, interconnected by a dense network of black lines representing signals. The diagram is organized into several layers, with a large block at the bottom labeled 'pnios2:1' and several smaller blocks above it, including 'pnios2:1' and 'pnios2:1'.

## Technology Map Viewer (Post-Fitting)

