

```
01 .model small
02 .stack 100h
03 .data
04 .code
05
06
07 main proc
08
09     mov ah, 1
10     int 21h
11     mov bl, al
12
13     mov ah, 1
14     int 21h
15     mov bh, al
16
17     cmp bl, bh
18     jg 12
19     jmp 11
20
21 11:
22     mov ah, 2
23     mov dl, bl
24     int 21h
25
26 12:
27     mov ah, 2
28     mov dl, bh
29     int 21h
30
31 end main
32
33
```



```
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04 .code
05
06
07 main proc
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09     mov ah, 1
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11     mov bl, al
12
13     mov ah, 1
14     int 21h
15     mov bh, al
16
17     cmp bl, bh
```

```
original source co...
12
13     mov ah, 1
14     int 21h
15     mov bh, al
16
17     cmp bl, bh
18     jg 12
19     jmp 11
20
21 11:
22     mov ah, 2
23     mov dl, bl
24     int 21h
25
26 12:
27     mov ah, 2
28     mov dl, bh
29     int 21h
30
```

emulator: lab 2.exe

file math debug view external virtual devices virtual drive help

Load reload step back single step run step delay ms: 0

registers

	H	L
AX	02	32
BX	32	32
CX	01	1E
DX	00	32
CS	0720	
IP	0032	
SS	0710	
SP	0100	
BP	0000	
SI	0000	
DI	0000	
DS	0700	
ES	0700	

0720:0032 0720:0032

Address	Hex	Dec	Symbol
0721E	90	144	E
0721F	90	144	E
07220	90	144	E
07221	90	144	E
07222	90	144	E
07223	90	144	E
07224	90	144	E
07225	90	144	E
07226	90	144	E
07227	90	144	E
07228	90	144	E
07229	90	144	E
0722A	90	144	E
0722B	90	144	E
0722C	90	144	E
0722D	90	144	E
0722E	90	144	E
0722F	90	144	E
07230	90	144	E
07231	90	144	E
07232	F4	244	J
07233	00	000	NULL

screen source reset aux vars debug stack flags

emulator screen (80x25 chars)

2222

clear screen change font 0/16