|  | NORTH SOUTH UNIVERSITY DEPARTMENT OF ELECTRICAL & COMPUTER ENGINEERING **Fall 2021** |
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**EEE 111/ ETE 111 Lab | Analog Electronics I Lab Course Outline**

**Course Faculty:** Mr. Saif Ahmed (SfA) **Lab Instructor:** Tasmina Imam  **Email:** [**tasmina.imam@northsouth.edu**](mailto:tasmina.imam@northsouth.edu)**; Mobile:** +880 1689794008 **Office Room:** TBA; **Office Hours:** TBA  
**Lab Section:**  7; **Lab Room:** TBA   
**Lab Timing:** M 11:20 AM – 2:40 PM

# Lab Outline

| Lab 1 | **Experiment no.1:** I-V Characteristics of Diode |
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| Lab 2 | **Experiment no.2:** Diode Rectifier Circuits |
| Lab 3 | **Experiment no.3:** Clipper And Clamper Circuits |
| Lab 4 | **Exam & Experiment no.4:** Zener Diode Applications |
| Lab 5 | **Experiment no.5:** The Output Characteristics Of CE (Common Emitter) Configuration Of BJT |
| Lab 6 | **Experiment no.6:** The BJT Biasing Circuit |
| Lab 7 | **Experiment no.7:** Study of Switching Characteristics of MOSFET |

# Marks Distribution (Tentative)

| **Attendance** | 5 % |
| --- | --- |
| **Lab Performance** | 10% |
| **Lab Report (Average)** | 20% |
| **Class Assignment (7)** | 10% |
| **Quiz** | 20% |
| **Simulation Test** | 20 (or 10%) |
| **Demonstration and VIVA** | 15% |
| **Final Assessment (on Campus)** | 10% |

# Tentative Course Schedule

| Date | Topics | Dues | Remarks |
| --- | --- | --- | --- |
| 18/10/2021 | Introductory Class & Group formation | NA |  |
| 25/10/2021 | **Experiment no.1:** I-V Characteristics of Diode |  |  |
| 01/11/2021 | **Experiment no.2:** Diode Rectifier Circuits | Report 01 |  |
| 08/11/2021 | **Experiment no.3:** Clipper And Clamper Circuits | Report 02 |  |
| 15/11/2021 | **Exam & Experiment no.4:** Zener Diode Applications | Report 03 |  |
| 22/11/2021 | **Experiment no.5:** The Output Characteristics Of CE (Common Emitter) Configuration Of BJT | Report 04 | **Quiz 01**  **on Exp(1-4)** |
| 29/11/2021 | **Experiment no.6:** The BJT Biasing Circuit | Report 05 |  |
| 06/12/2021 | **Experiment no.7:** Study of Switching Characteristics of MOSFET | Report 06 |  |
| 13/12/2021 | **Quiz\_02 & Practice Class** | Report 07 | **Quiz(5-7)** |
| 20/12/2021 | **Final Setup Test (Simulation Test) & VIVA** | Report 08 |  |
| 27/12/2021 |  |  |  |
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# RULES:

* Do not talk during the class. If you are inattentive and miss your attendance in the process, you will be marked as absent.
* If any lab is missed, one will get 0% in *Attendance, Lab performance* and *Lab report submission* for that particular lab class.
* Use of mobile phones during the class is strictly prohibited.
* Lab Report copied from others will be marked negatively.
* You must bring printed lab manuals to the class.
* No Late Submissions will be accepted whatever may be the reason.
* Only use the cover page as instructed in the class, using cover page of any other form will lead to rejection of your submission.
* All information in the cover page must be typed; handwritten cover pages will be rejected.
* All submissions are due at the beginning of the class.
* Ensure that your group prints all necessary materials well ahead of the class.
* Grading will be done according to NSU Grading Policy.
* No makeup exam will be taken.

Important Links:

Multisim: <https://drive.google.com/drive/folders/16p1PezOeQMUspxGTN99lYkeavlqU5X1L?authuser=0>

Installation Procedure: <https://www.youtube.com/watch?v=0w14_s-WCH4>

Live Account: <https://www.multisim.com/>

Group\_Info: <https://docs.google.com/spreadsheets/d/19azMpMKL6YlifCDgek_foxON9MKee88MozqQ5vuFo4A/edit#gid=0>