

# Lattice FPGA Project Weekly Progress Report - Week 15

Report date: 2025/04/17

- **Last Week (time in hours)**

- Team Review
  -
- Nathaniel Fraly
  - Troubleshoot button module (17)
- Riley Cox
  - Troubleshoot button module (17)
- Mohammad Alshaiji
  - Troubleshooting Radiant port (7)
- Xiang Li
  - Find out battery power supply options for Upduino board (4.5)
- Haoyang Han
  - Start designing PCB (4)
  - Complete the final 3D printed version (3)

- **Next week**

- Team Plan
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- Nathaniel Fraly
  - Finish up button module integration
- Riley Cox
  - Finish button module integration
- Mohammad Alshaiji
  - Finish porting core to Radiant
- Xiang Li
  - Order the power components
  - Help with solving the incompatibility of icicle on Radiant
- Haoyang Han
  - Continue to design PCB

- **Blocked**

- Team Blocks
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- Nathaniel Fraly
  - No screen; Rahul meeting on Monday to resolve issue
- Riley Cox
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- Mohammad Alshaiji
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- Xiang Li
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- Haoyang Han
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