

Lattice FPGA Project Weekly Progress Report - Week 21

Report date: 2025/05/30

- **Last Week (time in hours)**

- Team Review
 - Soldering the final product
 - Radiant Port progress
 - Draft final report
- Nathaniel Fraly
 - Finished soldering and populating PCB (12)
- Riley Cox
 - Finished soldering headers on Upduino, created debounce in the button module (12)
- Mohammad Alshaiji
 - Continued work on Radiant Port (8)
- Xiang Li
 - Finish up the draft of final report and send out for review (6)
- Haoyang Han
 - Improve the 3D model and print out the enclosure(6)

- **Next week**

- Team Plan
 - Finish the poster
- Nathaniel Fraly
 - Helped with poster
- Riley Cox
 - Helped with poster
- Mohammad Alshaiji
 - Continue final attempt at Radiant Port
 - Aid in poster Design
- Xiang Li
 - Design the poster
- Haoyang Han
 - Helped with poster

- **Blocked**

- Team Blocks
 - N/A
- Nathaniel Fraly
 - N/A
- Riley Cox
 - N/A
- Mohammad Alshaiji
 - N/A
- Xiang Li
 - N/A
- Haoyang Han
 - N/A