Lattice FPGA Project Weekly Progress Report - Week 22

Report date: 2025/06/06

- **Last Week (time in hours)**
  - Team Review
    - Have the poster finished
  - Nathaniel Fraly
    - Helped with poster (2)
  - Riley Cox
    - Help with poster design (2)
  - Mohammad Alshaiji
    - Help with Poster (3)
  - Xiang Li
    - Finish the design of poster (6)
  - Haoyang Han
    - Helped with poster (2)
- **Next week**
  - Team Plan
    - Finish up documentation
  - Nathaniel Fraly
    - Finish up documentation
  - Riley Cox
    - Finish up documentation
  - Mohammad Alshaiji
    - Finish documentation
  - Xiang Li
    - Revise the poster based on feedbacks, finish up final report
  - Haoyang Han
    - Finish documentation
- **Blocked**
  - Team Blocks
    - N/A
  - Nathaniel Fraly
    - N/A
  - Riley Cox
    - N/A
  - Mohammad Alshaiji
    - N/A
  - Xiang Li
    - N/A
  - Haoyang Han
    - N/A