

Lattice FPGA Project Weekly Progress Report - Week 11

Report date: 2025/03/21

- **Last Week (time in hours)**

- Team Review
 - FINALS, nobody worked on anything
- Nathaniel Fraly
 - FINALS, nobody worked on anything
- Riley Cox
 - FINALS, nobody worked on anything
- Mohammad Alshaiji
 - FINALS, nobody worked on anything
- Xiang Li
 - FINALS, nobody worked on anything
- Haoyang Han
 - FINALS, nobody worked on anything

- **Next week**

- Team Plan
 - Begin integration work
- Nathaniel Fraly
 - Integrate PWM/button modules with Radiant model
- Riley Cox
 - Assist with integration of SPI module
- Mohammad Alshaiji
 - Integrate SPI module
- Xiang Li
 - Start docs/assist with PWM/button integration
- Haoyang Han
 - Continue 3D model work

- **Blocked**

- Team Blocks
 - None
- Nathaniel Fraly
 - N/A
- Riley Cox
 - N/A
- Mohammad Alshaiji
 - N/A
- Xiang Li
 - N/A
- Haoyang Han
 - N/A