

# Lattice FPGA Project Weekly Progress Report - Week 14

Report date: 2025/04/10

- **Last Week (time in hours)**

- Team Review
  -
- Nathaniel Fraly
  - Attempting to integrate button module (4)
  - Rework button module for simpler integration (2)
  - Test simpler button module to find out it isn't any simpler (3)
- Riley Cox
  - Integrating button module (4)
  - Troubleshoot button module changes via testbench and on board (4)
- Mohammad Alshaiji
  - Integrated SPI Module (1)
  - Created unified testing program for Screen testing (1)
  - Prep for program integration with Button module (1)
- Xiang Li
  - New C functions on text display on screen (6)
- Haoyang Han
  - Complete 3D Printing (8)

- **Next week**

- Team Plan
  -
- Nathaniel Fraly
  - Get buttons working so phase 1 is complete
  - Get started on Radiant troubleshooting again
- Riley Cox
  - Finish integrating button module
- Mohammad Alshaiji
  - Integrate Button module with Screen Program
  - Help Xiang with text display
- Xiang Li
  - Debug and make sure the text display functions work
- Haoyang Han
  - Start PCB Design

- **Blocked**

- Team Blocks
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- Nathaniel Fraly
  - No screen, can't test actual output
- Riley Cox
  - Button team only has one screen making testing slow
- Mohammad Alshaiji
  - Waiting for Button module to be ready
- Xiang Li
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- Haoyang Han

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