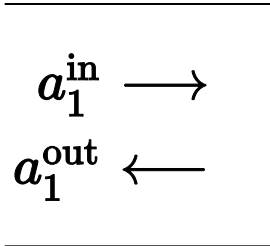
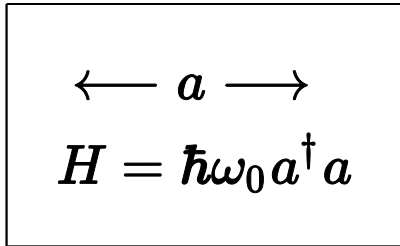


Port 1



κ_1

Cavity



κ_2

Port 2

