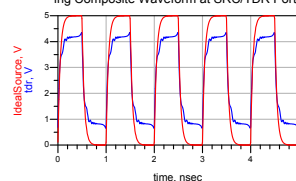


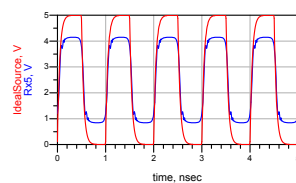
You must make sure this value matches the setting on the LinearStepResp simulation component.

Ideal Transmitted Pulse Waveform and Resulting Composite Waveform at SRC/TDR Port

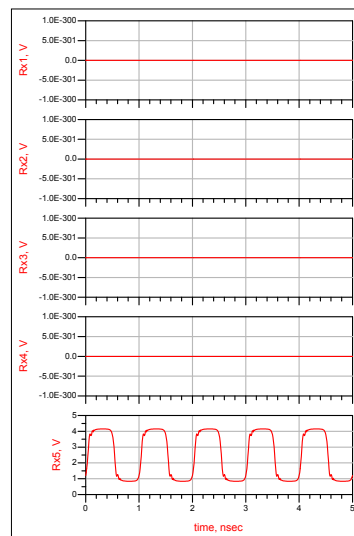


If the input impedance of the device or circuit under test were identical to the source impedance, these two waveforms would be identical.

Ideal Transmitted Pulse Waveform and Resulting Waveform at Receive Port



Ideally, these two waveforms should be identical except for a time delay.



```
Eqn start_time1=start[0]
Eqn stop_time1=stop[0]
Eqn points1=1+(stop_time-start_time)/step_time
Eqn IdealSource1=ts(Source.start_time.stop_time_points,"Hamming")
Eqn tdr1=ts(TDR.start_time.stop_time_points,"Hamming")
Eqn Rx6=ts(Rcv1.start_time.stop_time_points,"Hamming")
Eqn Rx8=ts(Rcv2.start_time.stop_time_points,"Hamming")
Eqn Rx9=ts(Rcv3.start_time.stop_time_points,"Hamming")
Eqn Rx10=ts(Rcv4.start_time.stop_time_points,"Hamming")
Eqn Rx7=ts(Rcv5.start_time.stop_time_points,"Hamming")
```