

B. JASHWANTH RISHI

jashwanthrishib@gmail.com

+91-9848413281

Bengaluru, INDIA

Summary

I am a dedicated, highly motivated student with strong analytical, problem-solving, communication, and interpersonal skills, demonstrated through active student organization involvement, successful projects, and internships.

Education

Gandhi Institute of Technology and Management

B.Tech, Electronics and Communications Engineering, CGPA- 9.81

Bengaluru, Karnataka

2021 – 2025

Narayana Junior College

M.P.C, Higher Secondary Education, Marks 957/1000

Anantapur, Andhra Pradesh

2019 – 2021

Experience

14 Si Solutions

Intern

May 2024 – June 2024

Bengaluru, Karnataka

- Intern in a startup specialized in contamination control in the semiconductor industry.
- Worked in Clean Room operations and procedures.
- Engaged in IoT and Image processing projects.
- Contributed to sensor development for various applications.

MURTI Labs-GITAM

Research Intern

May 2024 – Present

Bengaluru, Karnataka

- Conducting research in the NANO-SENSOR INNOVATION LAB in nanotechnology.
- Working on a final year project in VLSI nanotechnology.

AICTE

Intern (Virtual)

March 2023 – September 2023

- Completed a virtual internship focused on MATLAB training across various domains.
- Gained knowledge in Image and Signal Processing, Machine Learning, and Deep Learning.

Projects

REEV E-KART

Sub System Captain

July 2023 – October 2023

- Led the E-Power train subsystem design and analysis operations.
- Secured 5th position among 19 colleges in the competition.

DRONE Project

Team Member

July 2022 – August 2022

- Developed a surveillance drone model for monitoring purposes.

WUMPUS World Bot

Team Member

March 2023 – September 2023

- Developed a path-following robot for the WUMPUS World environment.

Verilog design projects

Team Member

April 2024 – present

- Simulation experiments using xilinx vivado EDA tool.

Workshops

CMOS IC Design (CADENCE)

02/2024

Ms. Neha Maheshwari, IIT-Indore

- Circuit design and analysis in CMOS IC Design.

Computing in RAM (SRAM)

02/2024

prof. Satosh kumar vishvakarma — Memory cells and RAM

- Explored computing on RAM designed from biological cells.

T-CAD Workshop on MOSFET Design

02/2024

Dr. Sankalp Singh — MOSFET designing and analysis

- MOSFET design and analysis using T-CAD Sentaurus from Synopsys.

Certifications

India Semiconductor Workforce Development Program - IISC Bangalore (06/2024)

Embedded Mobility Development - HCL-TECH (08/2023 – 09/2023)

VLSI for Beginners - NIELIT-CALICUT (05/2024)

Introduction to Electronics - COURSERA (10/2022 – 11/2022)

Languages

C

Verilog

VHDL

Python

Achievements

Smart GITAM Hackathon - Second place among 32 teams - (03/2023)

REEV Virtuals 2023-24 - SAEIBS - 6th place in the SAE-REEV Virtual competitions (10/2023 – 11/2023)