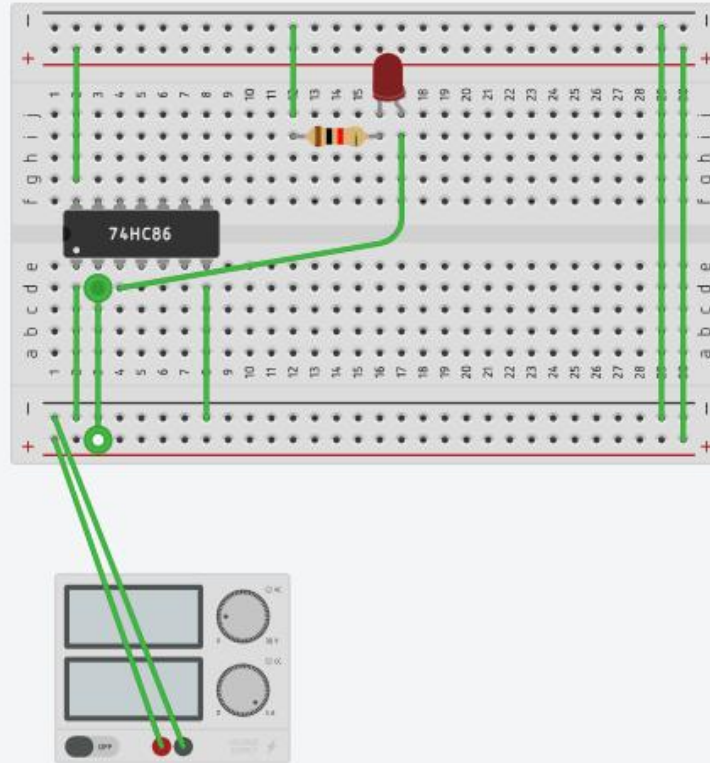
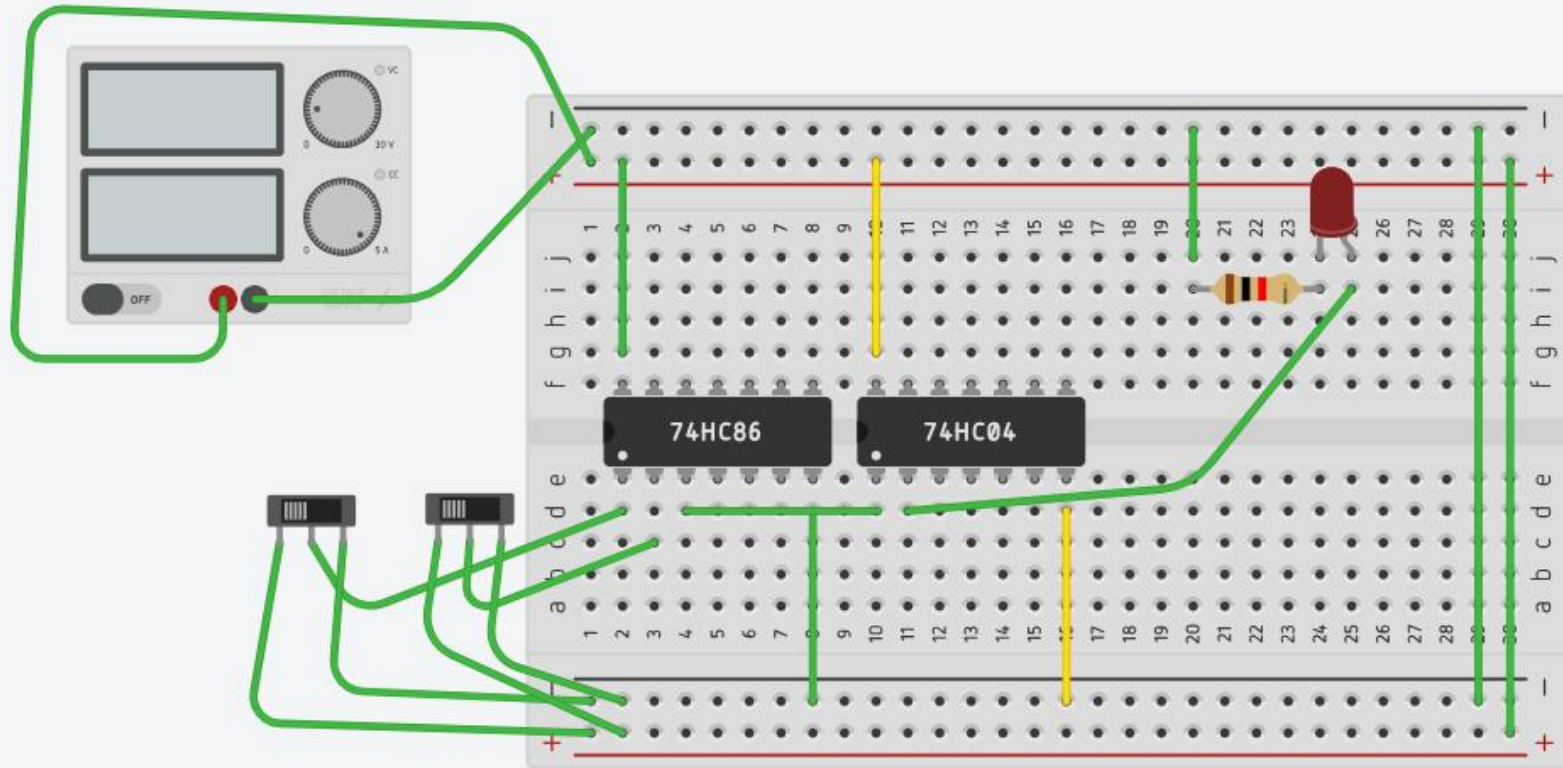


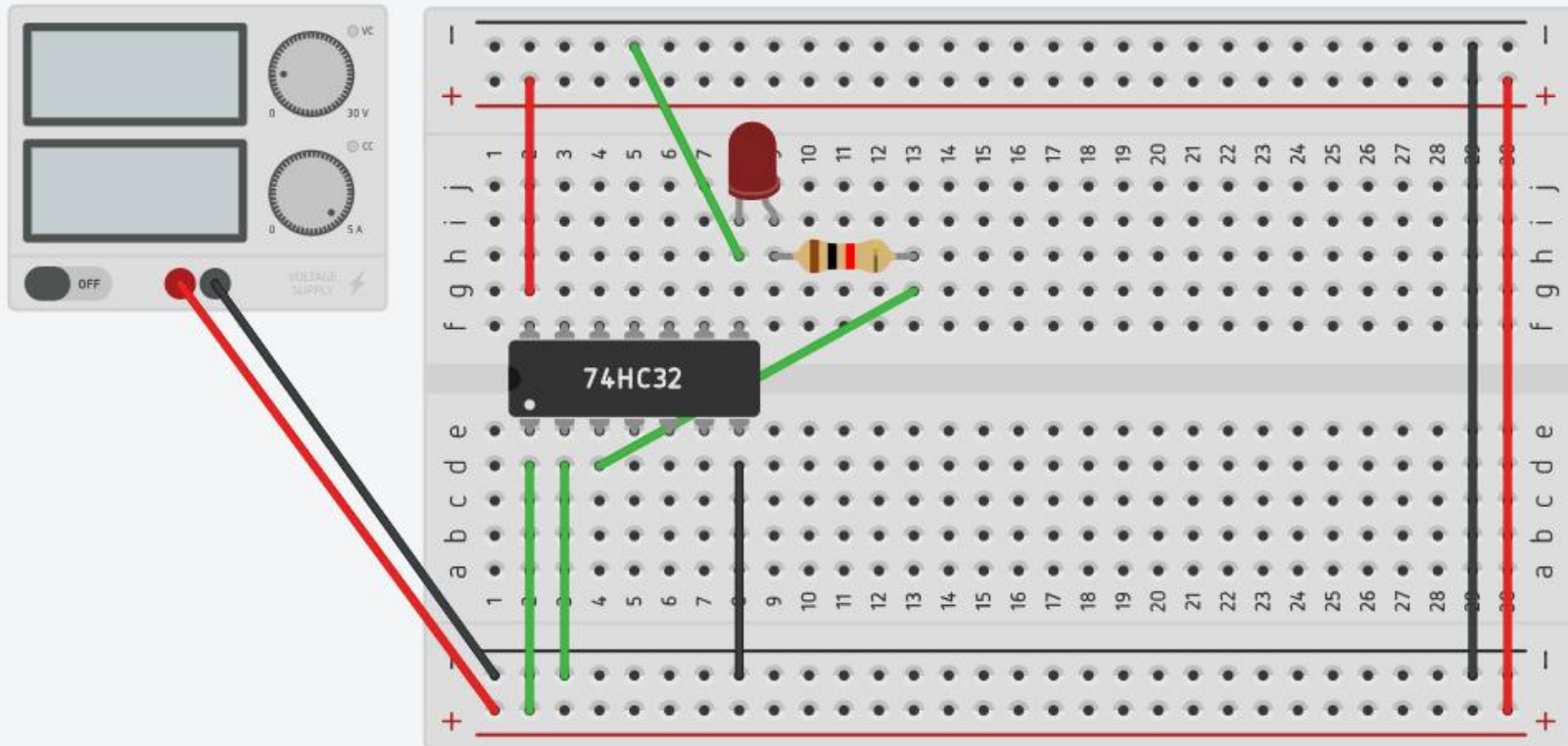
XOR GATE



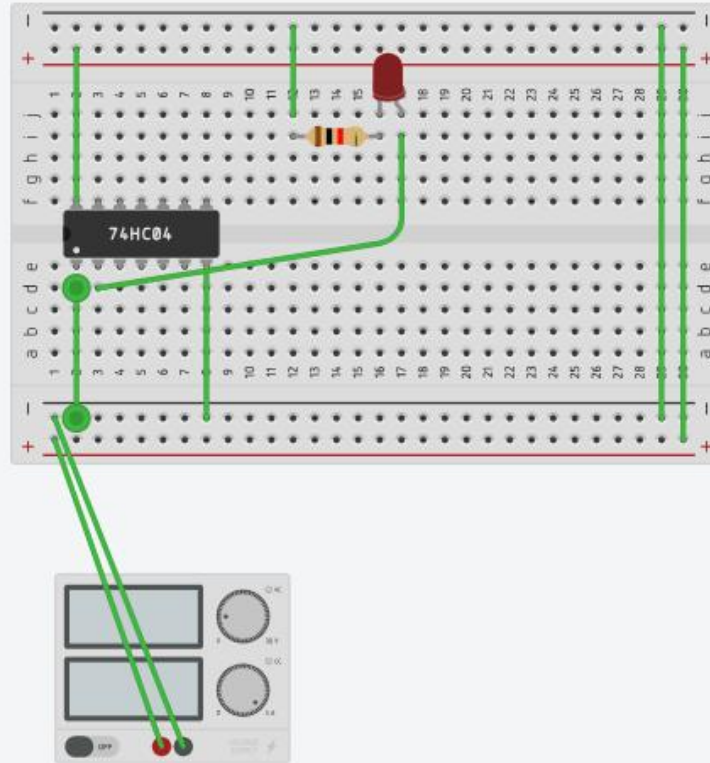
XNOR Gates



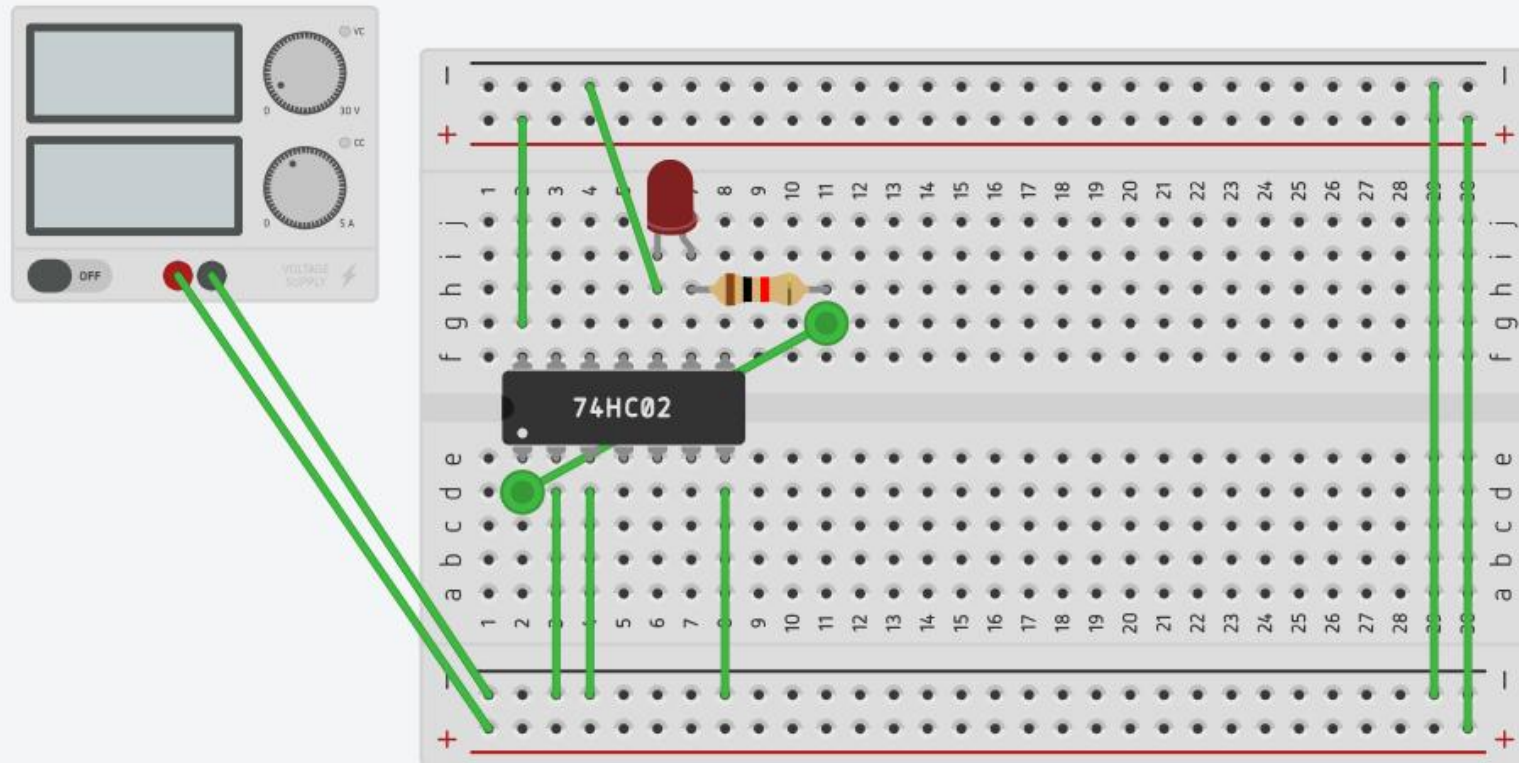
OR Gates



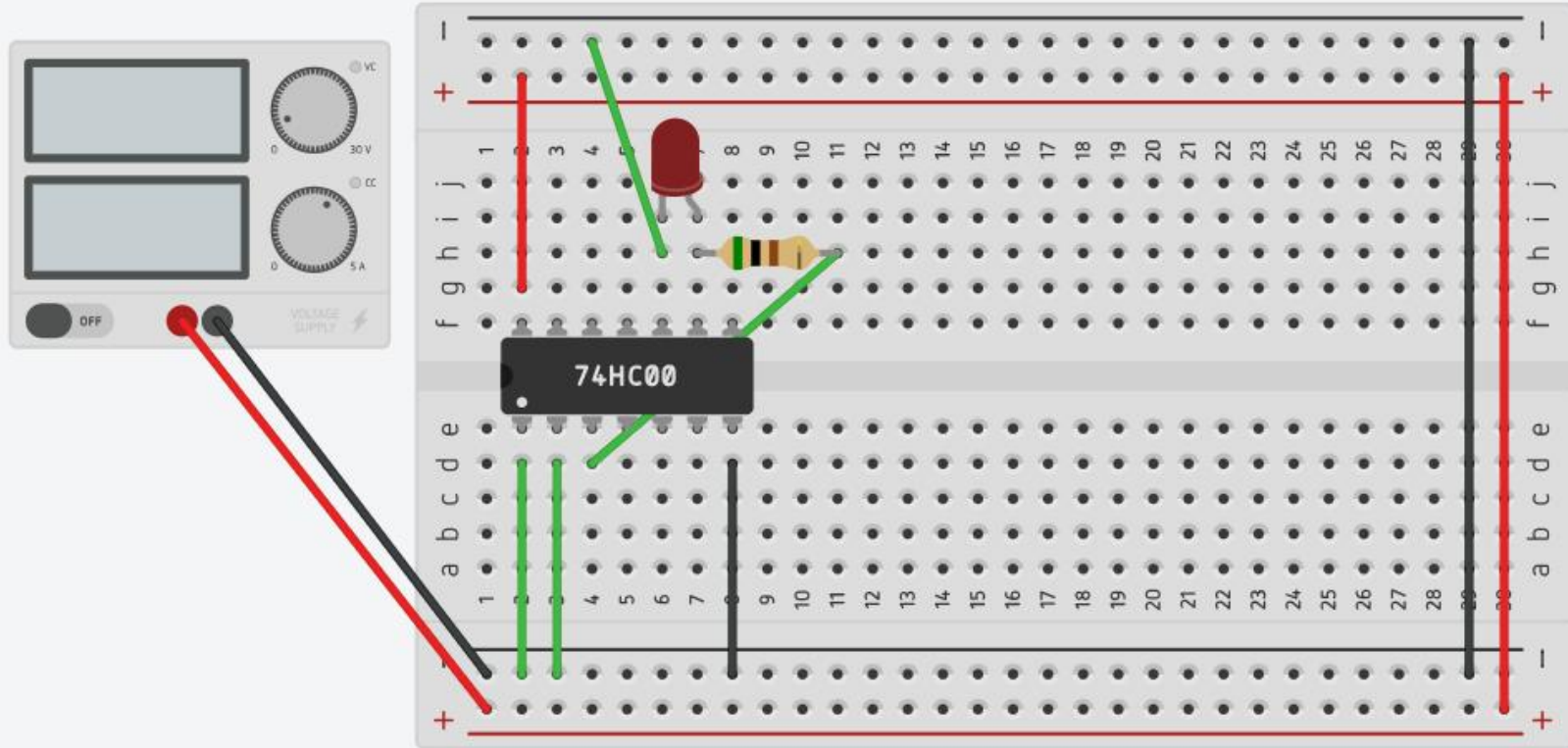
NOT Gate



NOR Gate



NAND Gate



AND Gate

