Individual Project

- Design a configurable 32 bit Floating Point Arithmetic Unit supporting the following:
  - Single Precision FP Addition/Subtraction
  - Single Precision FP Multiplication/Division
- You need to follow IEEE 754-2008 standard. The newest standard is IEEE 754-2019. For SP FP. 754-2008 should be sufficient.
- Your design should be evaluated based on performance (how fast the calculations are done) and resource efficiency (how much of logic resources have been used).
- *You cannot use any libraries – design from scratch.*

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