WORK EXPERIENCE AND SURVEY

Edelweiss Securities Limited, *Mumbai* *[May '18 - July '18]*

Institutional Equities - Trading Technology Team

- Designed UX and UI for easy access to Transaction Cost Analysis(TCA) report to help traders get actionable insights to enhance and synchronize trading related execution quality, compliance and management
- Implemented login authorization, dynamic forms to query single day and multiple day TCA reports based on date, account ID, portfolio and instrument with download link to summary file on Django framework
- Created infrastructure for logging errors, warnings and regular django server information
- Reviewed and reengineered the code base for plotting transaction execution graphs using python
- Introduced features like embedding the volume traded, hover for more details and colour schemes for different algorithms to help traders compare their performance with the market more efficiently

PV Module Field survey, *Leh* *[June '19]*

NCPRE, IIT Mumbai

- Collaborated with 2 others in PV module survey of 7 days to inspect plant installations and performance degradation
- Surveyed 88 modules at 3 sites in Laddakh region and carried out module and string level I-V characterization, IR thermography to detect hotspots and visual imaging to capture cracks on the cells
- Calculated average performance degradation rate per year to be 1.42%, 3.32% and 3.97% using MATLAB

Teaching Assitant *[June '19]*

Instructor: Prof. Narendra Shiradkar, EE dept. IIT Mumbai

- Developed an online portal for 40+ students to access personalized random failure data of devices and predict the nature of failure
- Generated artificial random data for normal, weibull, lognormal and exponential distributions with varying parameters

RESEARCH AND COURSE PROJECTS

Degradation rate determination of solar plant — *M.Tech Thesis* *[June '19]*

Guide: Prof. Narendra Shiradkar, EE dept. IIT Mumbai

- Aimimg to use data science to perform predicitve analysis of reliability of solar modules
- Creating models for solar cells to determine the dependency of parameters on degradation rate
- Implemented single diode model with five parameters for solar cell on python
- Processed module datasheet values to extract the 5 parameters and solved ideal diode equation.
- Implemented five parameters single diode model for solar cell by extracting the parameters from module datasheet and implicitly solving ideal diode equation using python
- Used Bokeh server to plot the I-V curve interactively with parameters sliders for all five parameters
- Keywords to add : predictive analytics, data science, pv performance, modelling and simulation, reliability,

Solar module mounting orientation and axis tracking effect *[Mar '19 - May '19]*

Course : Design and evaluation of PV power plants

- Determined the best possible orientation of solar panel for maximum power output in different regions.
- Performed parametric analysis on System Advisor Model software by varying tilt and azimuthal angle in northern, southern and equatorial regions for summers and winters.
- Concluded that optimal tilt angle is latitude angle and optimal azimuth is 180 in north and 0 in south

- Observed for equatorial regions optimal azimuth angle changed seasonally and axis tracking was effective

Image compression using wavelet transform algorithm *[Mar '19 - May '19]*
Course : VLSI Design Lab

- Implemented image compression using 4-taps, 2-D Daubechies wavelet transform and Huffaman encoding
- Synthesized the code on Quartus for Cyclone Altera FPGA using Nios II processor and SDRAM module
- Applied low pass and high pass filters (implemented using LUTs), followed by downsampling on 512 x 512 grayscale image to obtain LL, LH, HL and HH image components

Power Amplifier design *[Mar '19 - May '19]*
Course : Solid State Microwave devices

- Simulated a 2 stage power amplifier with matching & bias-T circuits with unilateral design approach in ADS
- Designed, fabricated & testsd the PCB using Vector Network Analyzer for gain and bandwidth specifications

Modelling gesture control *[Oct '18 Nov '19]*
Course : Sensors in Instrumentation

- Modelled 3-D Gesture Control using ADXL345 Digital Accelerometer interfaced with Arduino board
- Estimated inclination angle of board w.r.t. three axes with an error of less than 5% and plotted it in real time

IITB-RISC Microprocessor design *[Oct '17 Nov '17]*
Course : Microprocessors

- Designed a 16-bit microprocessor with 8 registers having multi-cycle point to point communication infrastructure
- Synthesized VHDL code by integrating the controller-FSM and data path on FPGA

Portable Solar cum Vibration Energy Harvesting Mobile Charger *[Oct '18 Nov '18]*
Course : Electronic Design Lab

- Prototyped and tested working model of solar cum vibration charger with optimized size and performance
- Designed a suitable AC-DC converter and a DC-DC Boost converter for vibration and solar circuit output

Maze Solver *[May '16 Jul '16]*
Summer School of Code, WnCC IIT Mumbai

- A command line Image Processing Project on Python platform assisted by OpenCV library
- Used thresholding, filters, contour extraction, and thinning (one pixel width) to get a path from start to end.

POSITIONS OF RESPONSIBILITY

Campaigning Coordinator *[2016]*
Abhuydaya, Social Body IIT Mumbai

- Led volunteer weekends at various schools for the underprivileged with a unified motive of circulating general awareness, computer basics and career counselling, with a team of 20-22 members
- Headed a 23 member volunteer team to HUMARA BACHPAN (National level initiative for children enforcement) in Bhajiwali slums, with a purpose of realizing the harsh situations of kids
- Co-ordinated and volunteered ANTARCHAKSHU, St. Xaviers XRCVCs initiative to demand from the government and people equal accessibility to science education for visually challenged people

TECHNICAL SKILLS

- Programming Languages : Python, C++, VHDL
- Tools : C++, MATLAB, VHDL, deign tools : Cadence Virtuoso, Quartus

RELEVANT COURSES UNDERTAKEN

- **Solar and Reliability** : Design and evluation of PV Plants, Reliability and Failure Analysis of Electronic Devices
- **Analog VLSI** : CMOS Analog VLSI Design, Mixed Signal VLSI Design, RF Microelectronics Chip Design
- **Others** : Data Structure and Algorithms, Probability and Random Processes, Intro to Quantum Mechanics

EXTRA CURRICULAR ACTIVITIES

- Bestowed with a Black belt (1st Dan) in Shotokan style Karate after regular training of 4 years
- Member of Gold medal recieving squad in Badminton General Championship among 12 hostels 2018
- Awarded silver medal in the Street Play competition, Freshiezza (Freshmen cultural competitions)
- Pursuing 50 hours official German language course provided by International Relation Cell, IIT Bombay