# **WORK EXPERIENCE AND SURVEY**

#### **Edelweiss Securities Limited**

May 2018 - July 2018

- Worked on Transaction Cost Analysis (TCA) report design that helped traders get actionable insights to enhance and synchronize trading related execution quality, compliance and management.
- o Designed UX and UI for inside traders to access TCA report easily for them and their clients
- Django framework, login, singleTCA, multipleday TCA, downloadable link to summary as a csv file, date, account, portfolio, instrument
- o Constructed 2 kinds of logging, one for Django related requests and other for data issues
- o Studied and reengineered the code base for plotting daily market-price vs time curve on plotly
- o Added features to the transaction execution plots like embedding the volume traded, hover for instant details and much more to help traders compare their performance with the market

### Leh Solar Power Plant Survey

*June* 2019

- o Examine the Leh Ladakh conditions for solar growth for further scope in solar setup in Leh
- o Survey of 4 solar plants of 12kWp units consisting of I-V sweeps, IR imaging of cells
- o Calculated the degradation rate to be ......

### **Teaching Assistant of PG course**

Jul 2019 to Nov 2019

- o Generated artificial random data for various distributions with varying parameters
- Develop online portal for students to access personalized random failure data of devices and predict the nature of failure

# **PROJECTS**

# Degradation rate of solar power plants | Dual Degree Project

July 2019 - Present

To create models for solar cells and find the dependency of parameters on the degradation rate

Work done till date:

- o Implemented single diode model with five parameters for solar cell on python
- o Processed module datasheet values to extract the 5 parameters and then solved diode equation.
- o Used Bokeh server to plot the I-V curve interactively with varying parameters sliders.

### Solar module mounting orientation and axis tracking effect

*March* 2019– *April* 2019

- o Determined the best possible orientation of solar panel for maximum power output in different regions.
- o Performed parametric analysis on System Advisor Model software by varying tilt and azimuthal angle.
- o Conclusions after analysis in 3 different regions (northern, southern and equatorial) each in 2 seasons (summers and winters):
- o Tilt angle should be equal to the latitude angle
- o Solar panel should be facing south in northern hemisphere and vice versa but for equatorial region the azimuthal angle for maximum output changes with summer and winter season

Image compression using wavelet transform algorithm

*March* 2019 – *April* 2019

- o Implemented image compression algorithm using 4-taps, 2-D Daubechies Wavelet Transform on 512 x 512 grayscale image and reconstructed the image using Inverse Daubechies Wavelet Transform
- Implemented whole system on Cyclone IV-E Altera FPGA using Nios II processor in platform designer interfaced with SDRAM module on-board which is capable of storing input and output image data of large sizes
- Applied low pass and high pass filtering followed by downsampling by 2 on rows and columns sequentially to obtain LL, LH, HL and HH image components
- o Implemented thresholding on image and performed Huffman encoding to obtain compressed image data which is decoded and then reconstructed back

#### Power Amplifier design

*March* 2019 – *April* 2019

- o Simulated in ADS a 2 stage power amplifier with matching & bias T circuits with unilateral design approach
- o Designed, fabricated & testsd the PCB using Vector Network Analyzer for gain and bandwidth specifications

#### Modelling gesture control

*March* 2019 – *April* 2019

- o Modelled 3-D Gesture Control using ADXL345 Digital Accelerometer interfaced with Arduino board
- o Estimated inclination angle of the three axes with an error of less than 5% and plotted the same in real time

### IITB-RISC Microprocessor design

*March* 2019 – *April* 2019

- o Designed a 16-bit system with 8 registers having multi-cycle point to point communication infrastructure
- o Synthesized VHDL code integrating the controller-FSM and data path for FPGA demonstration

# Portable Solar cum Vibration Energy Harvesting Mobile Charger

*March* 2019 – *April* 2019

- o Designed a suitable AC-DC converter and a DC-DC Boost converter for vibration and solar circuit output
- Prototyped and tested working model of the charger with optimized size and performance

### AREAS OF INTEREST

o Solar System Design, Finance, Reliability of devices, Data Structure and Algorithm

# **Extra curricular Activities**

- o Gold, GC Badminton
- Flute class
- German Class
- o Black belt 1st Dan Shotokan Karate