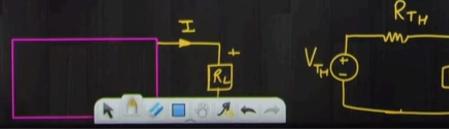
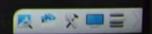
Therenin's Theorem

Any linear, bilateral two terminal network consisting of sources and resistors (Impedance) can be replaced by an equivalent circuit consisting of voltage source in series with resistance.

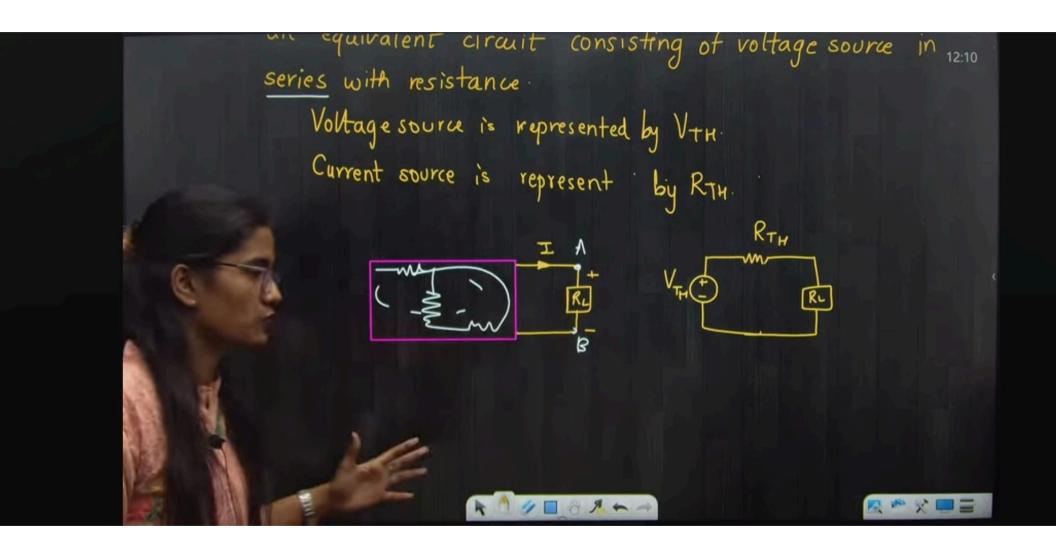
Voltage source is represented by VTH.

Current source is represent by RTH.



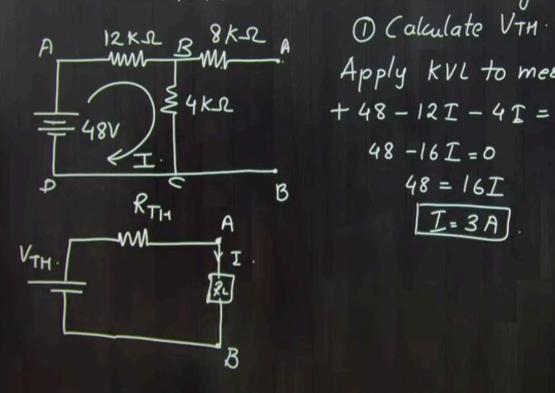








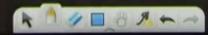
Calculate VTH, RTH and load current through AB

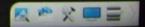


Apply KVL to mesh ABCDA.

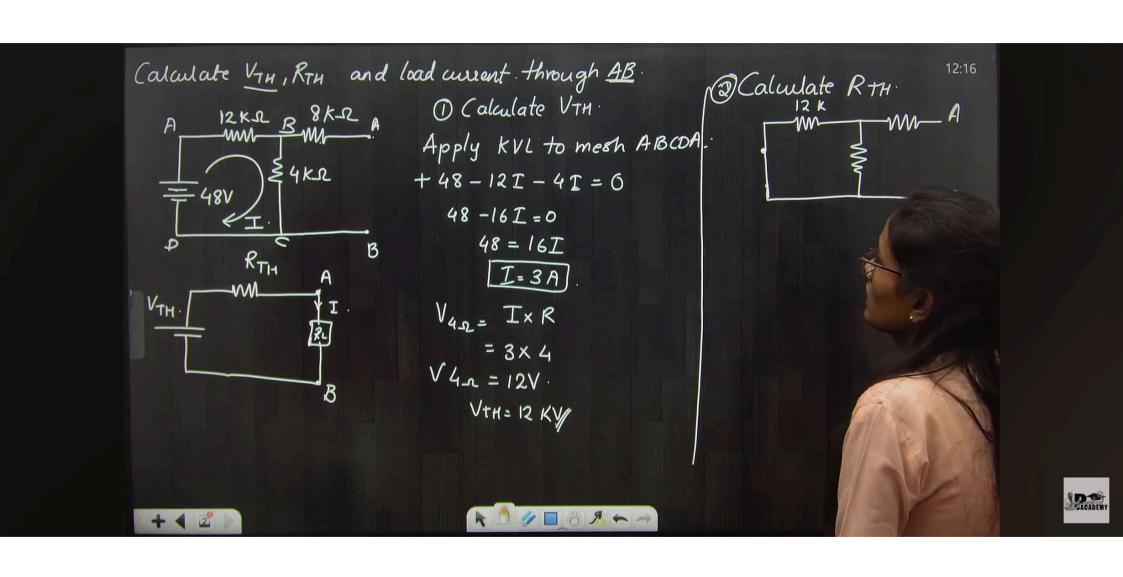
$$+48 - 12I - 4I = 0$$

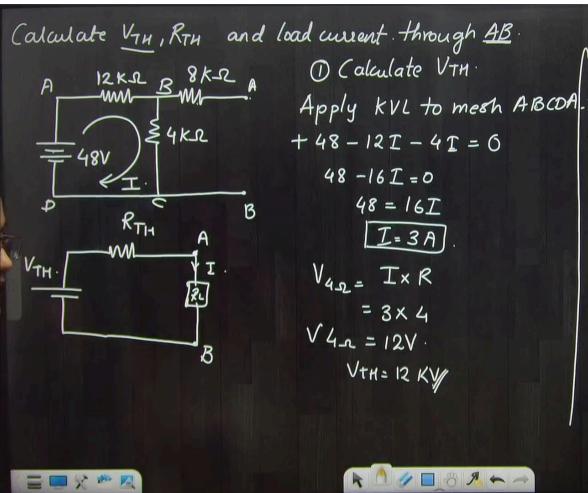
$$48 = 16I$$

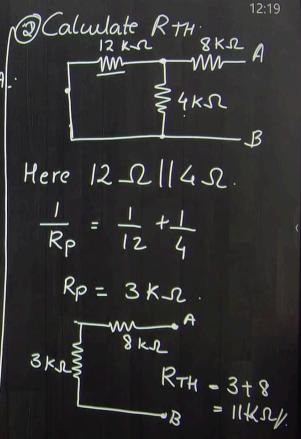




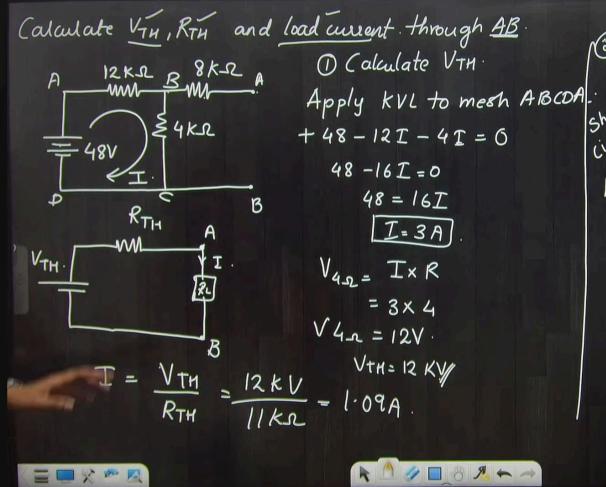


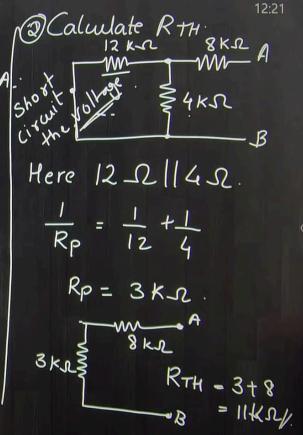






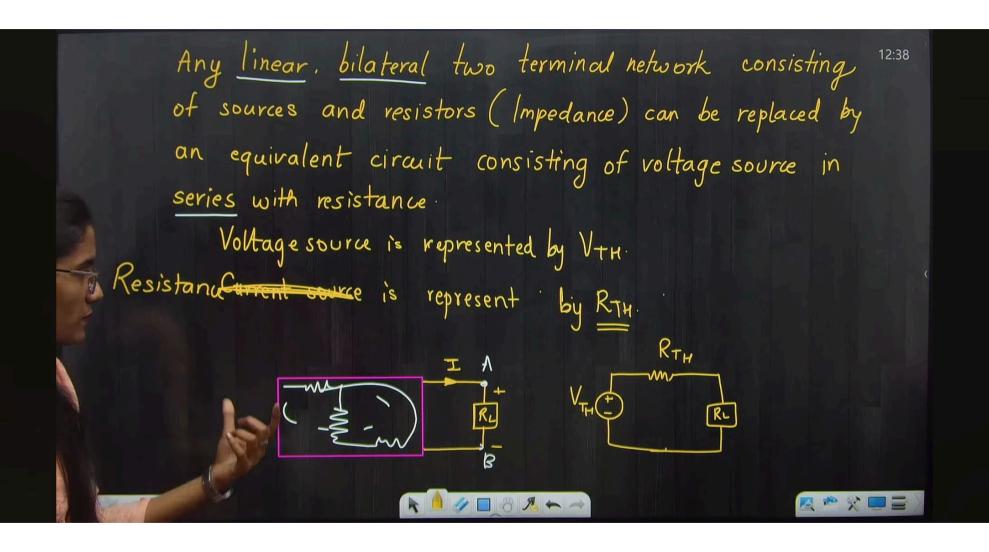








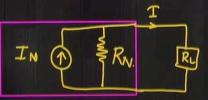


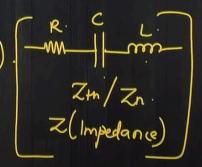


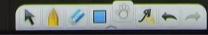
Norton's theorem.

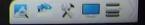
Any linear, bilateral two terminal network consisting of Sources & resistors (Impedance) can be replaced by an equivalent cinuit consisting current source in parallel with a resistance

> Current source is represented by IN



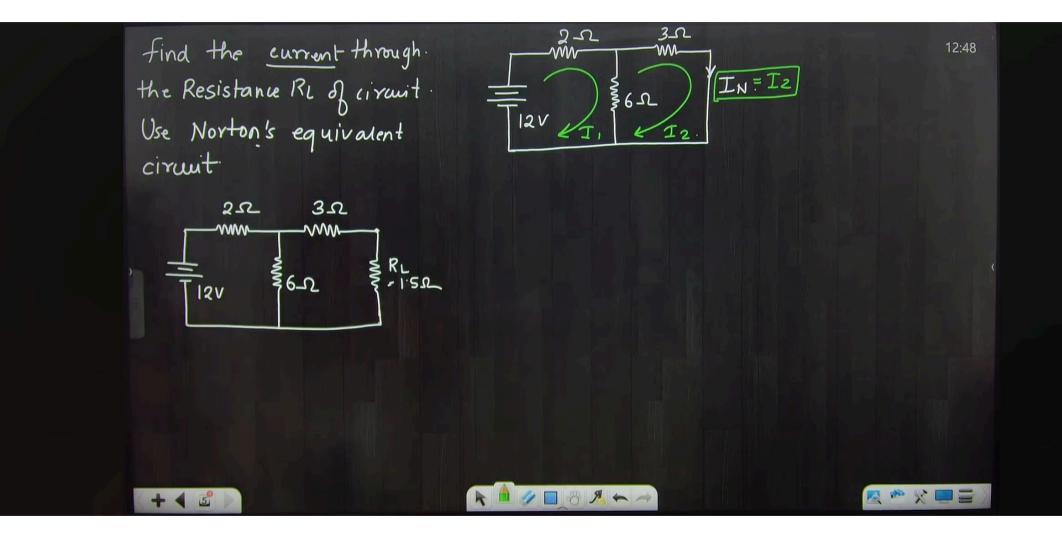




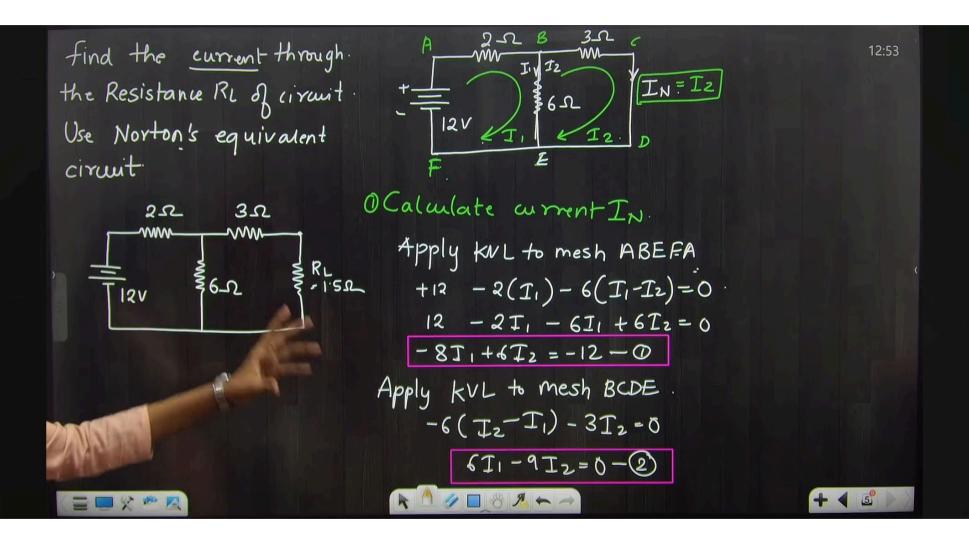




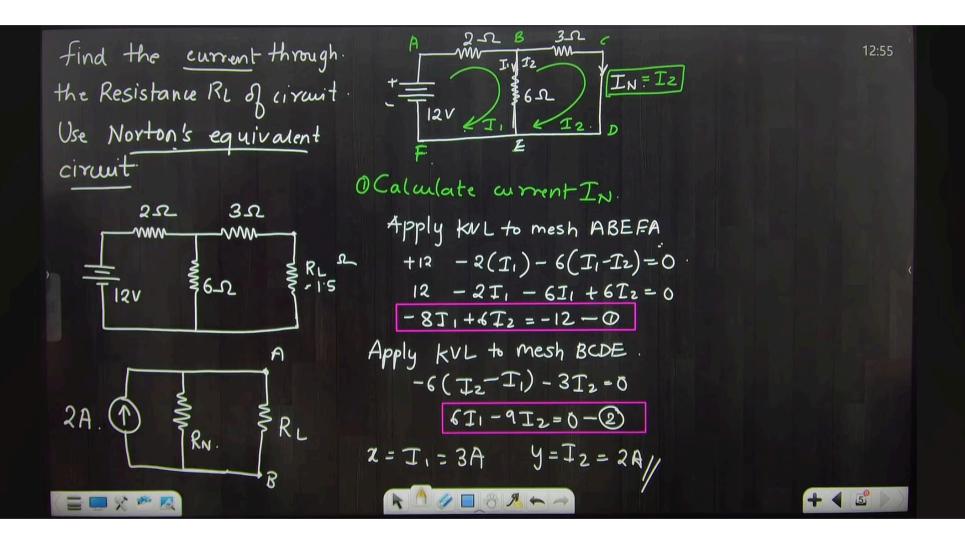




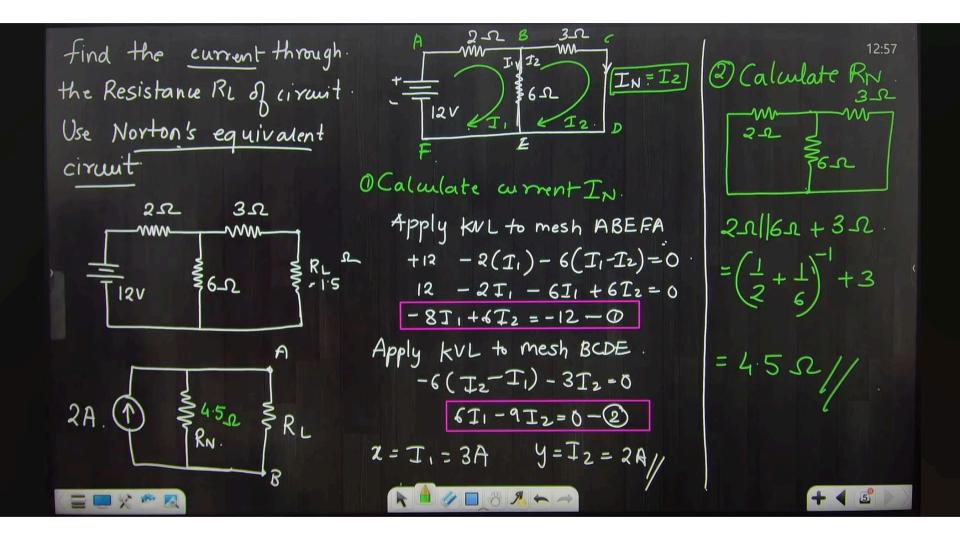
S PACADEMY

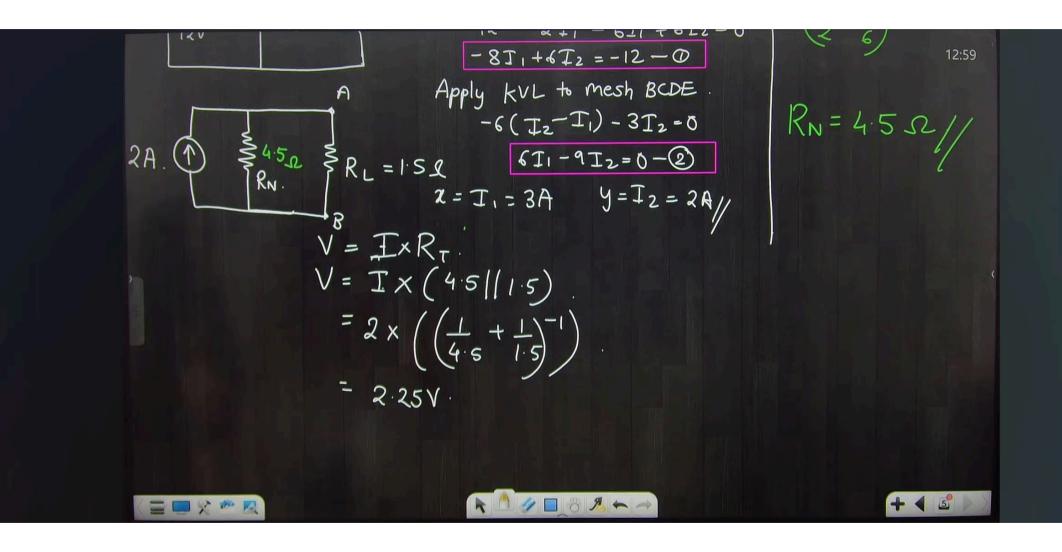


ACADEMY

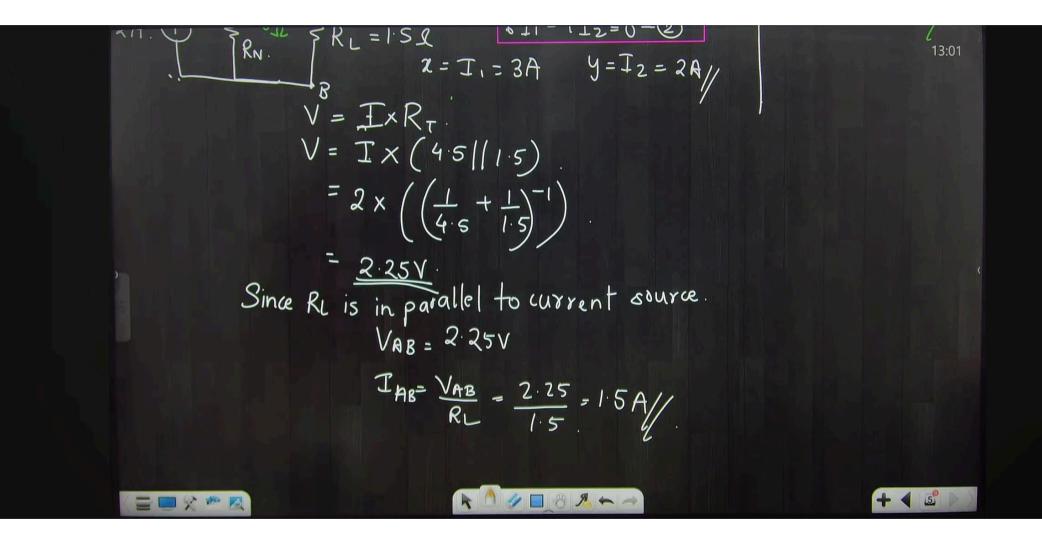


ACADEMY









ACADEMY